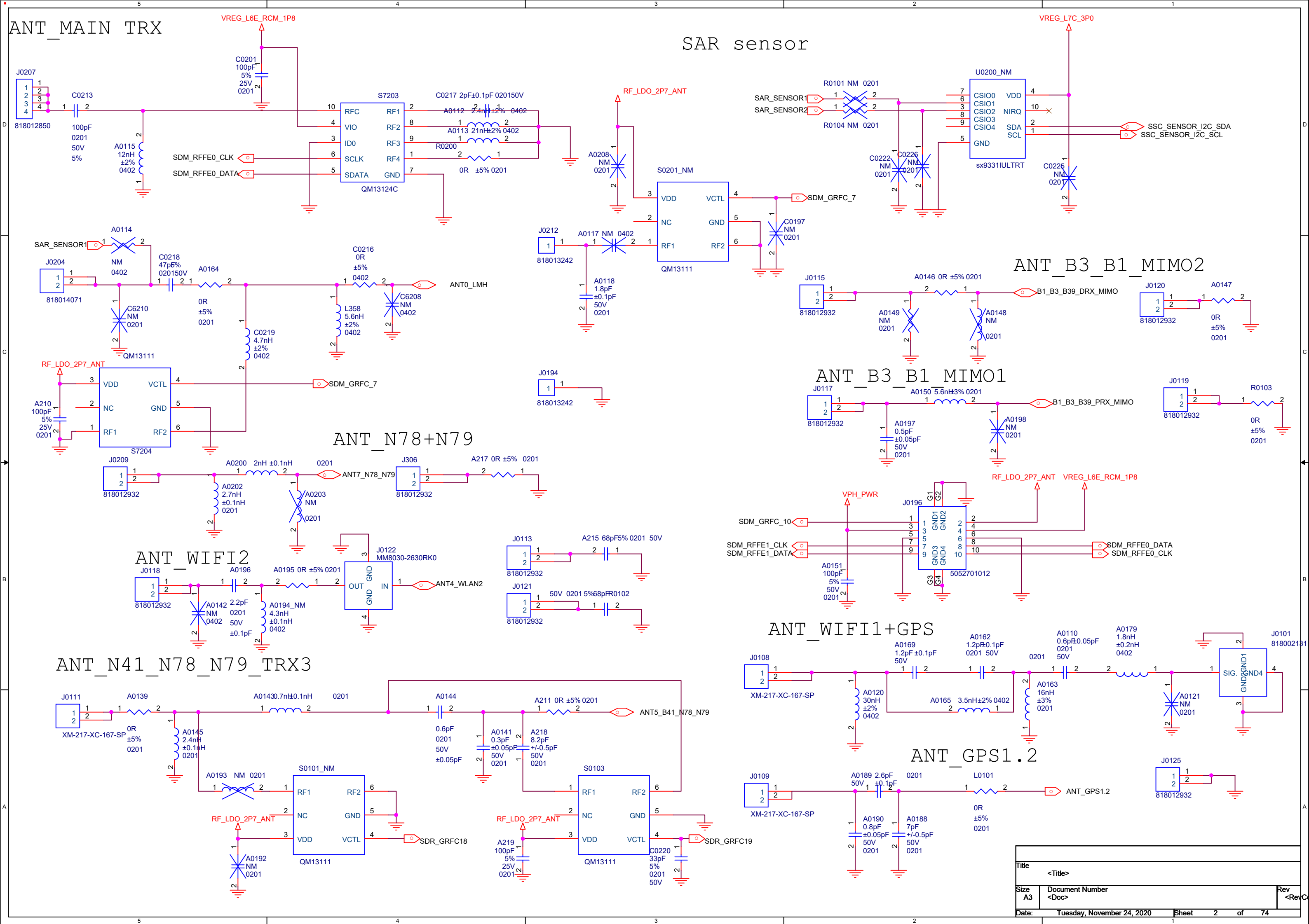
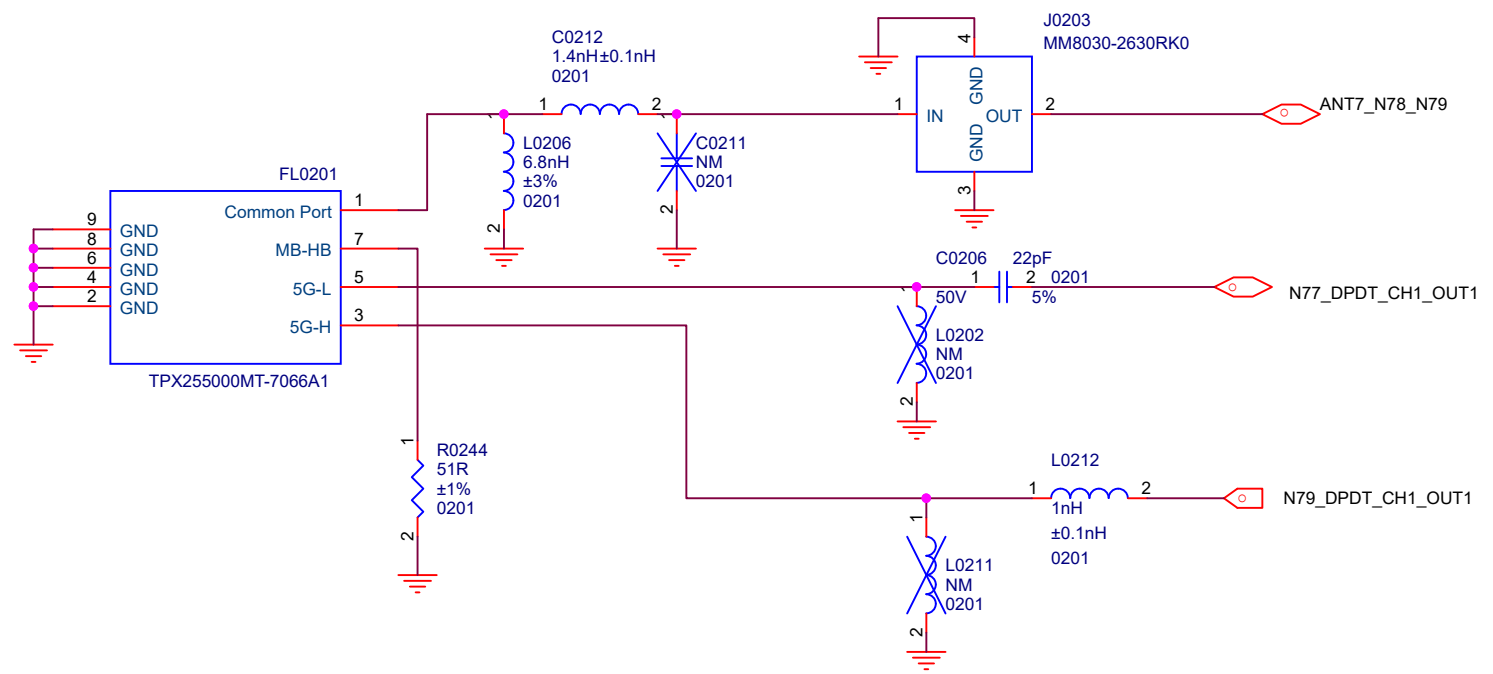
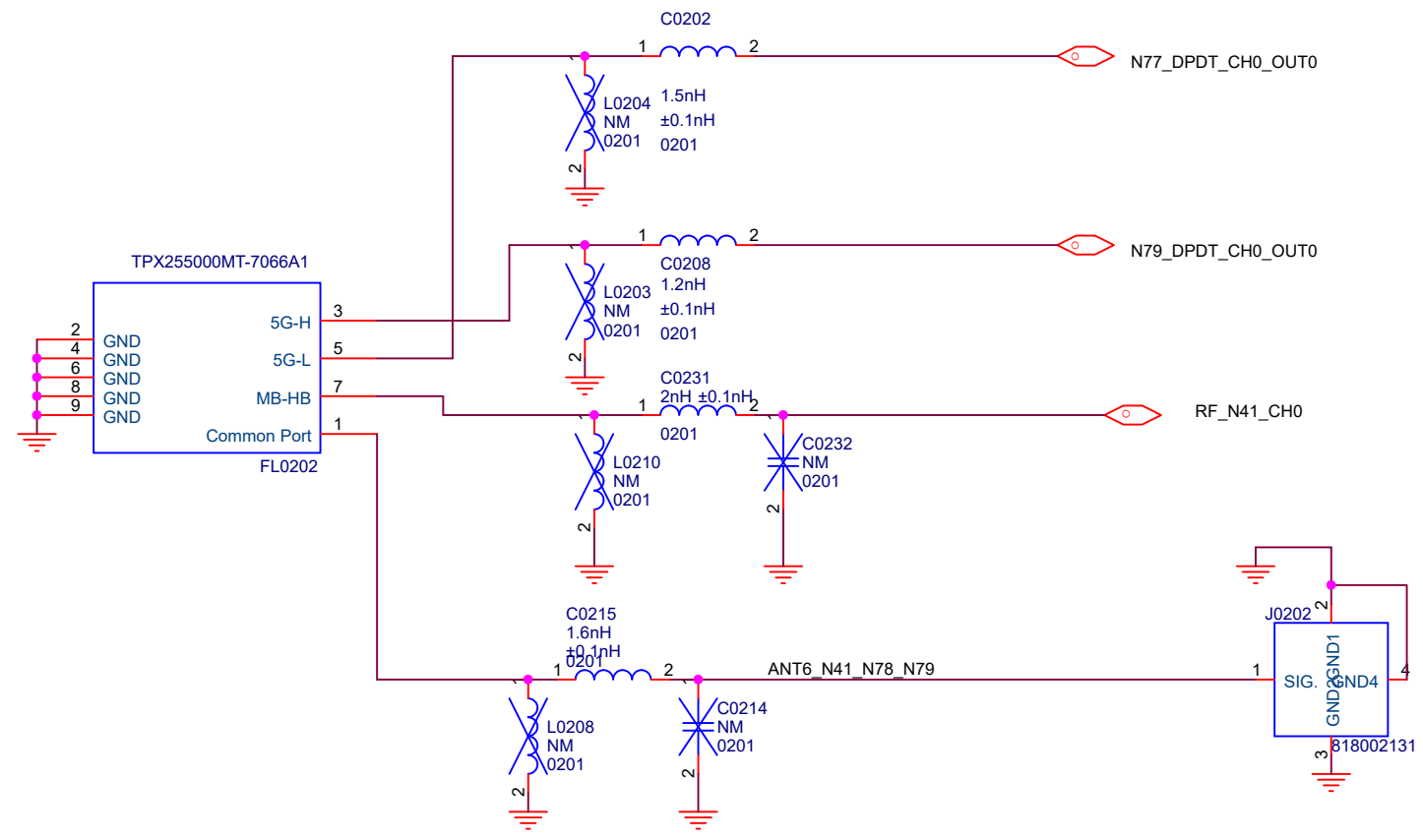
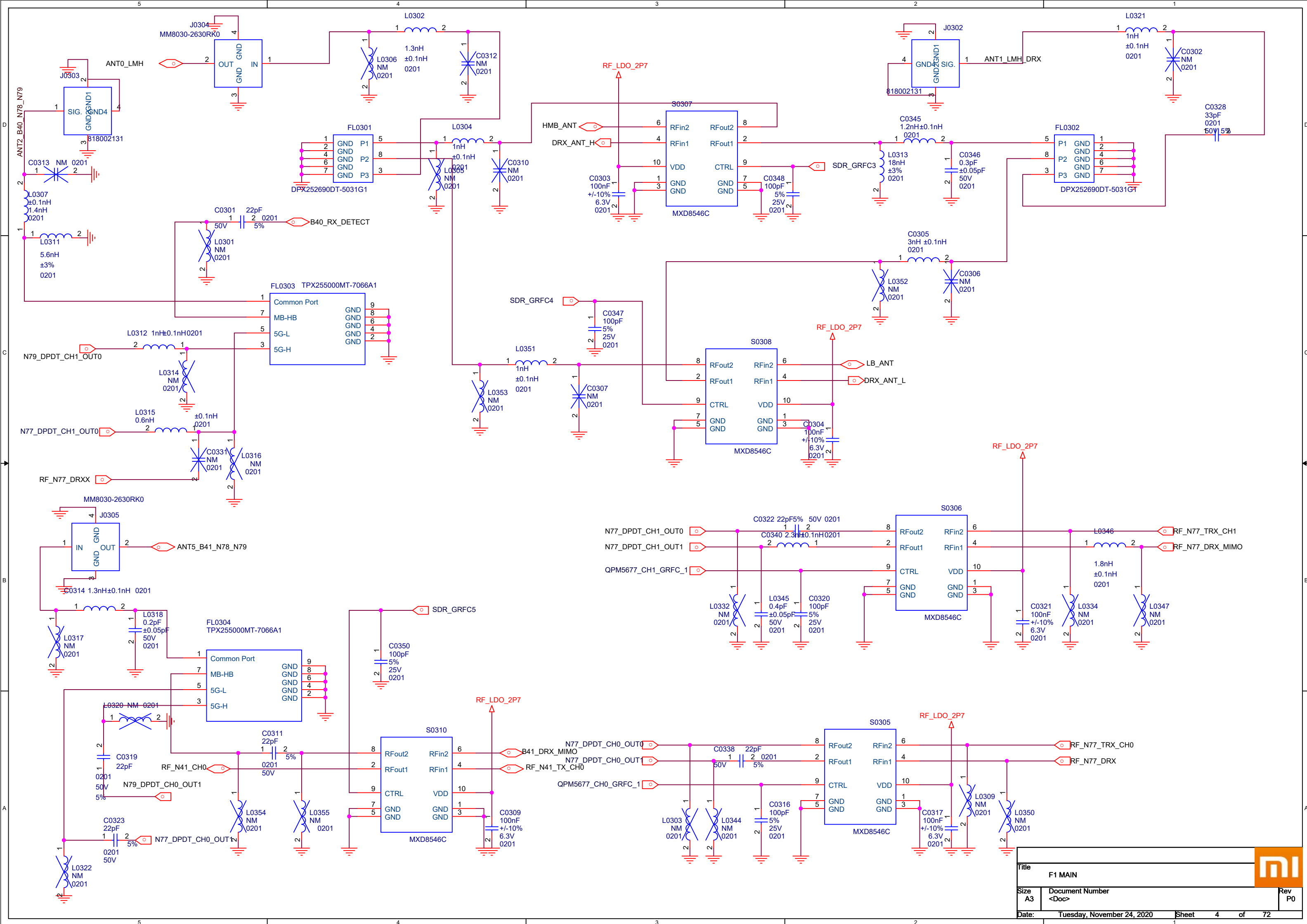


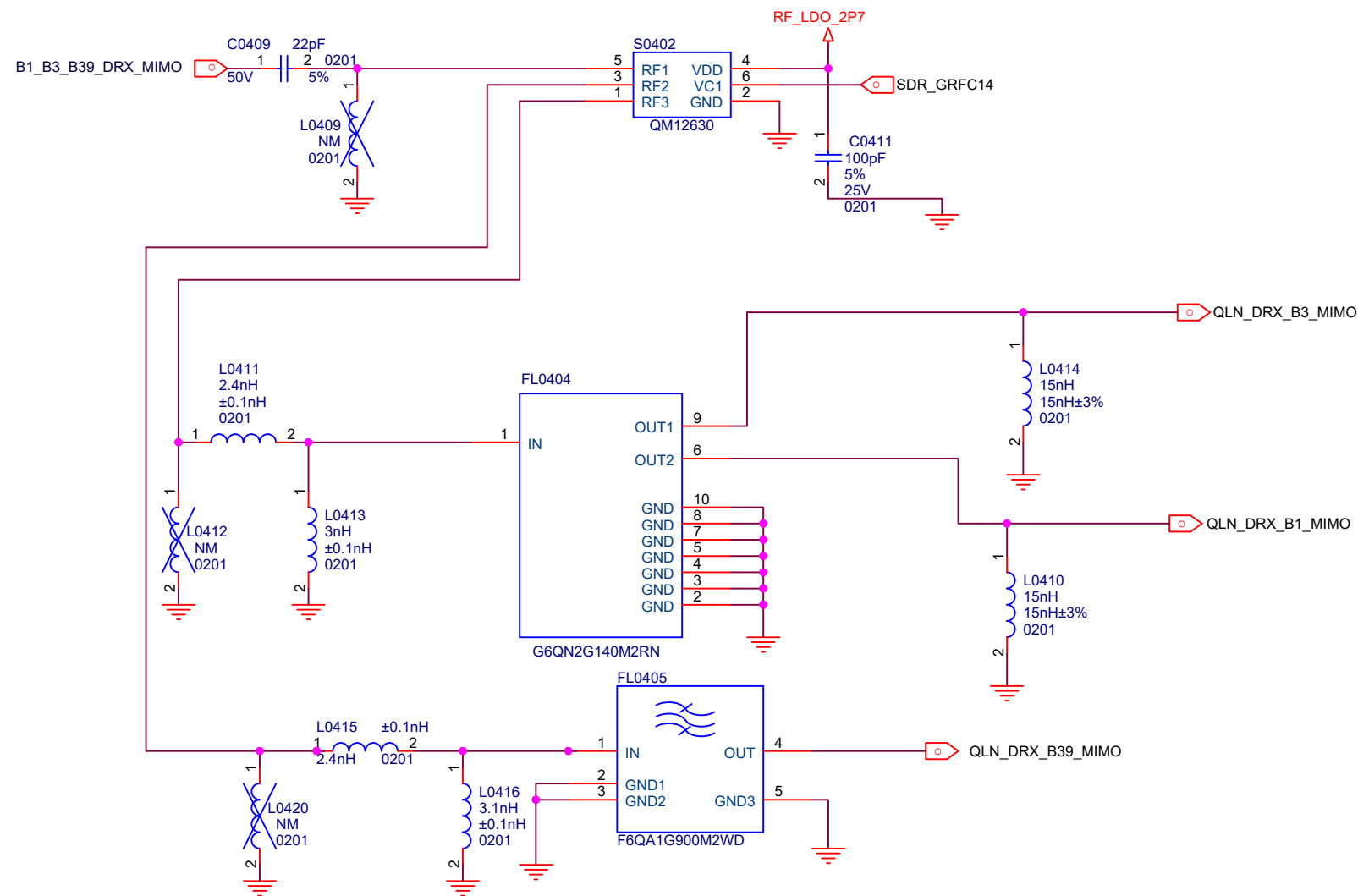
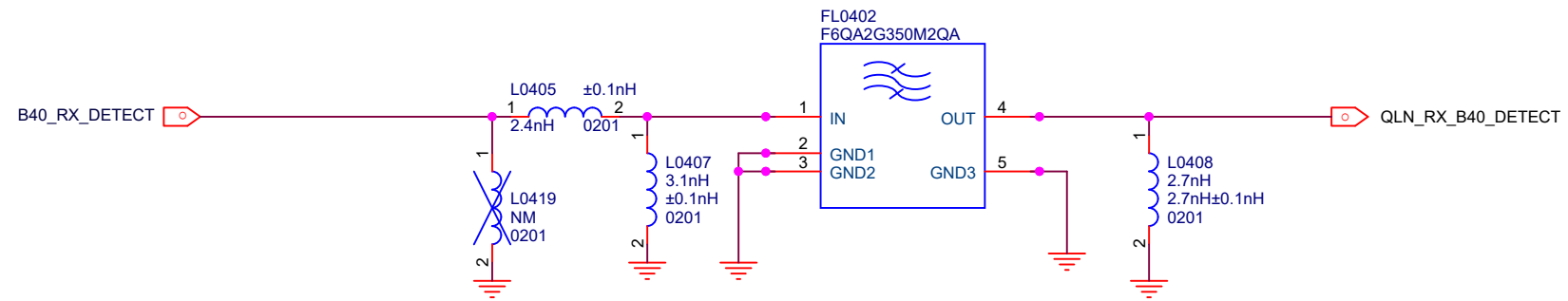
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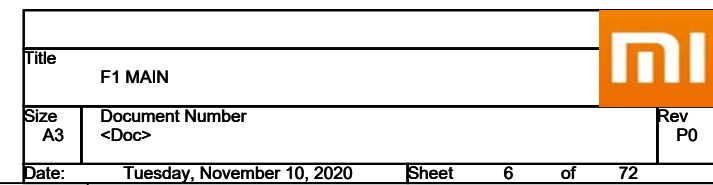
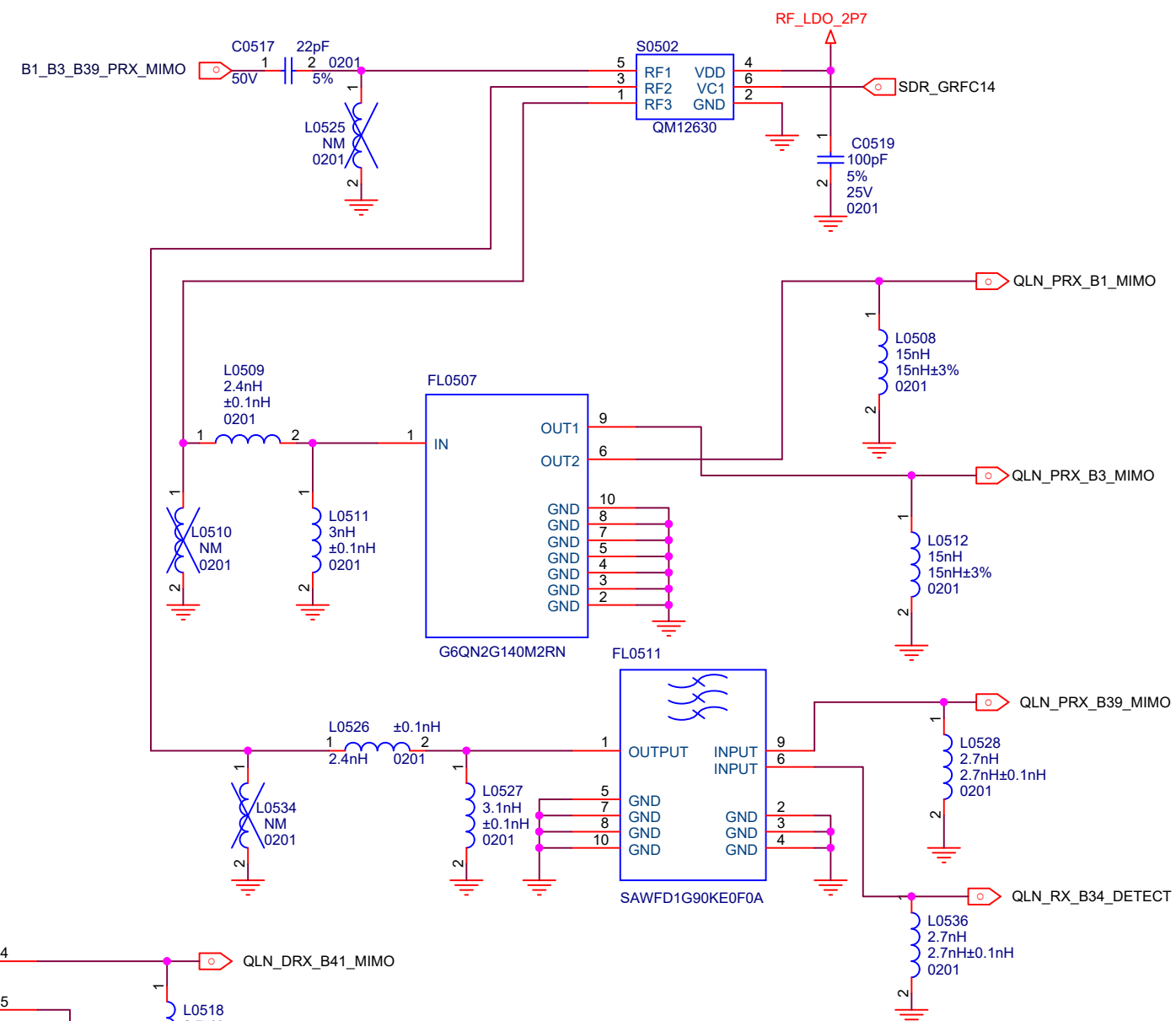
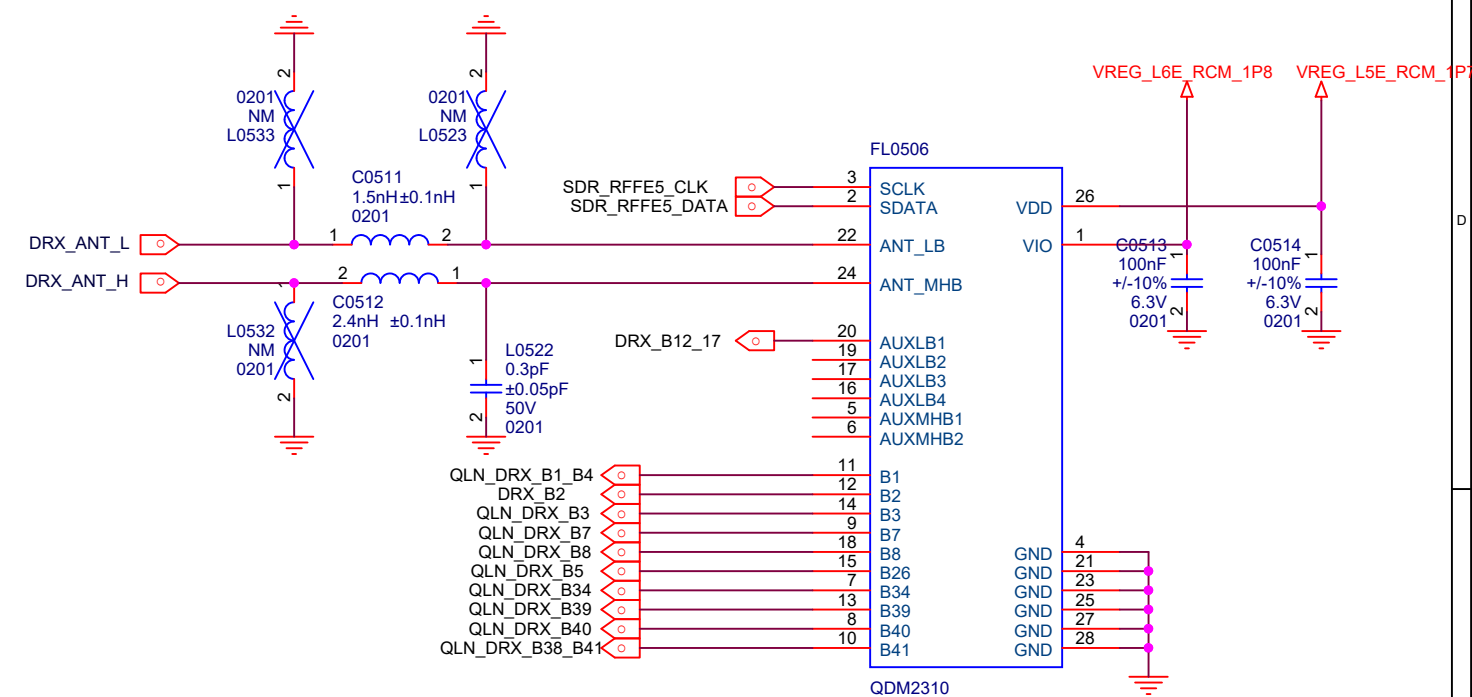
SAR sensor



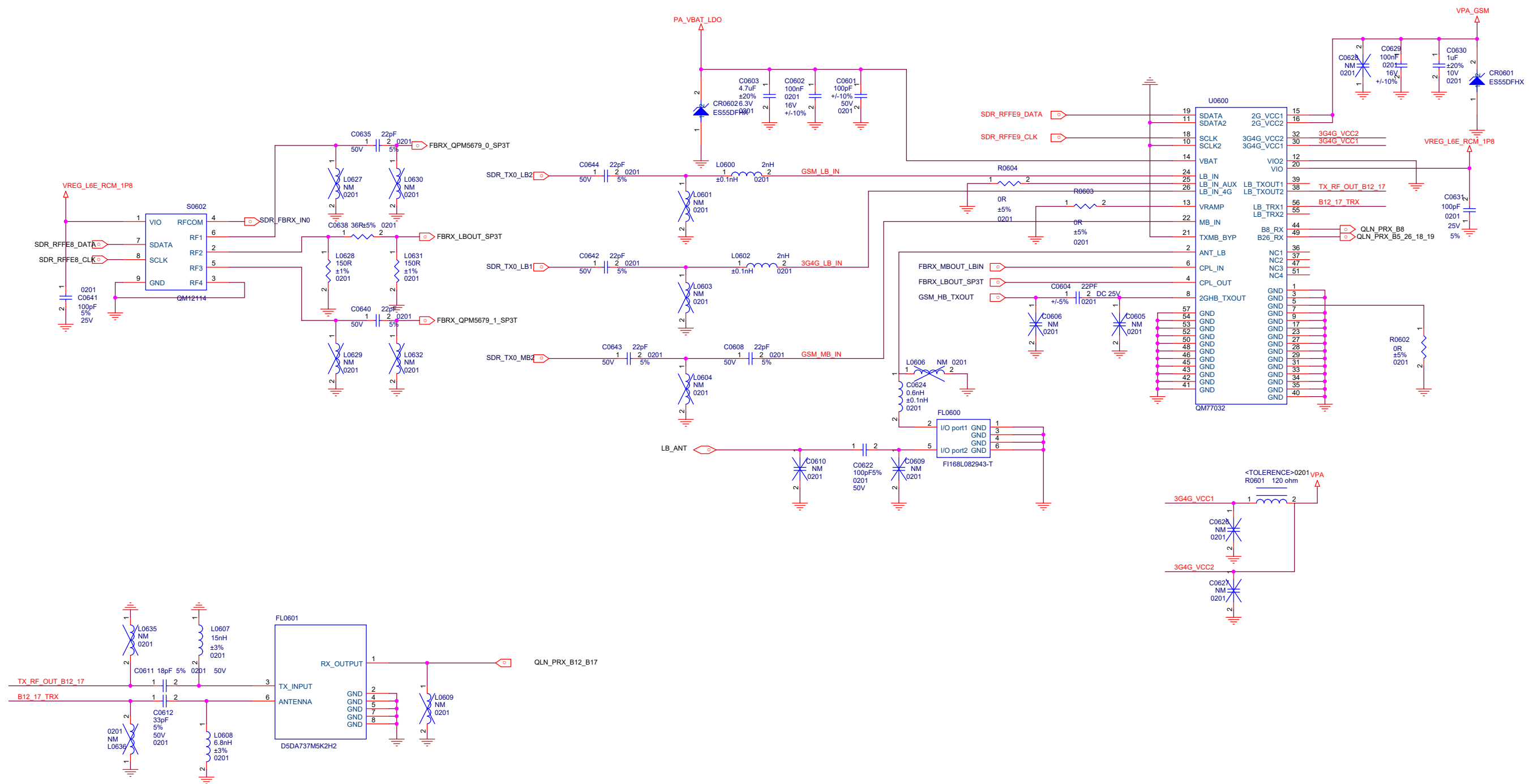




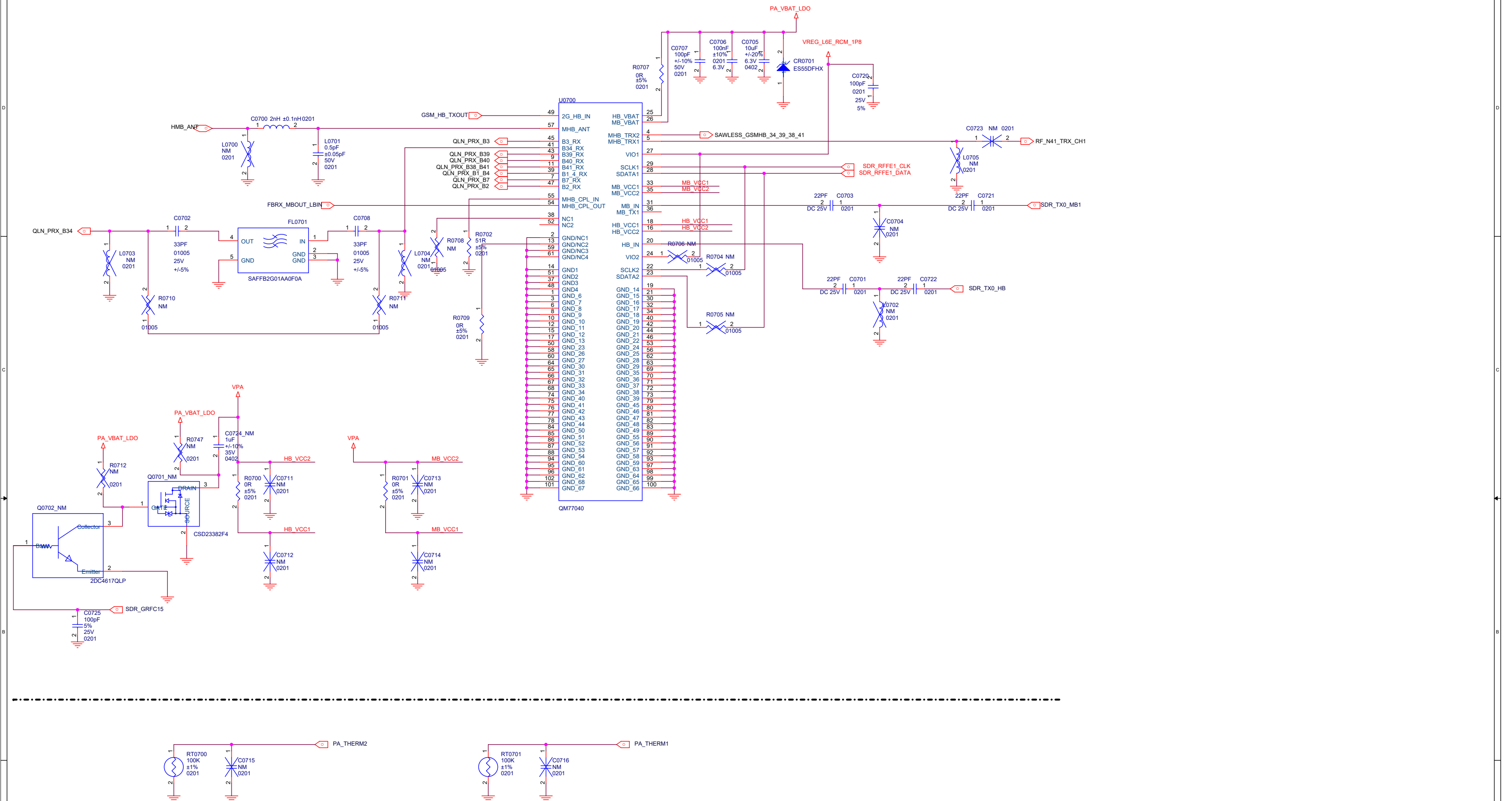


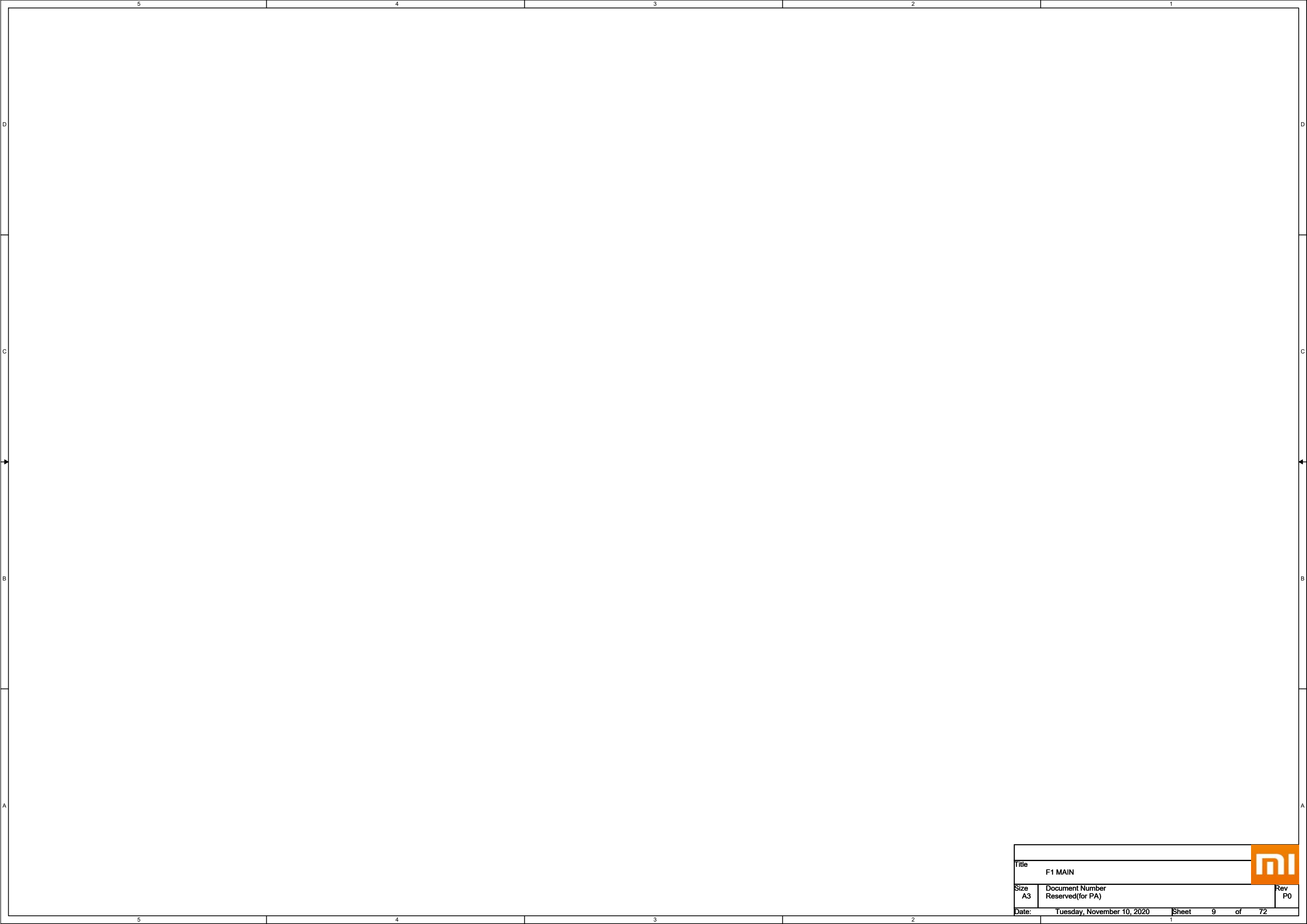



LB S-PAD

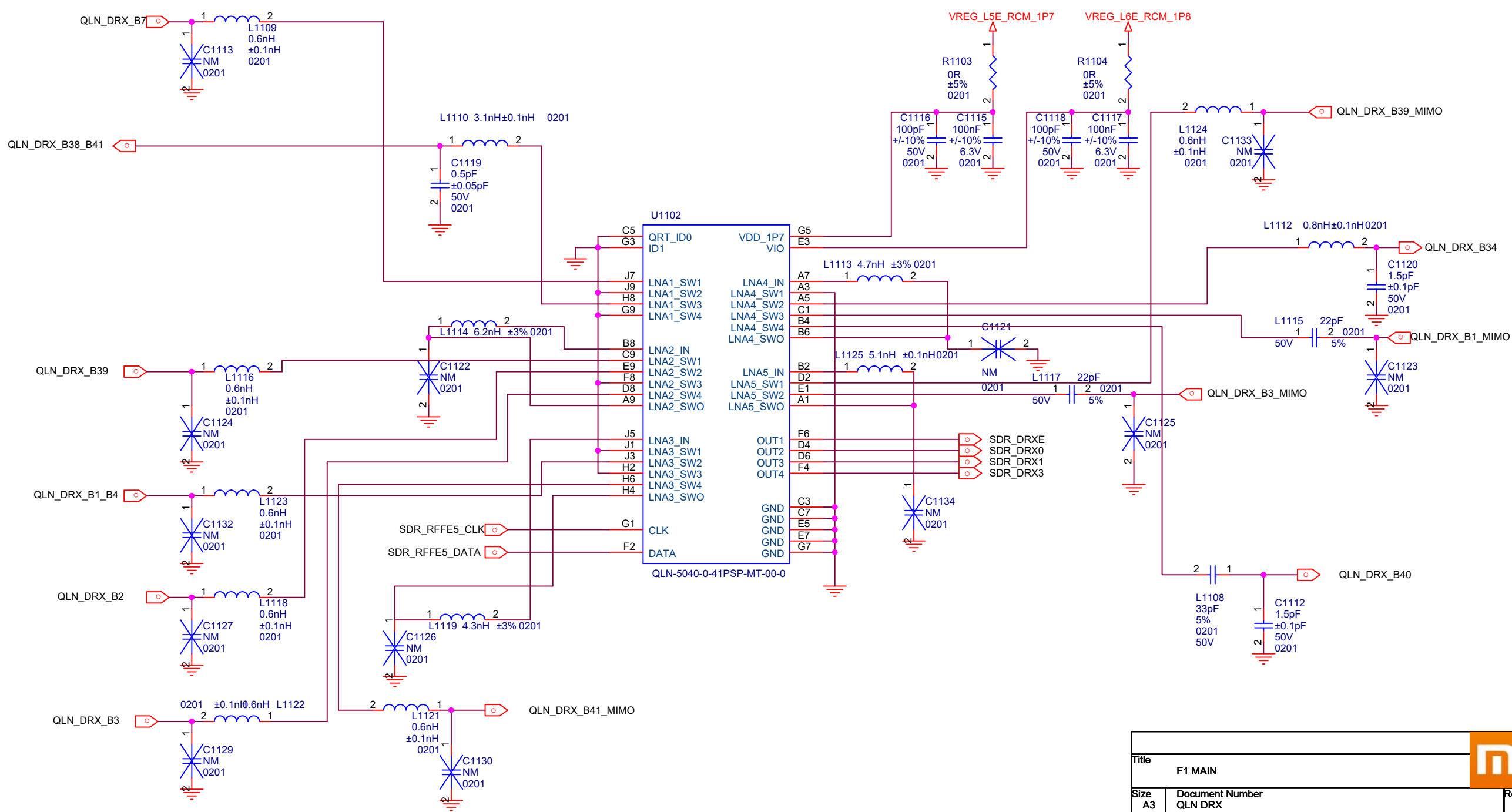
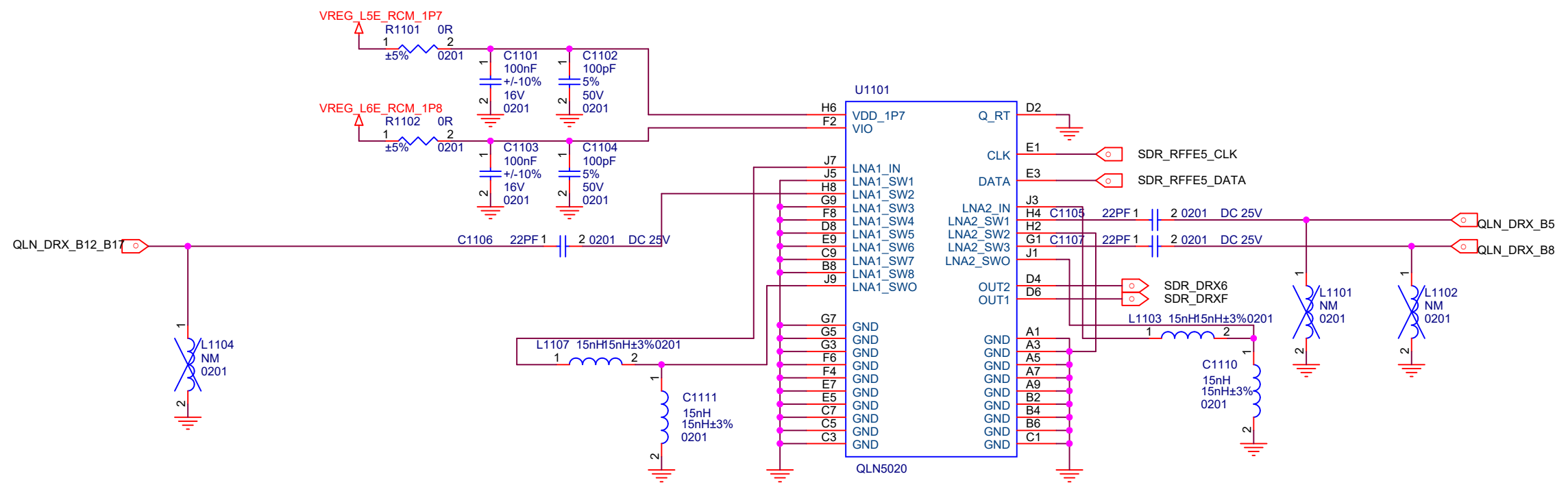


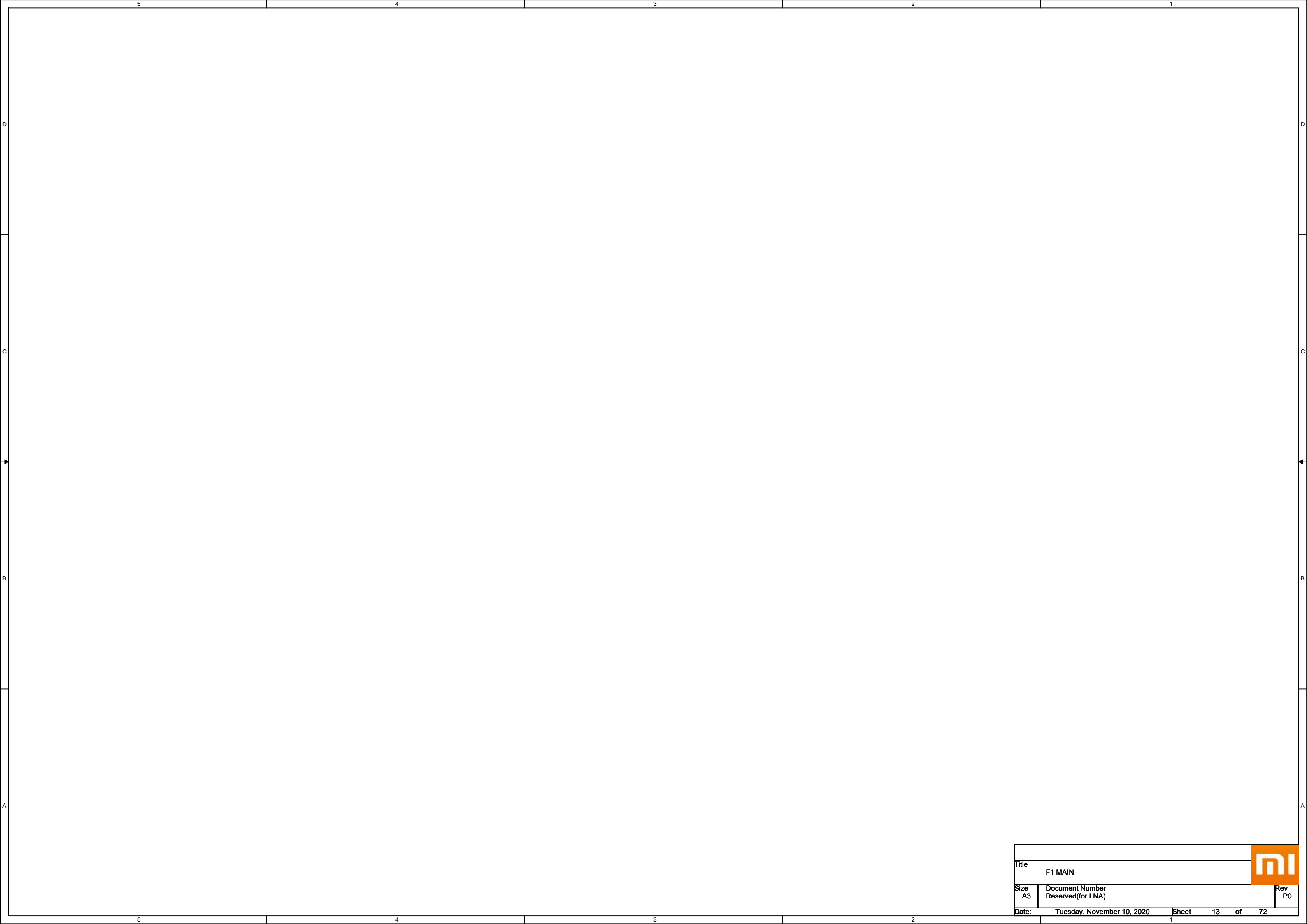
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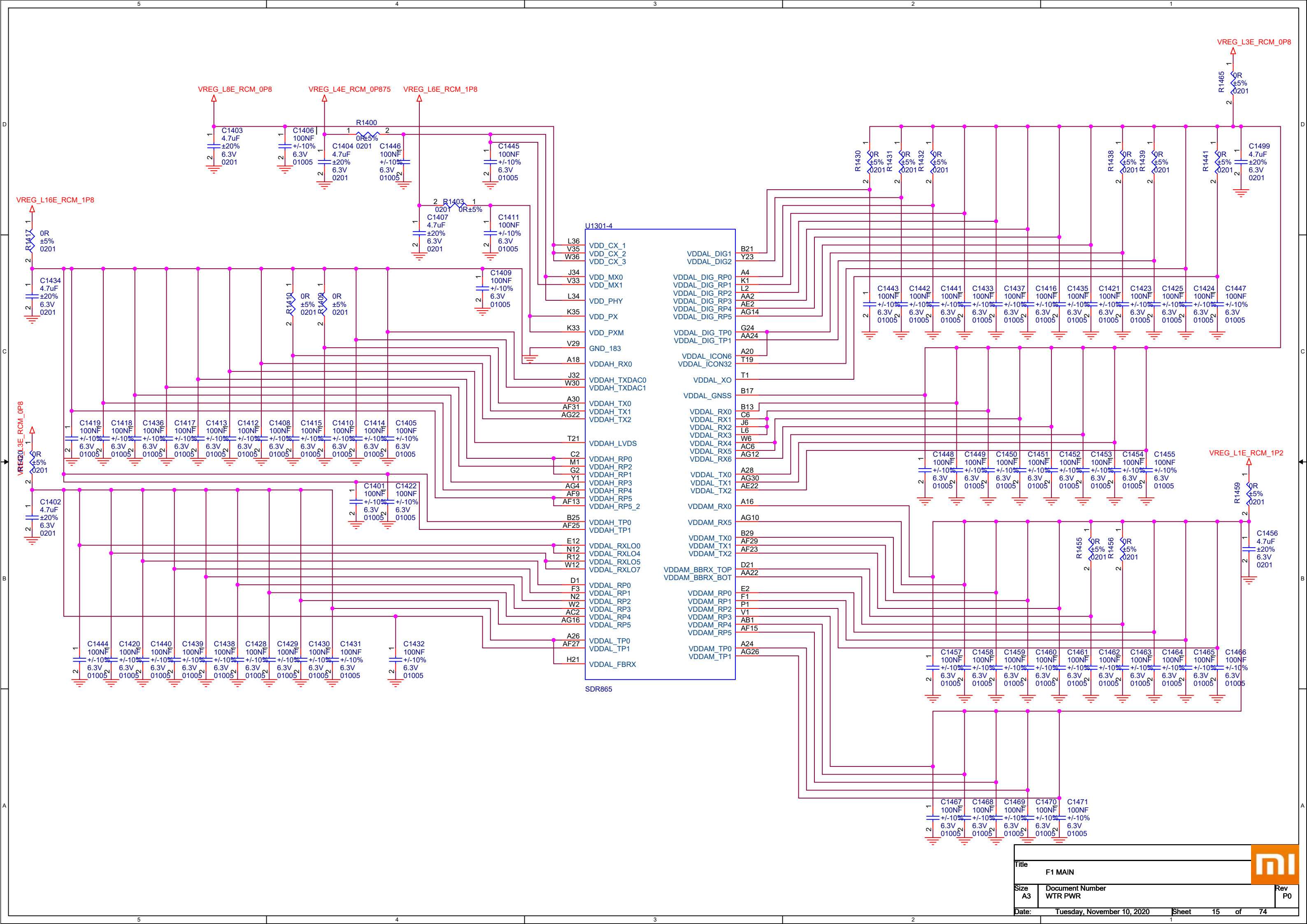


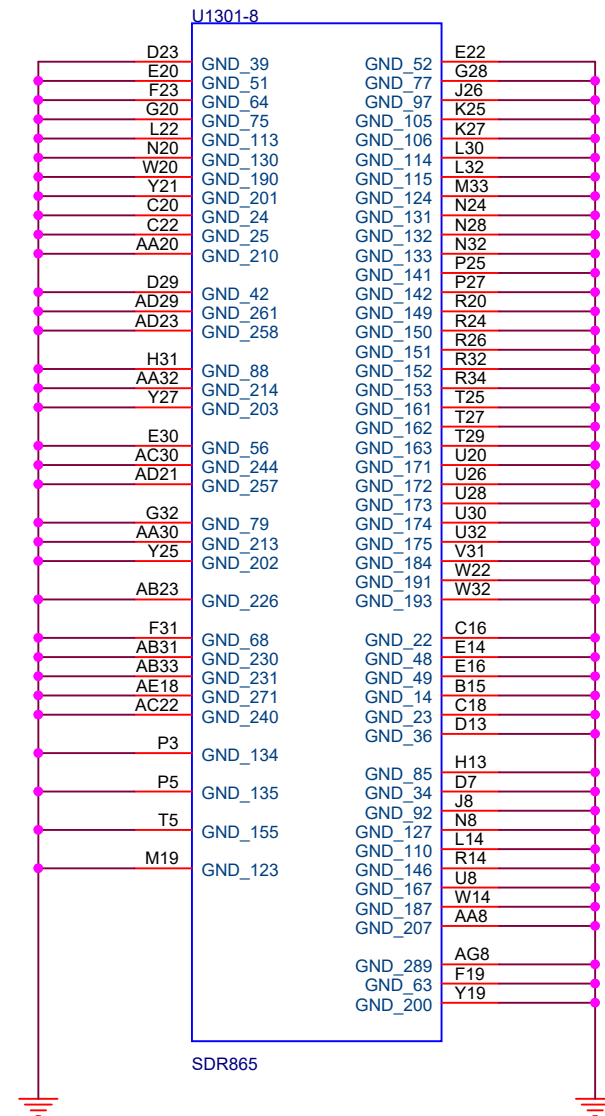
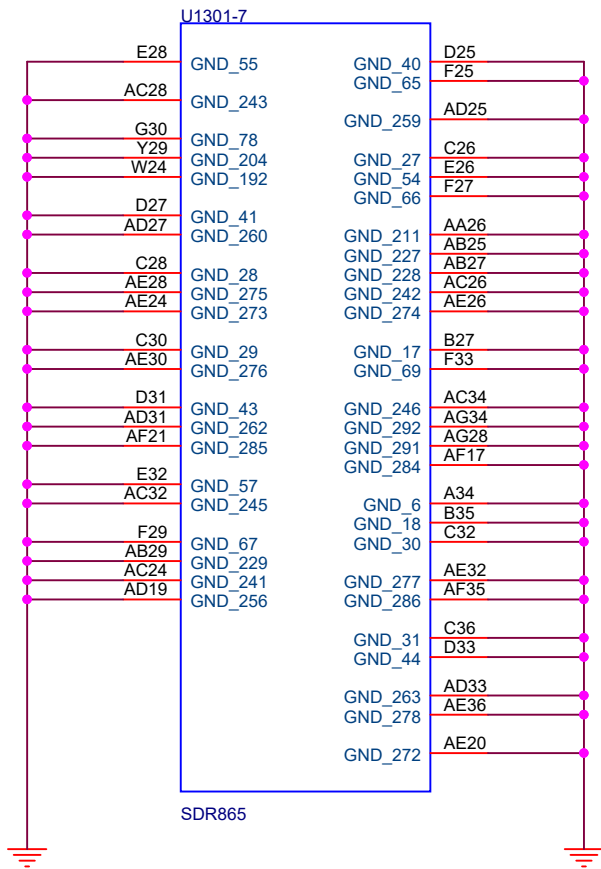
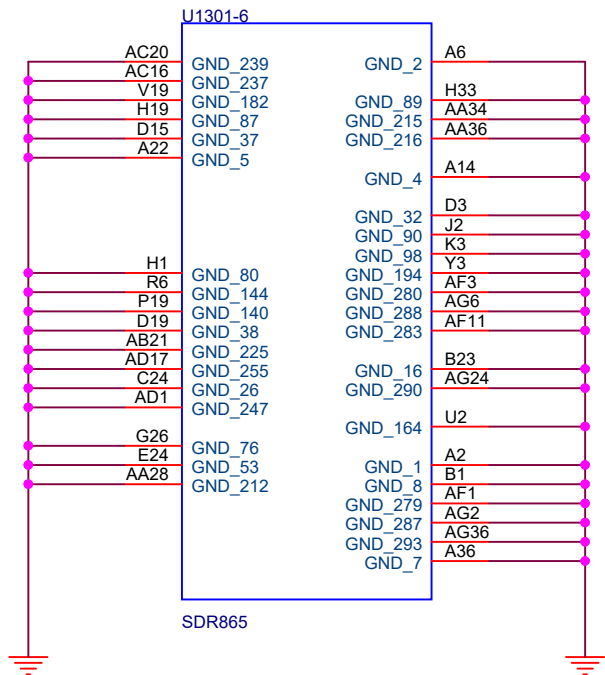
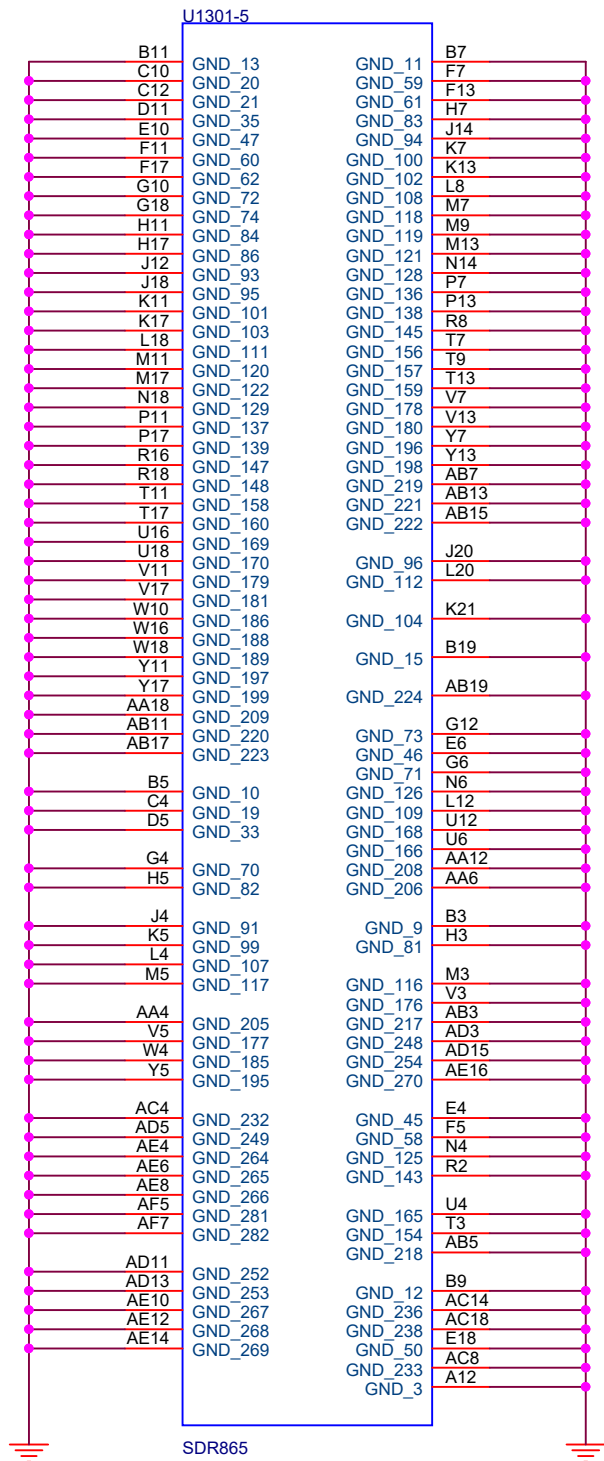


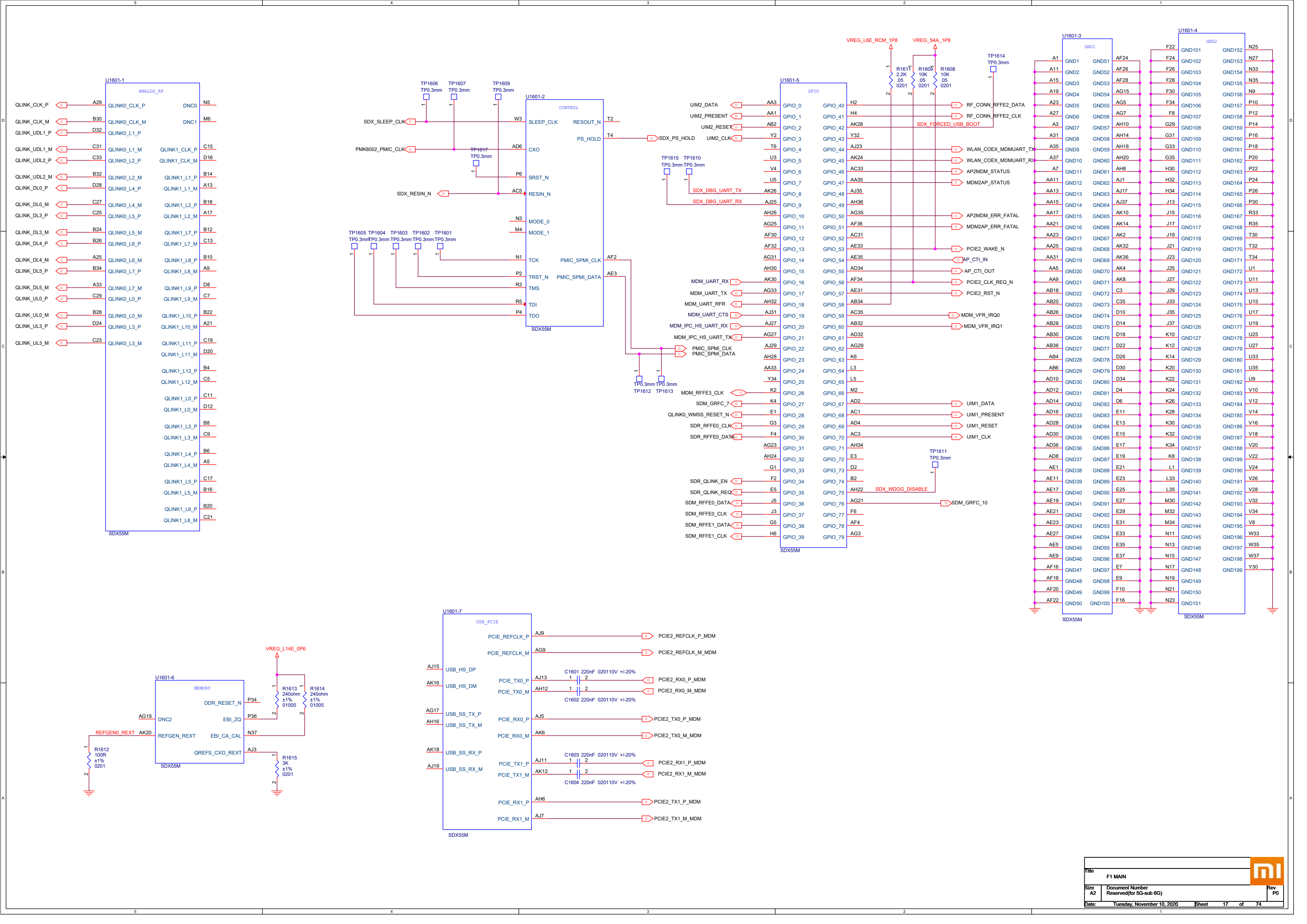
				
Title F1 MAIN				
Size A3	Document Number Reserved(for PA)			Rev P0
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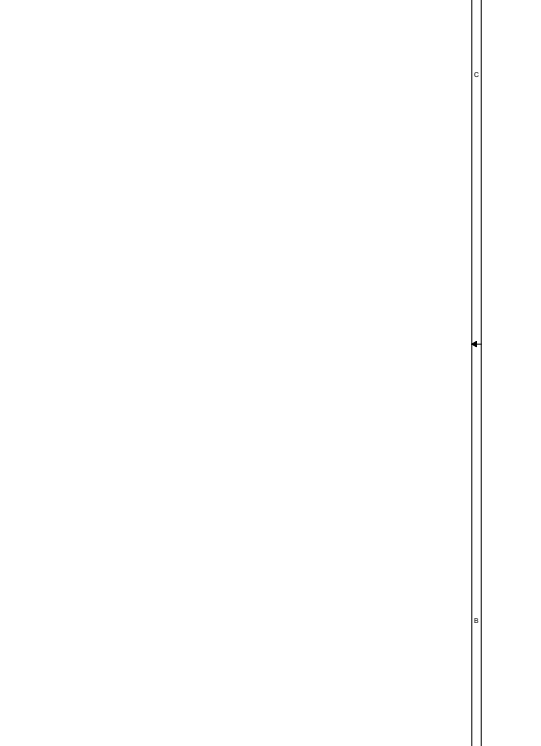
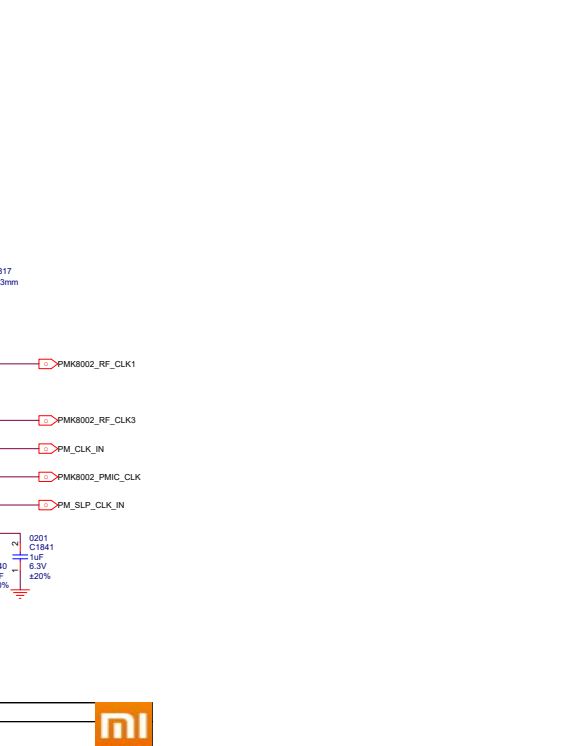
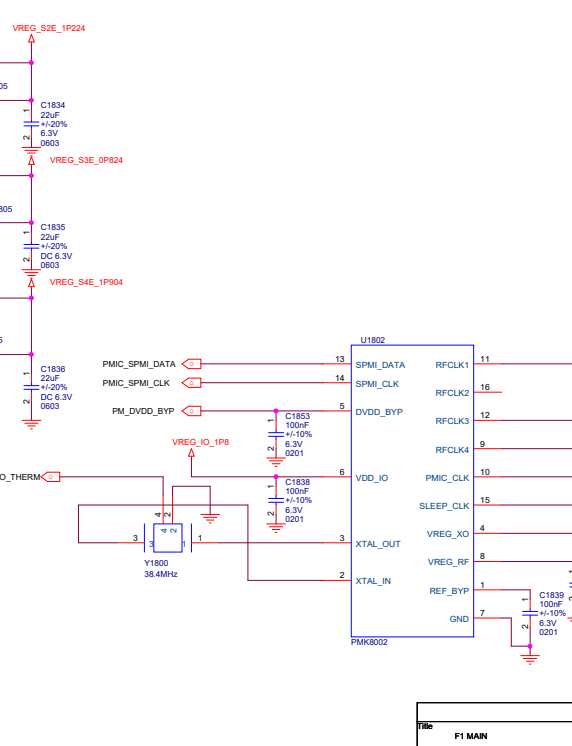
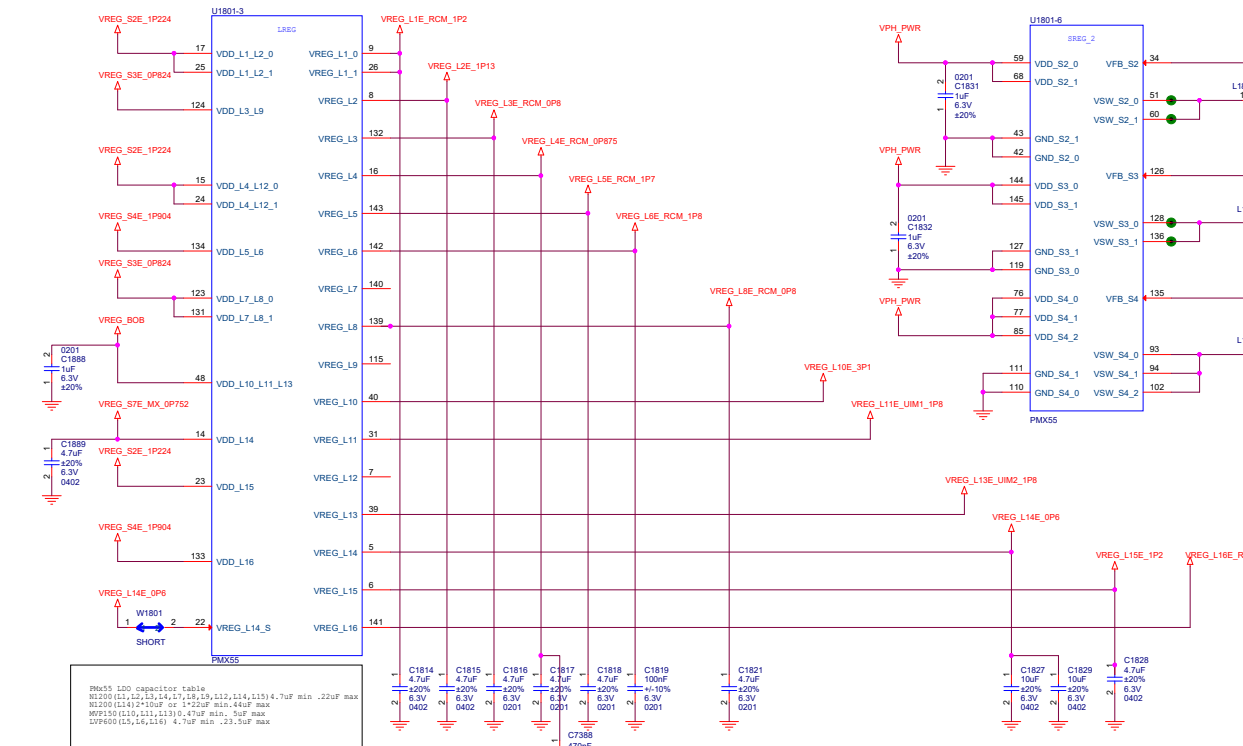
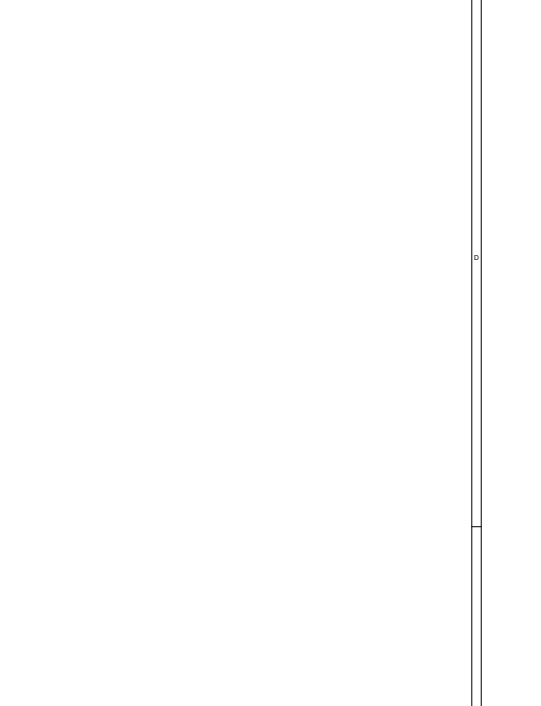
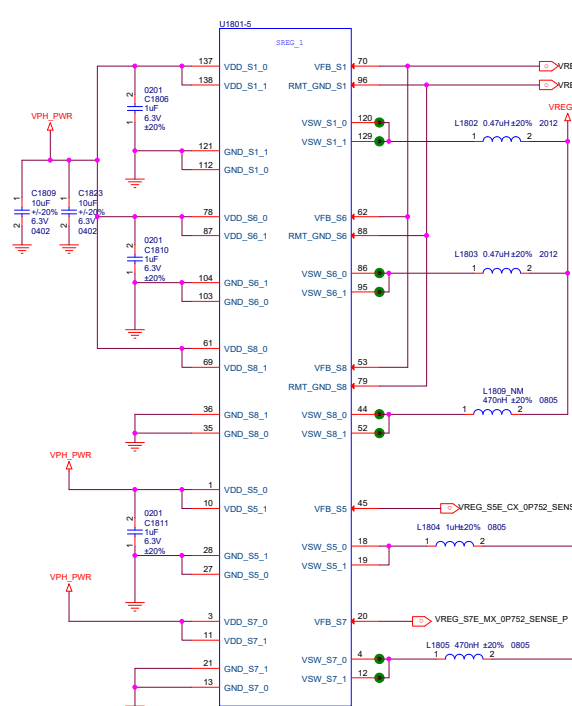
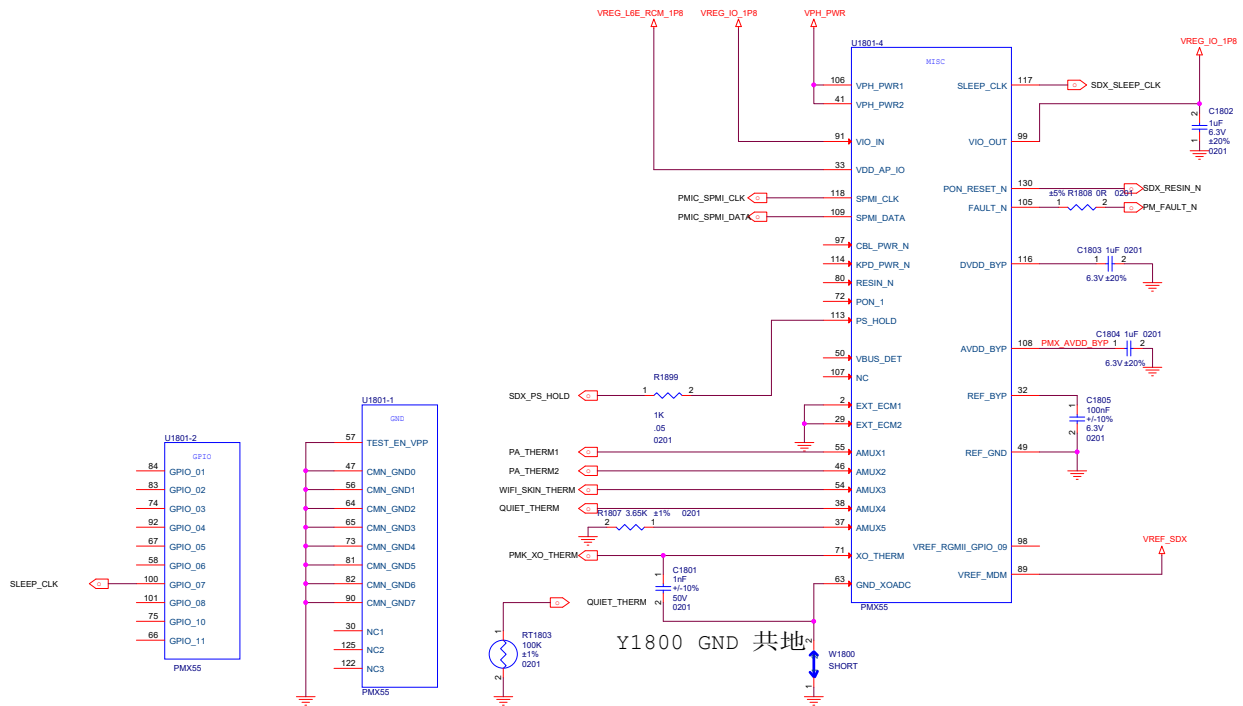







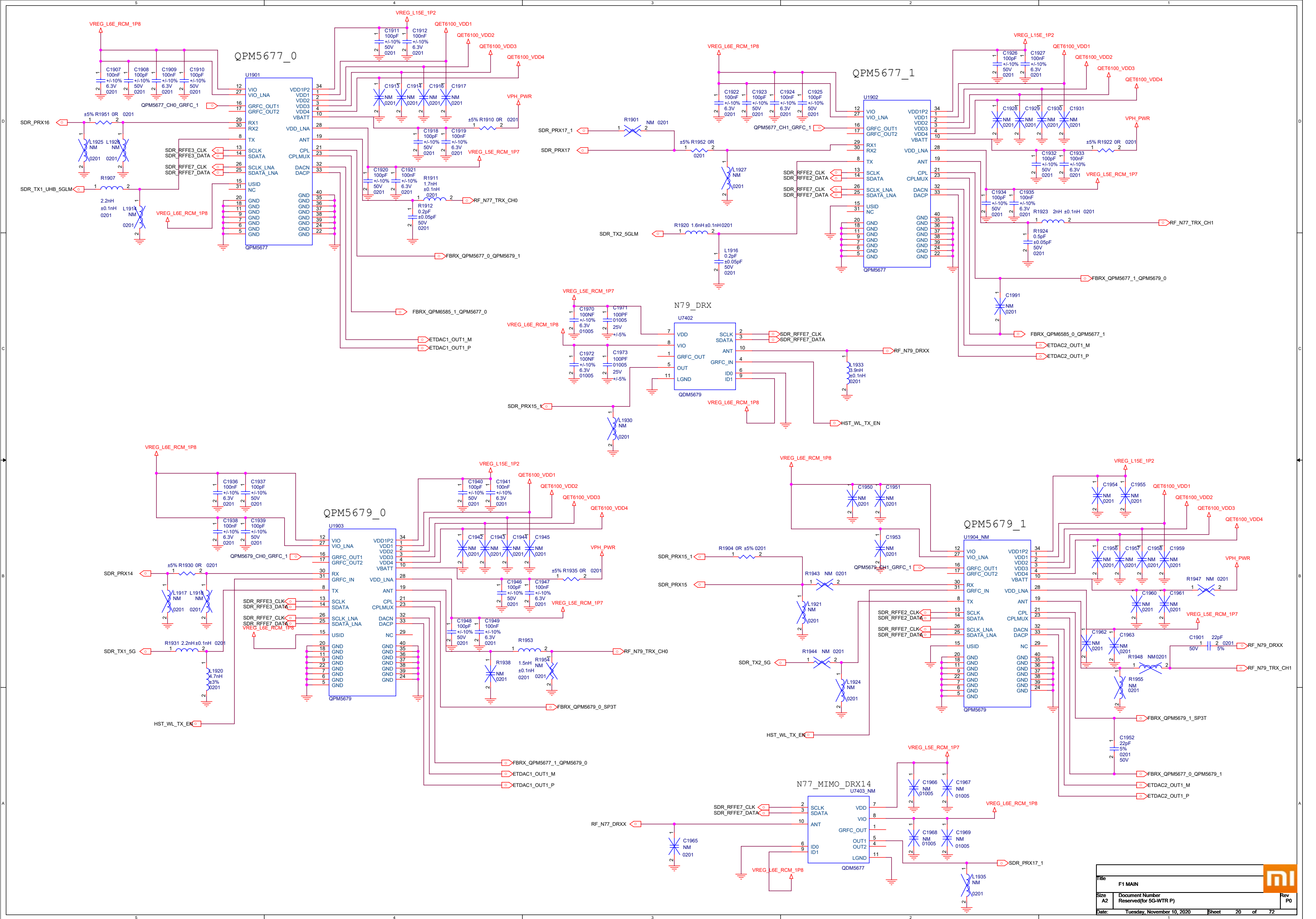


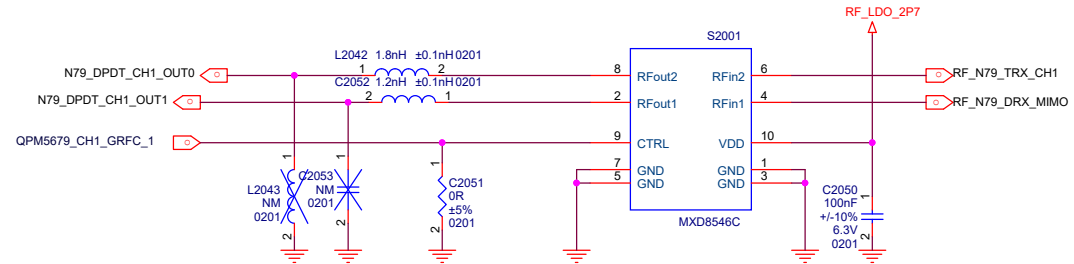
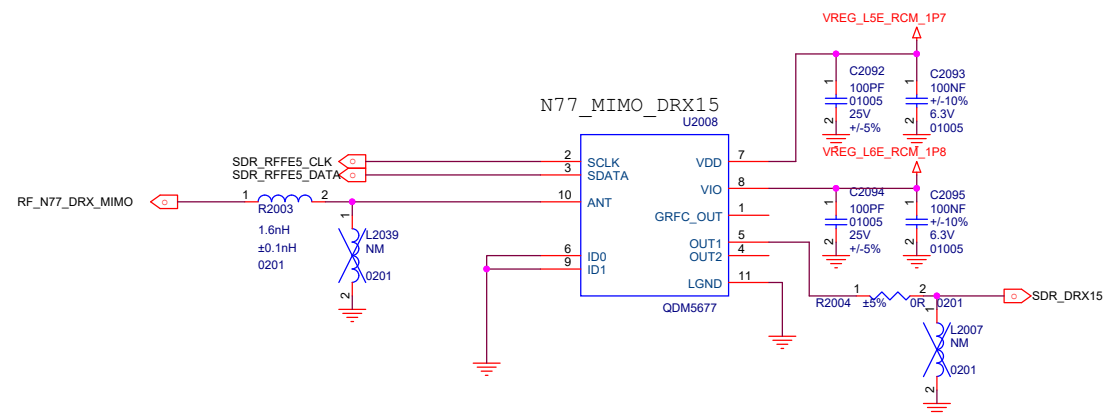
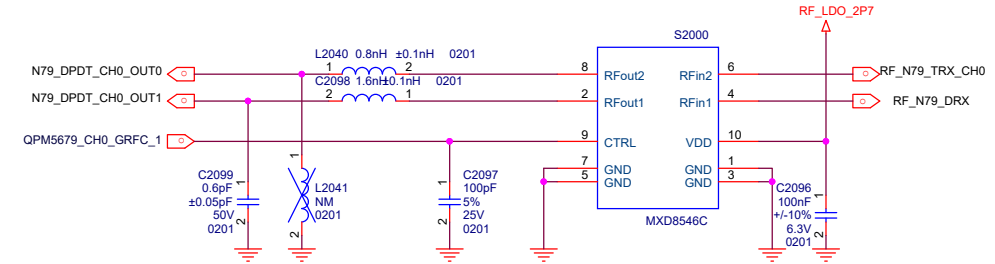
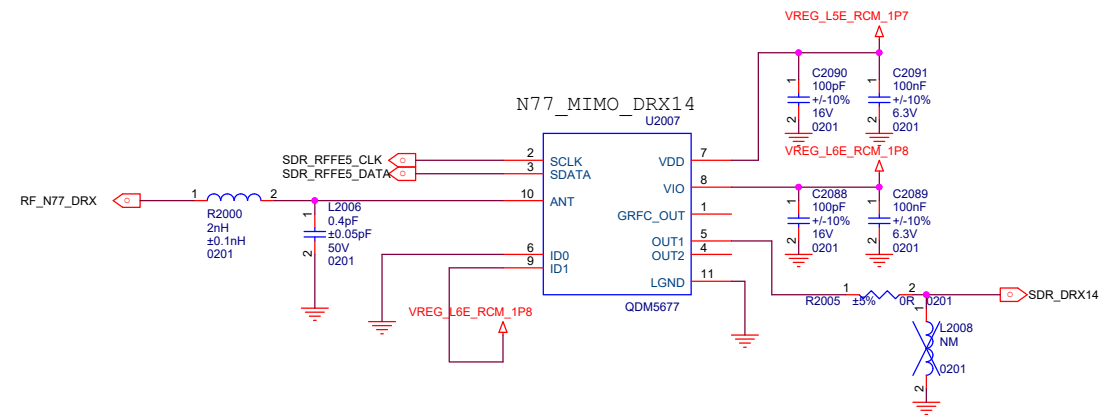
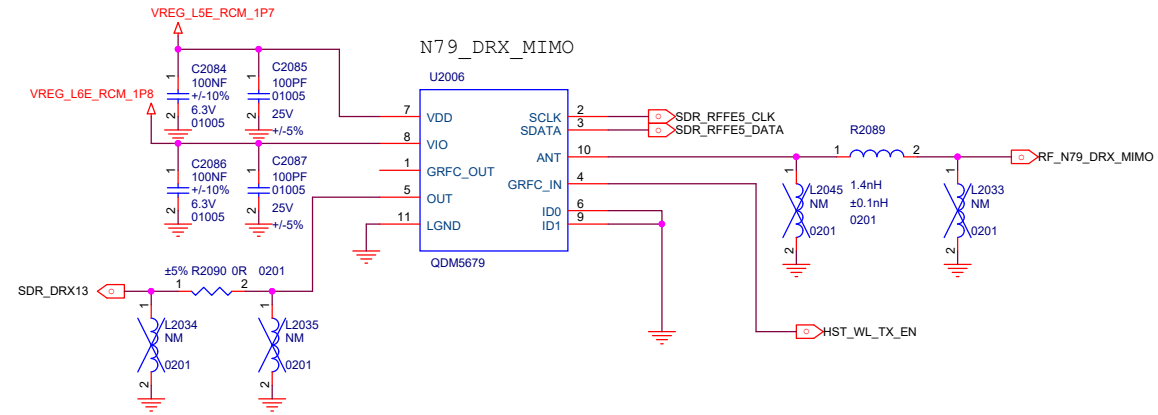
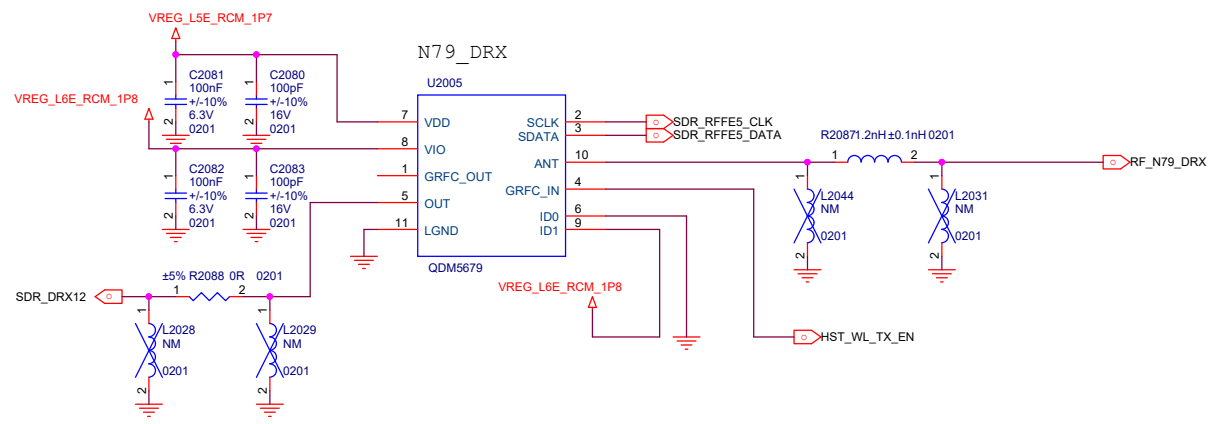
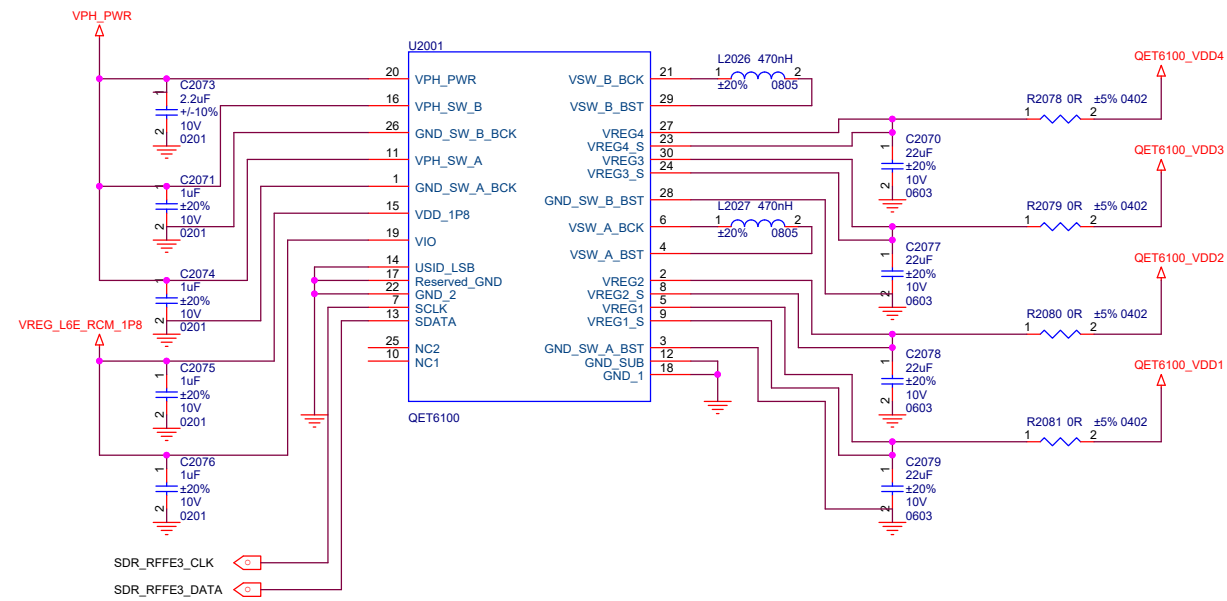


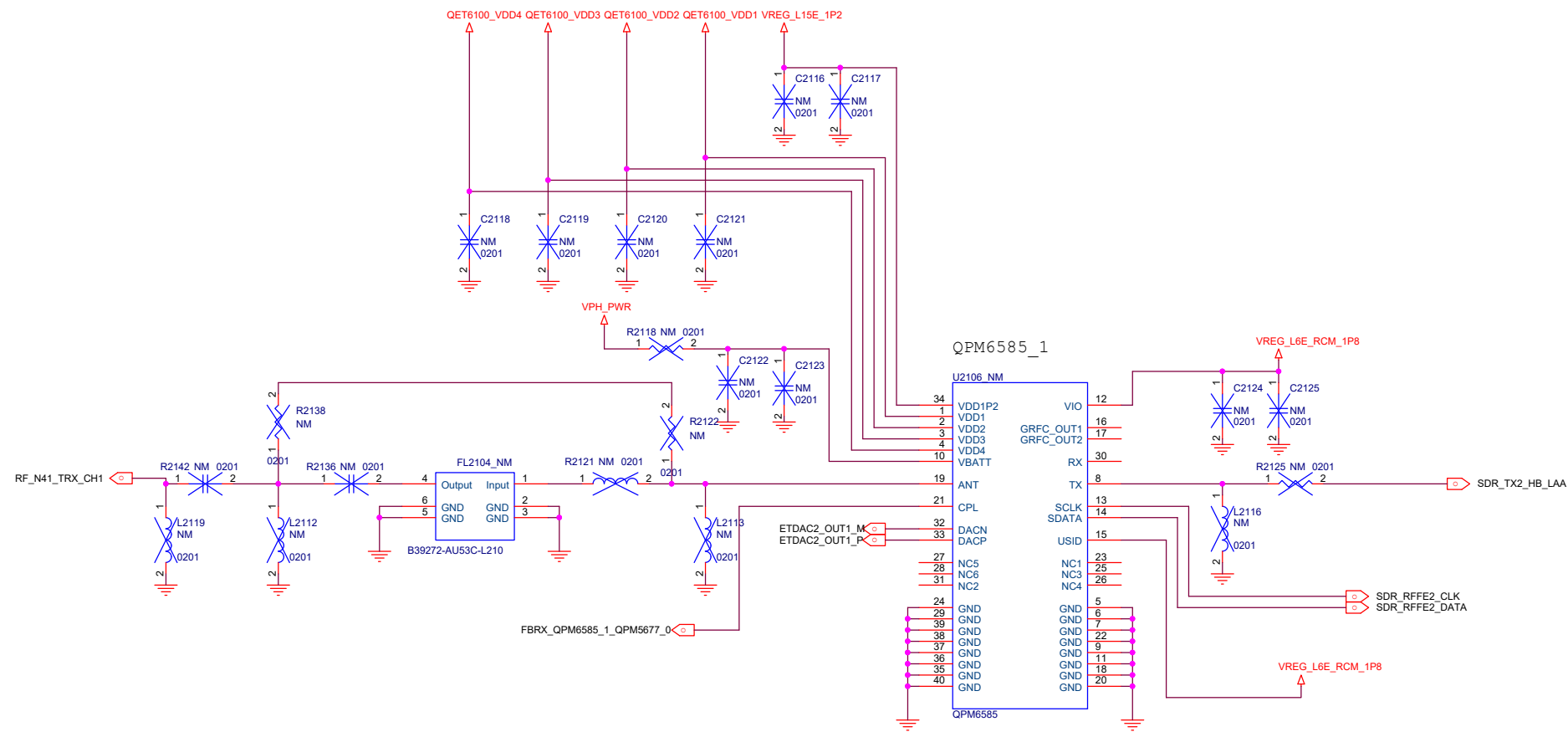
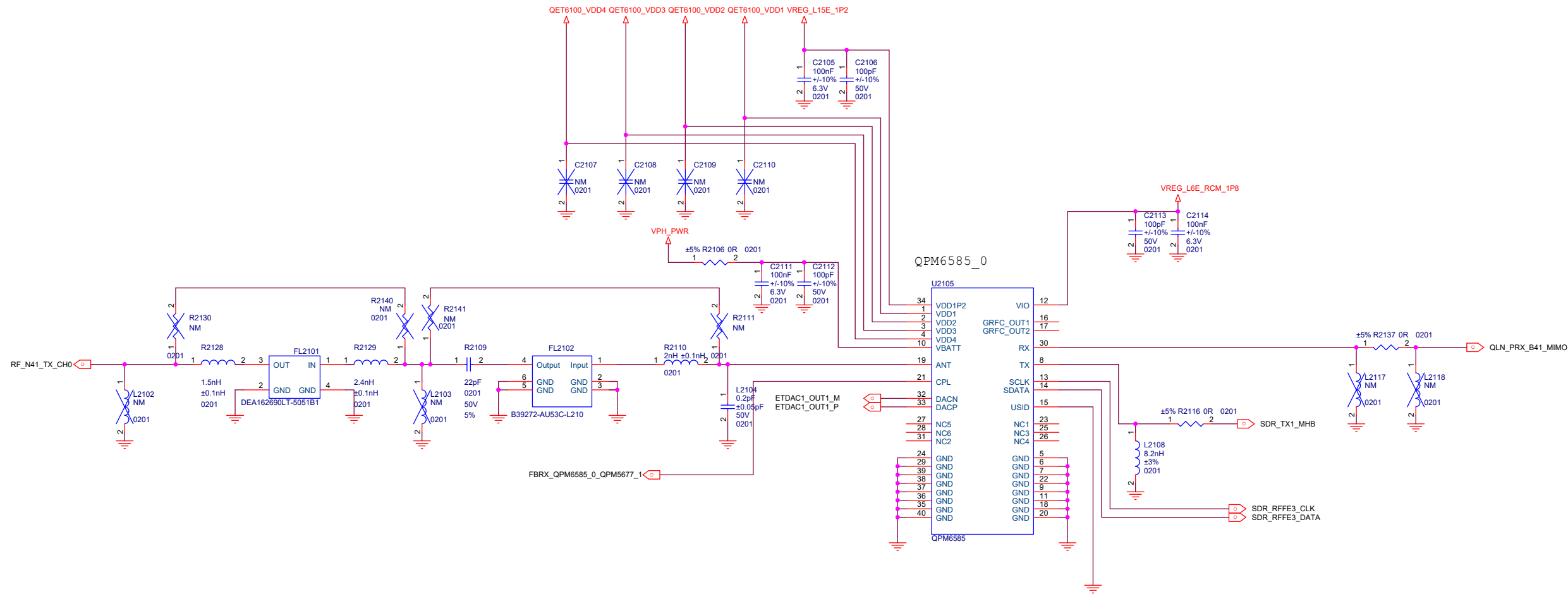


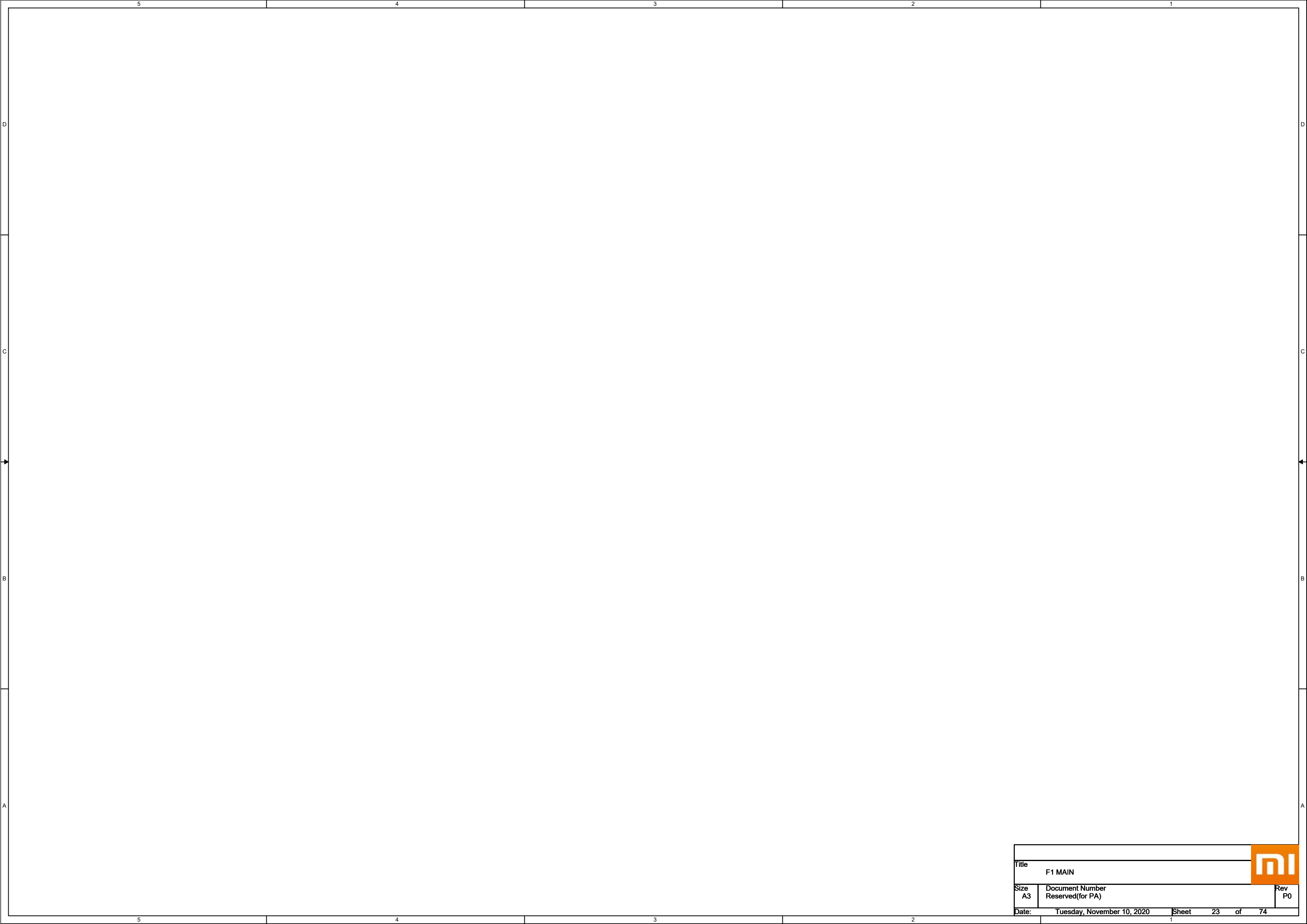
Rev55 100 capacitor table
 M200 (11,12,13,14,17,18,19,112,114,115) 4.7uF min. 22uF max
 M200 (14,17,18,19) 10uF or 112uF min. 44uF max
 M200 (11,12,13,14,17,18,19) 4.7uF min. 9uF max
 M200 (11,12,13,14) 4.7uF min. 22uF max


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Size	Document Number			Rev
A1	Reserved (for 5G-WTR I)			
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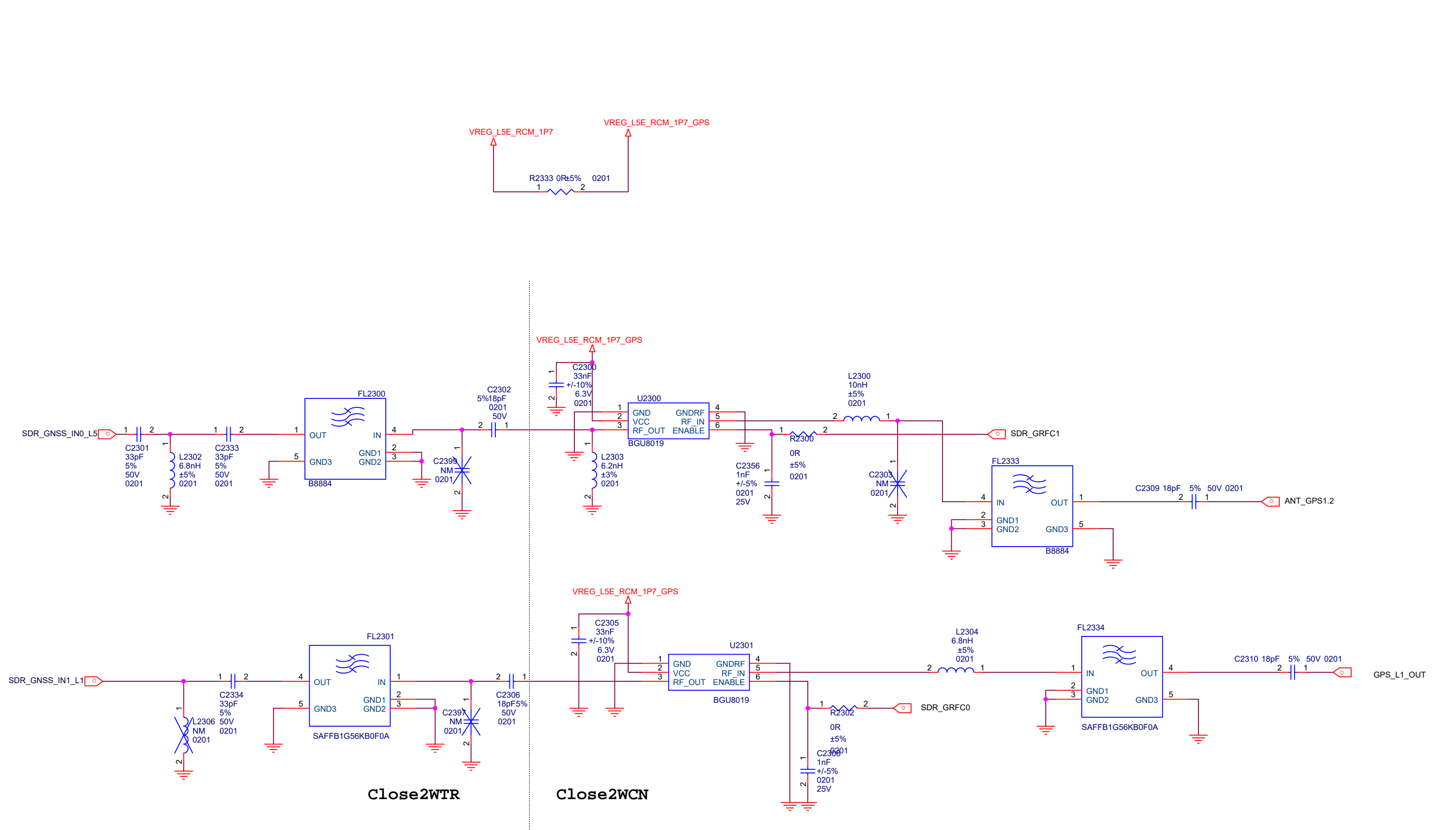


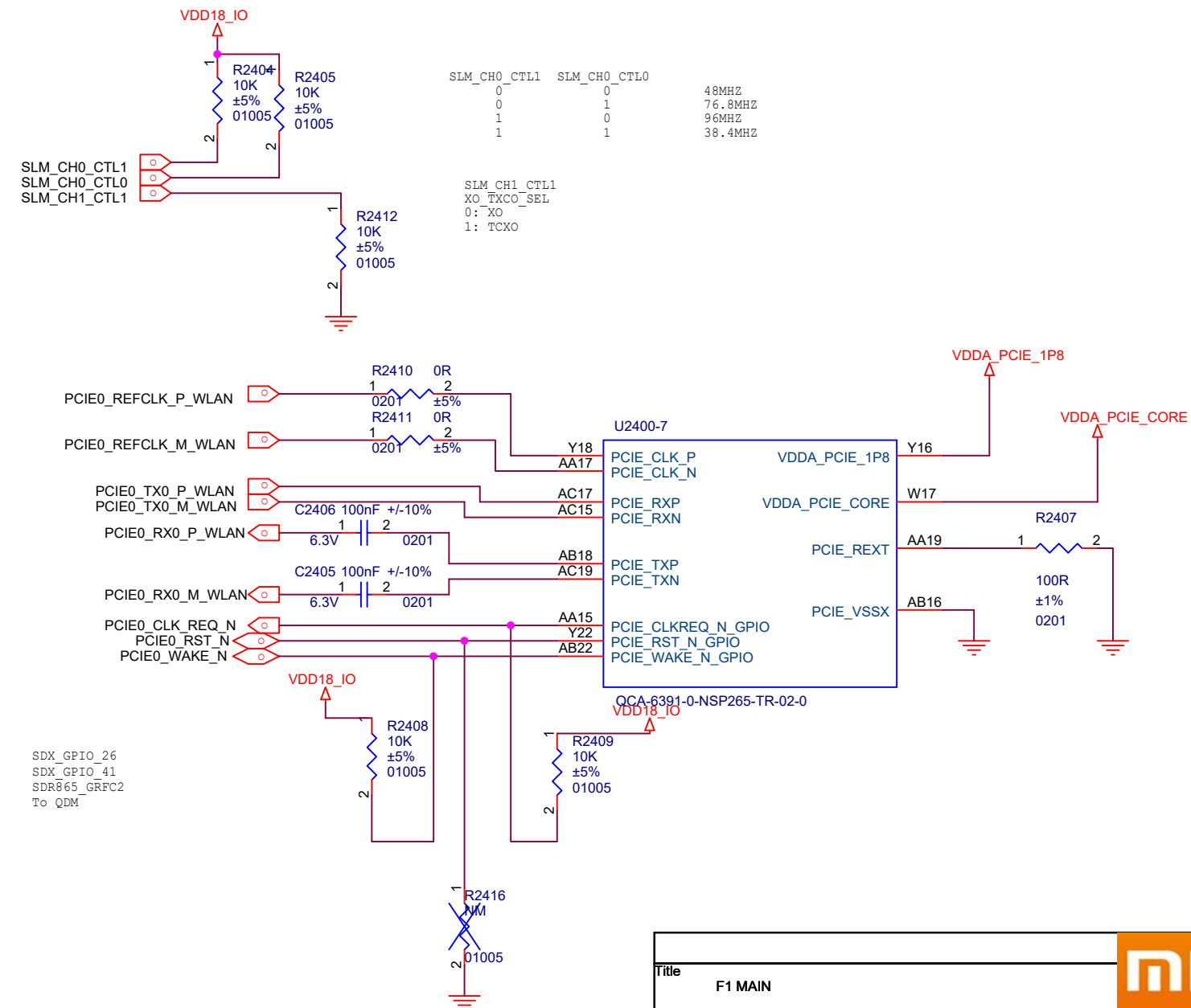
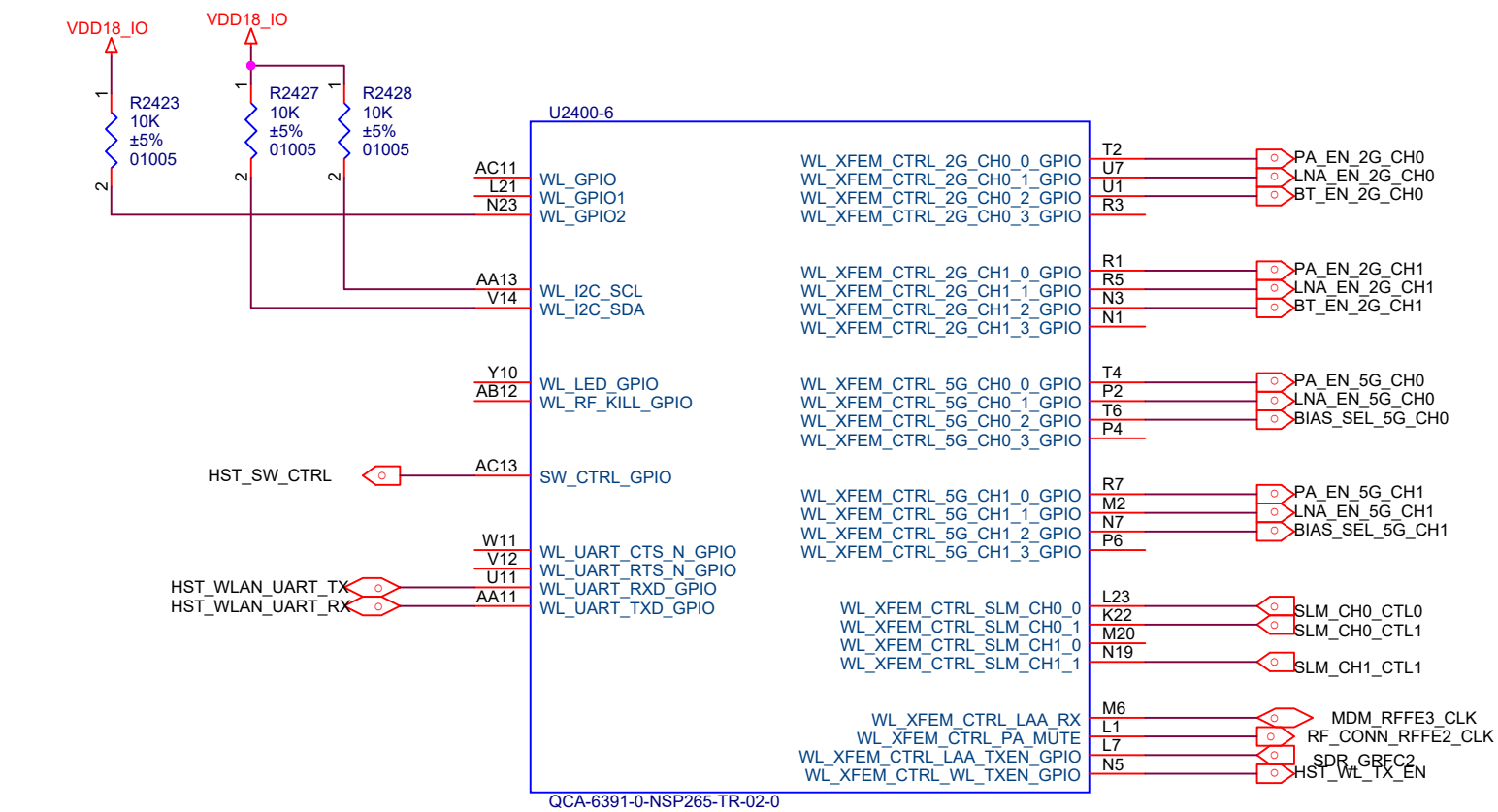
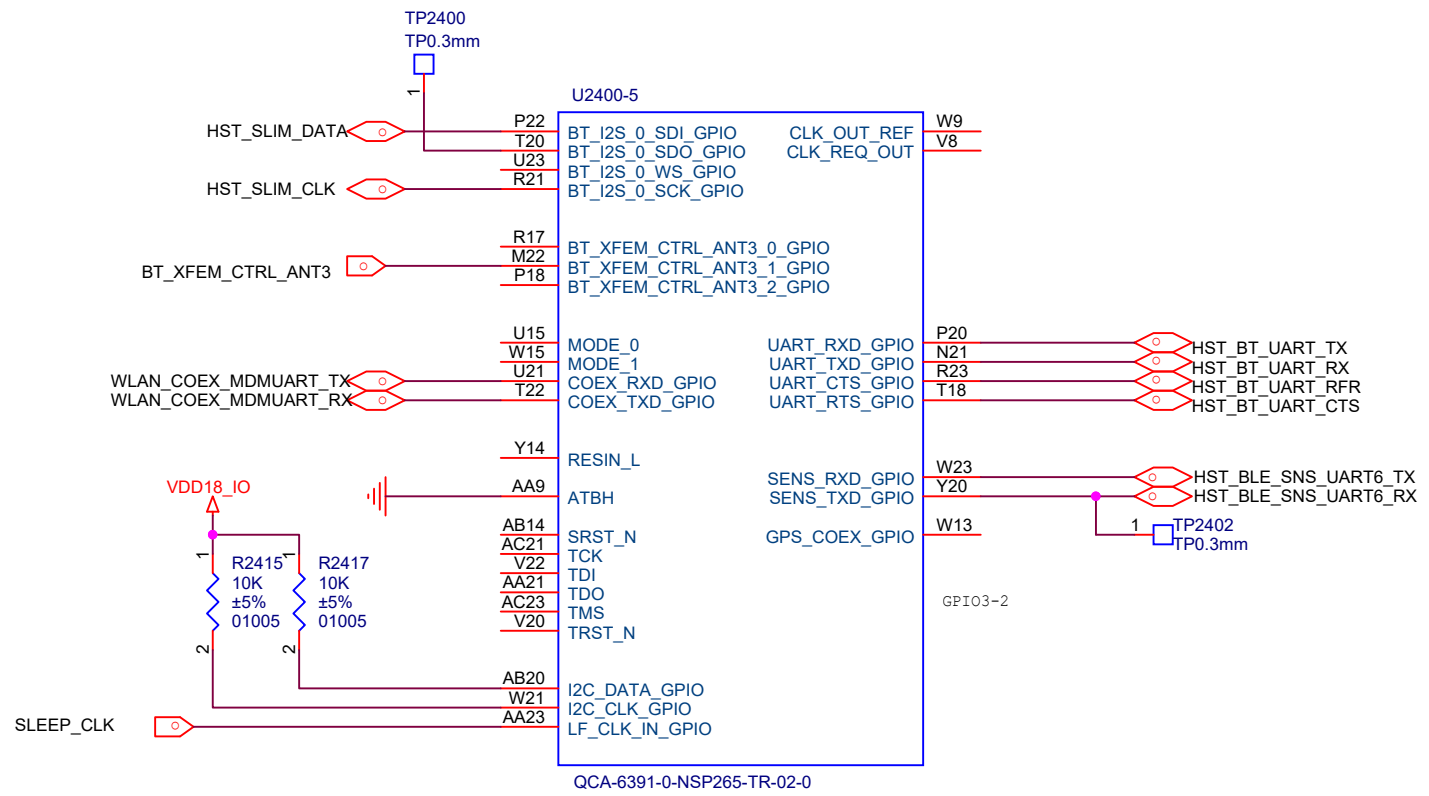
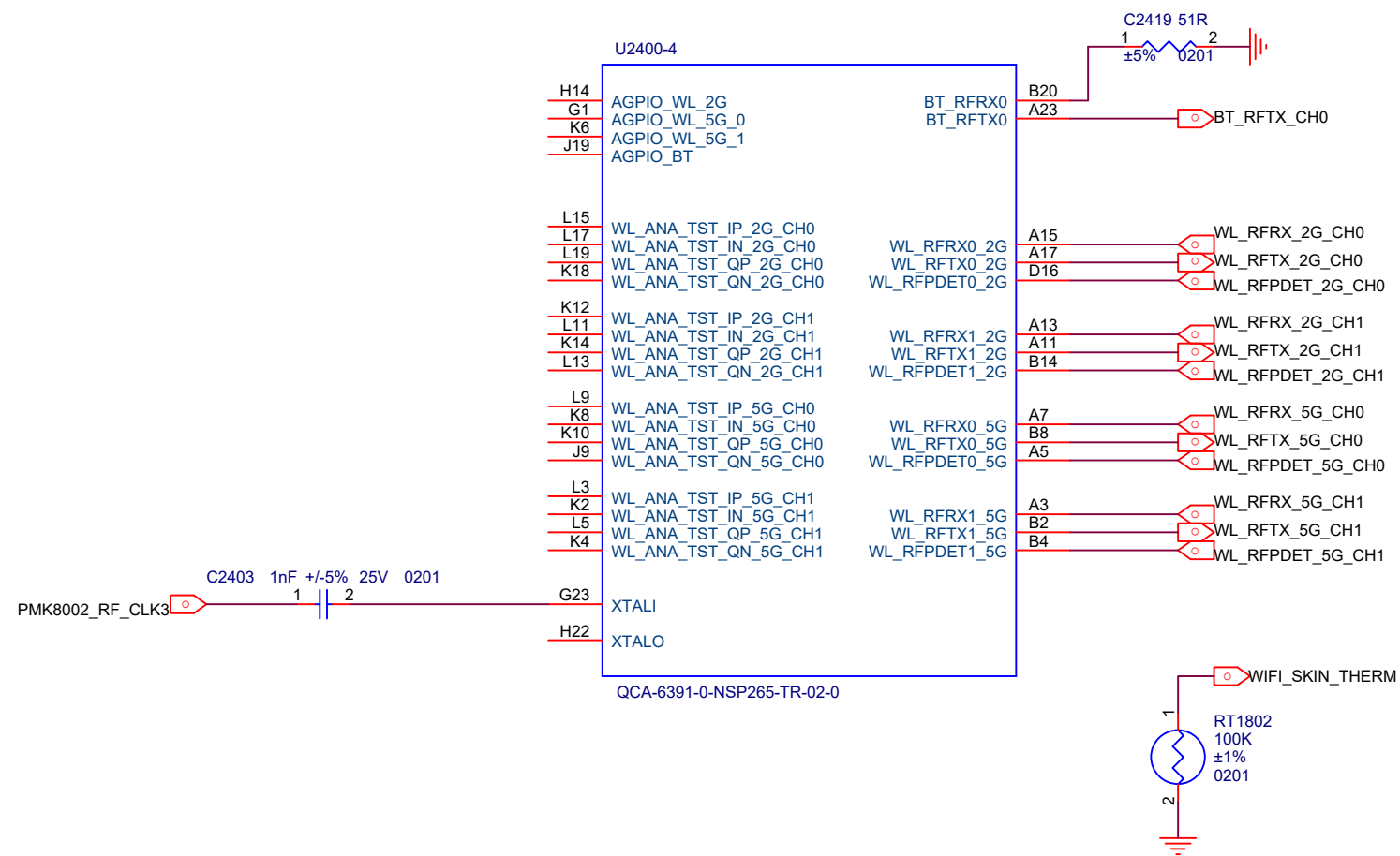


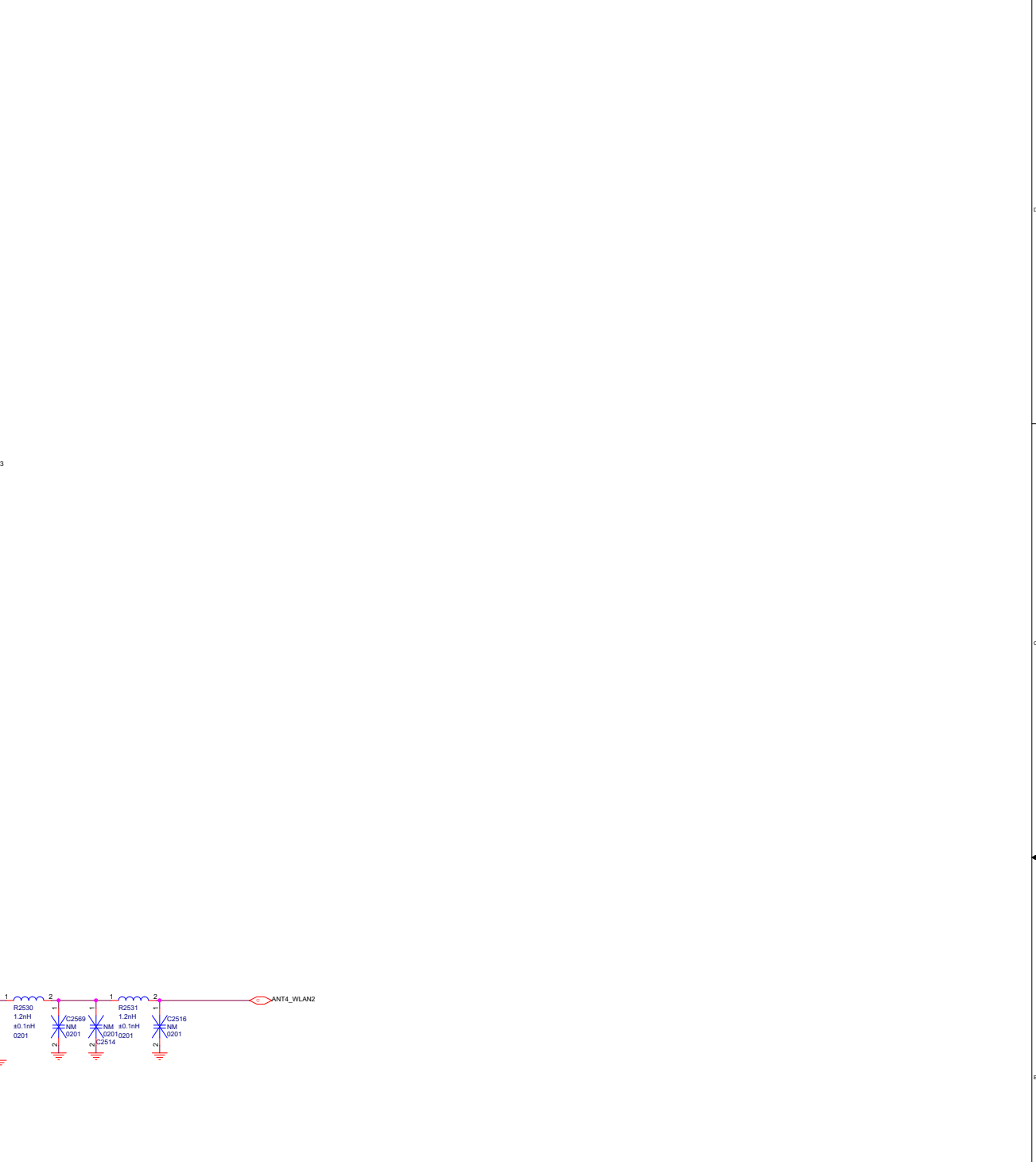
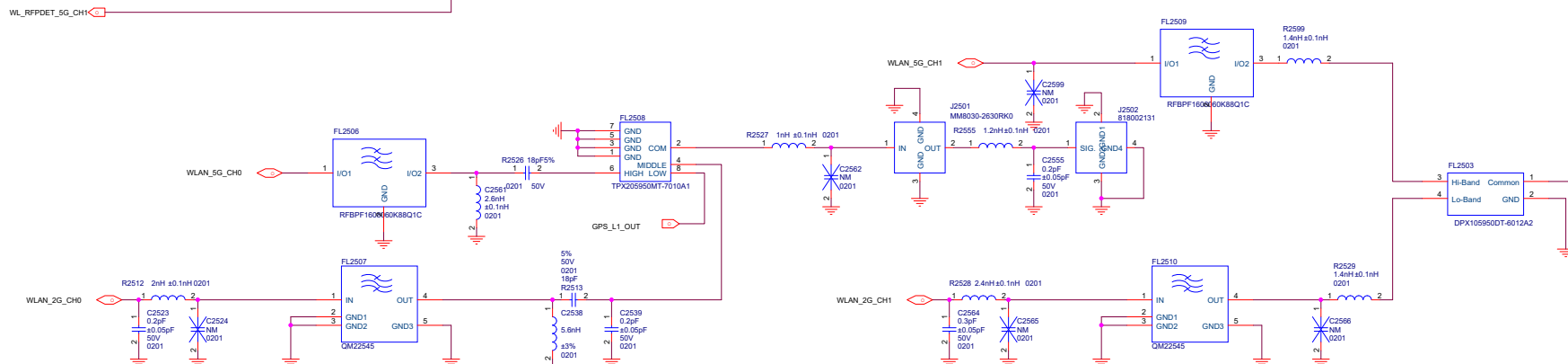
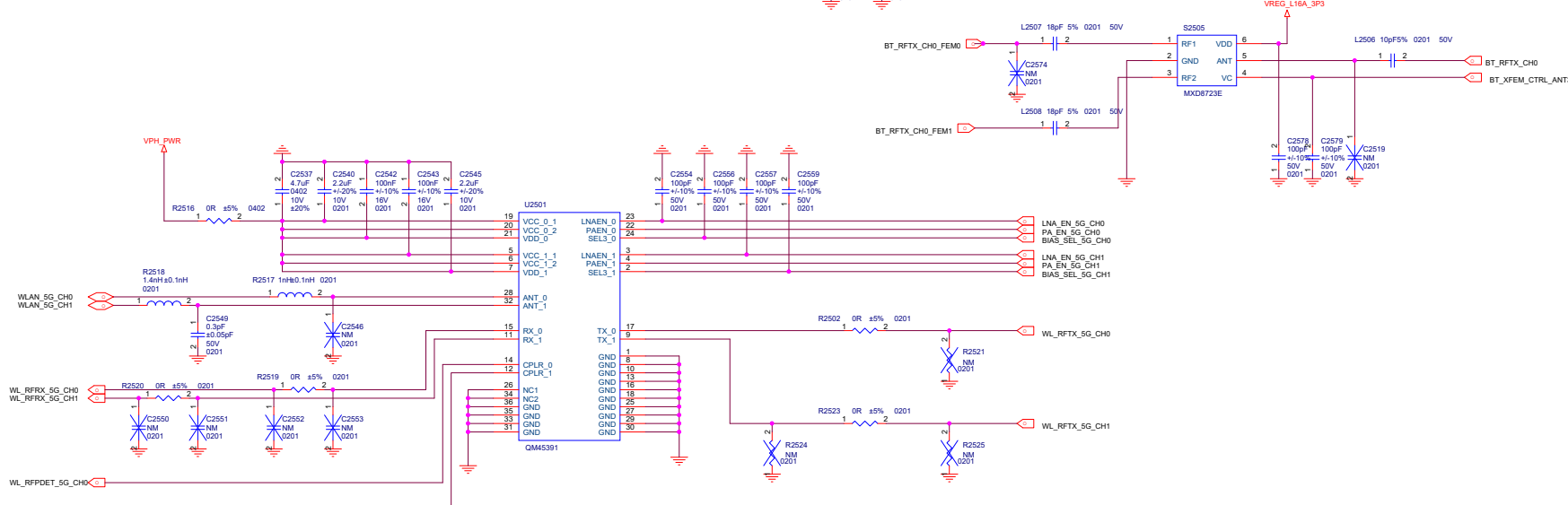
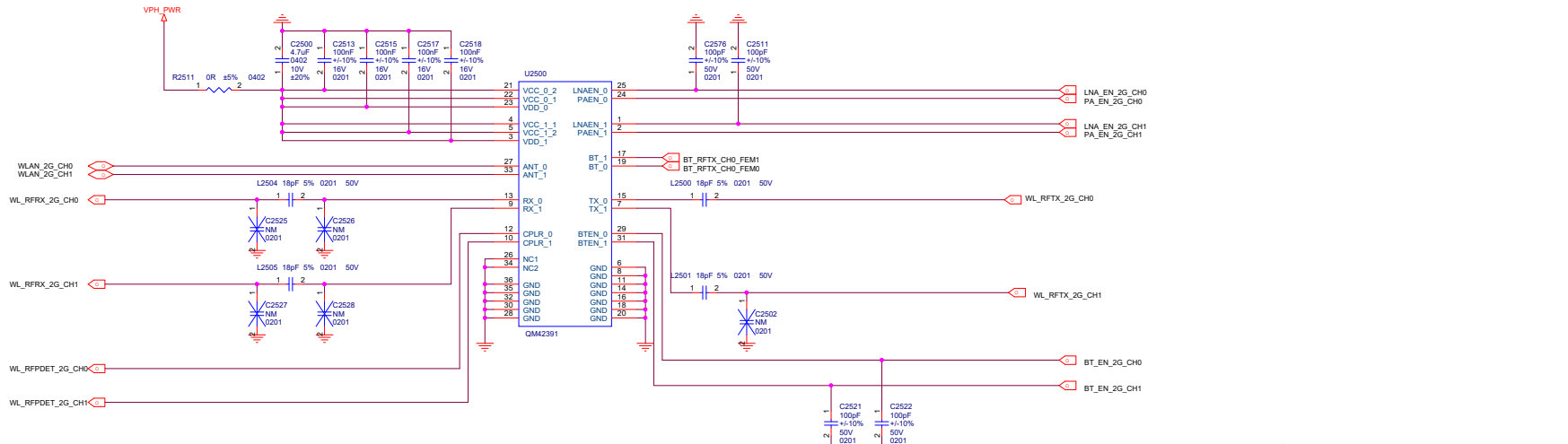




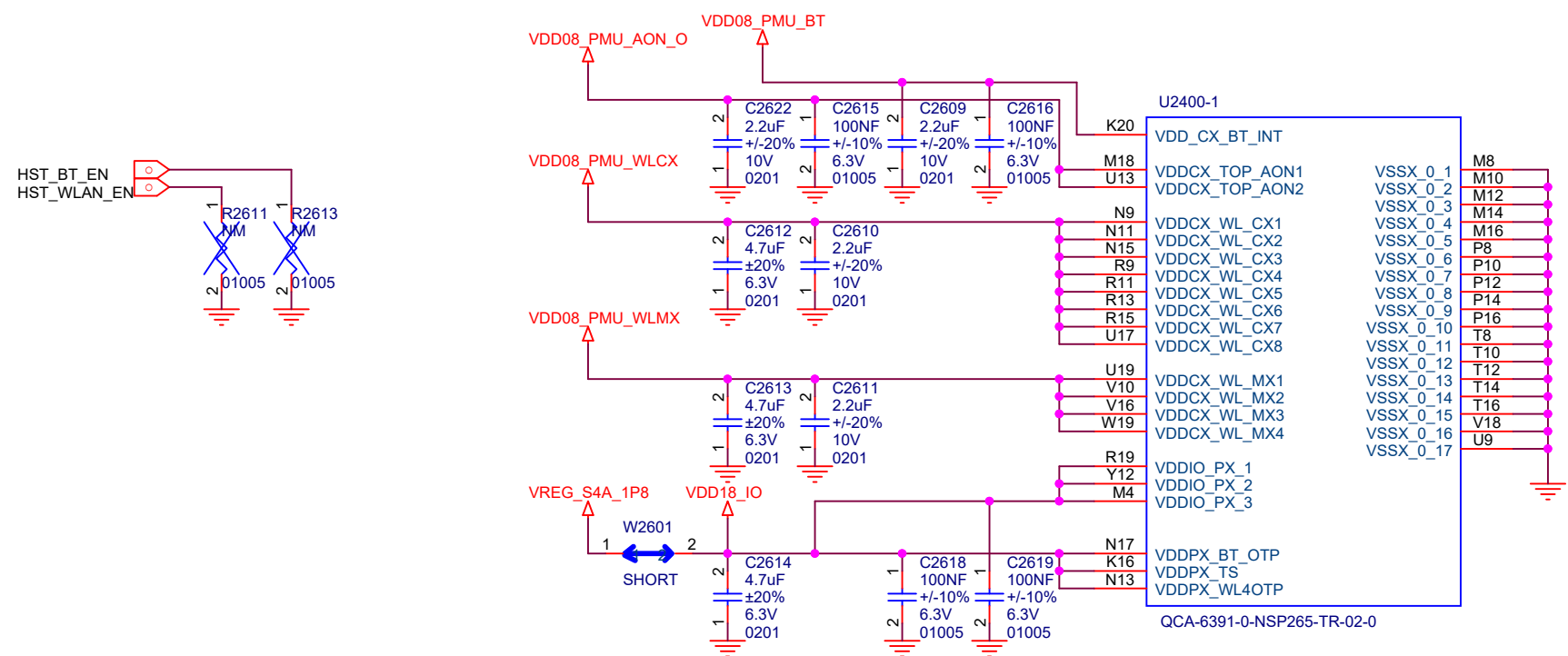
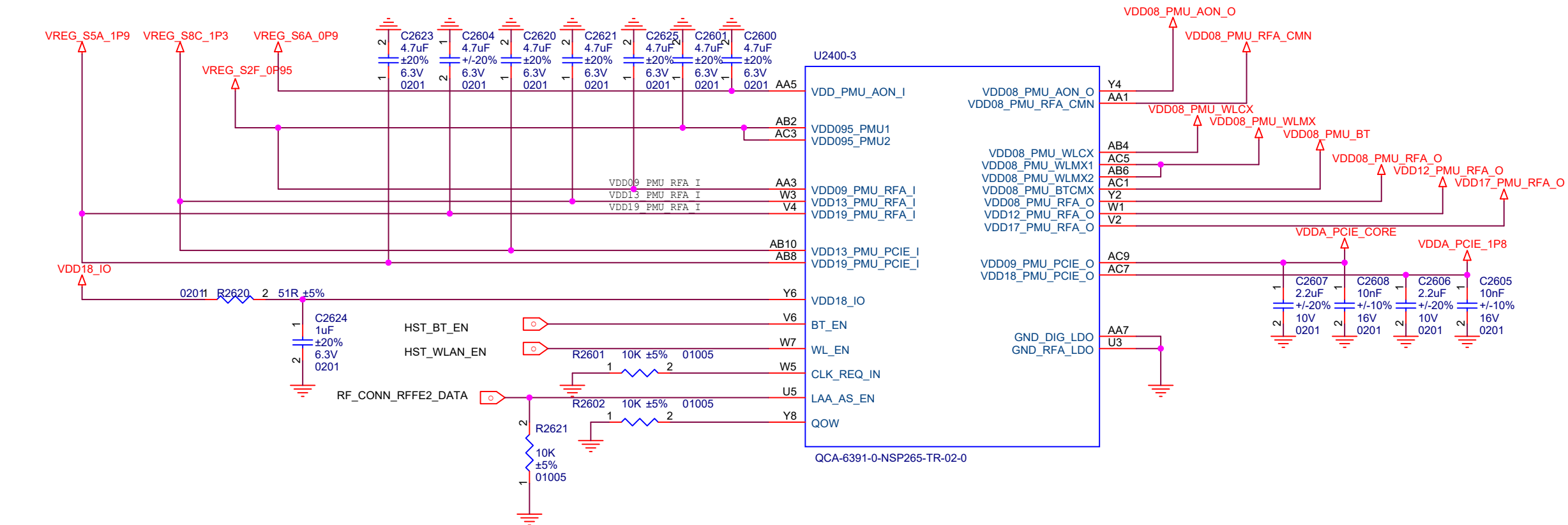
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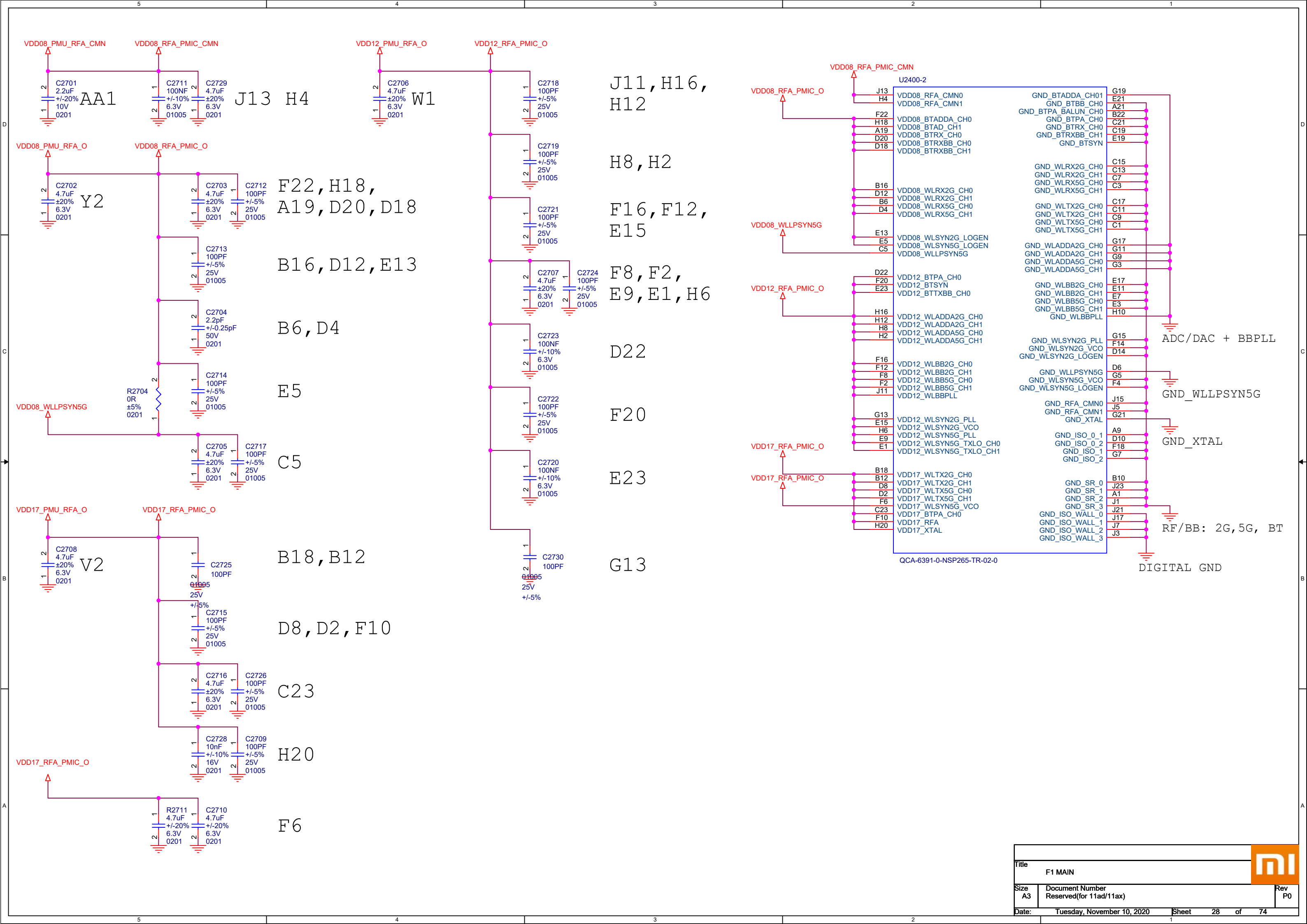


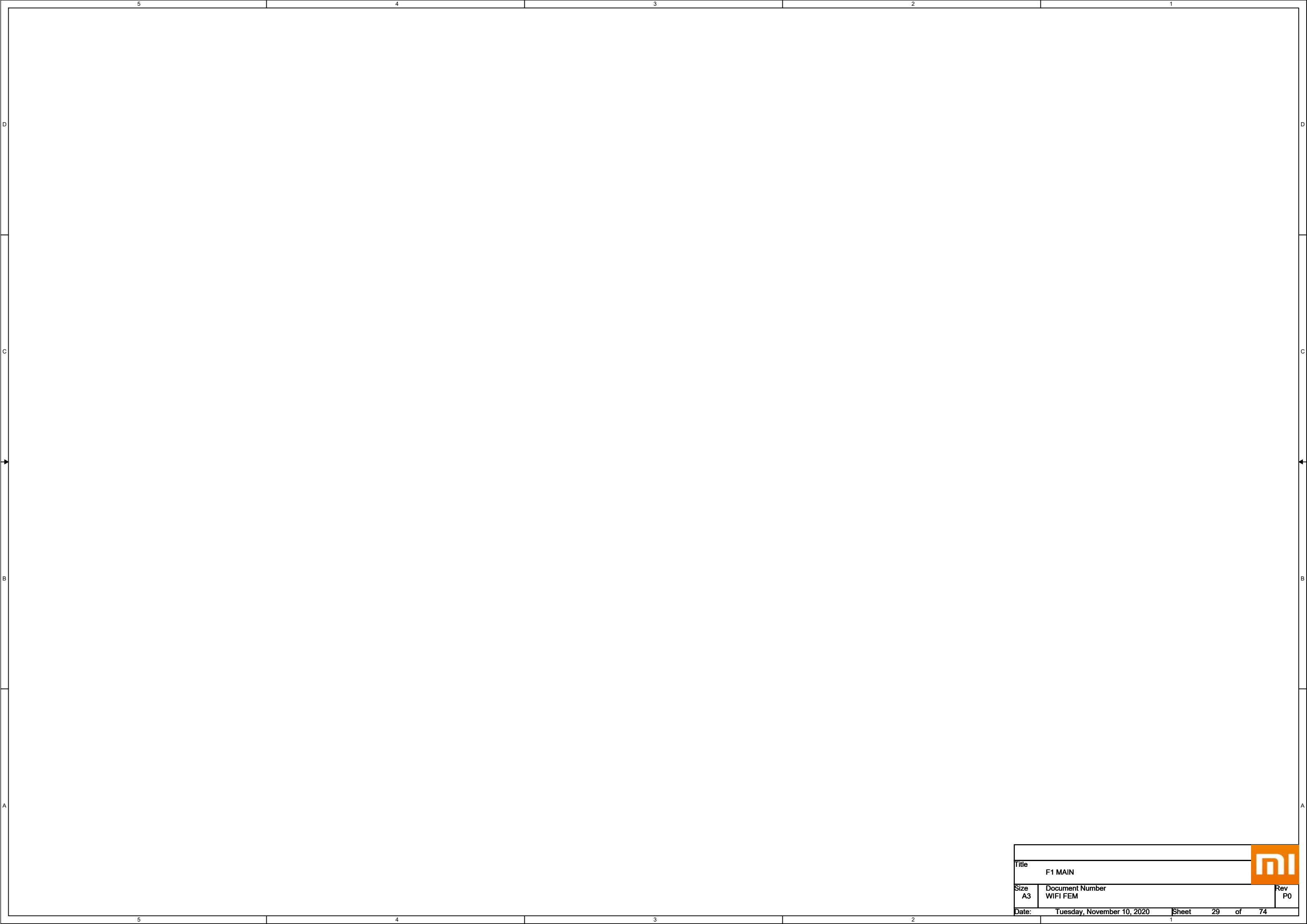





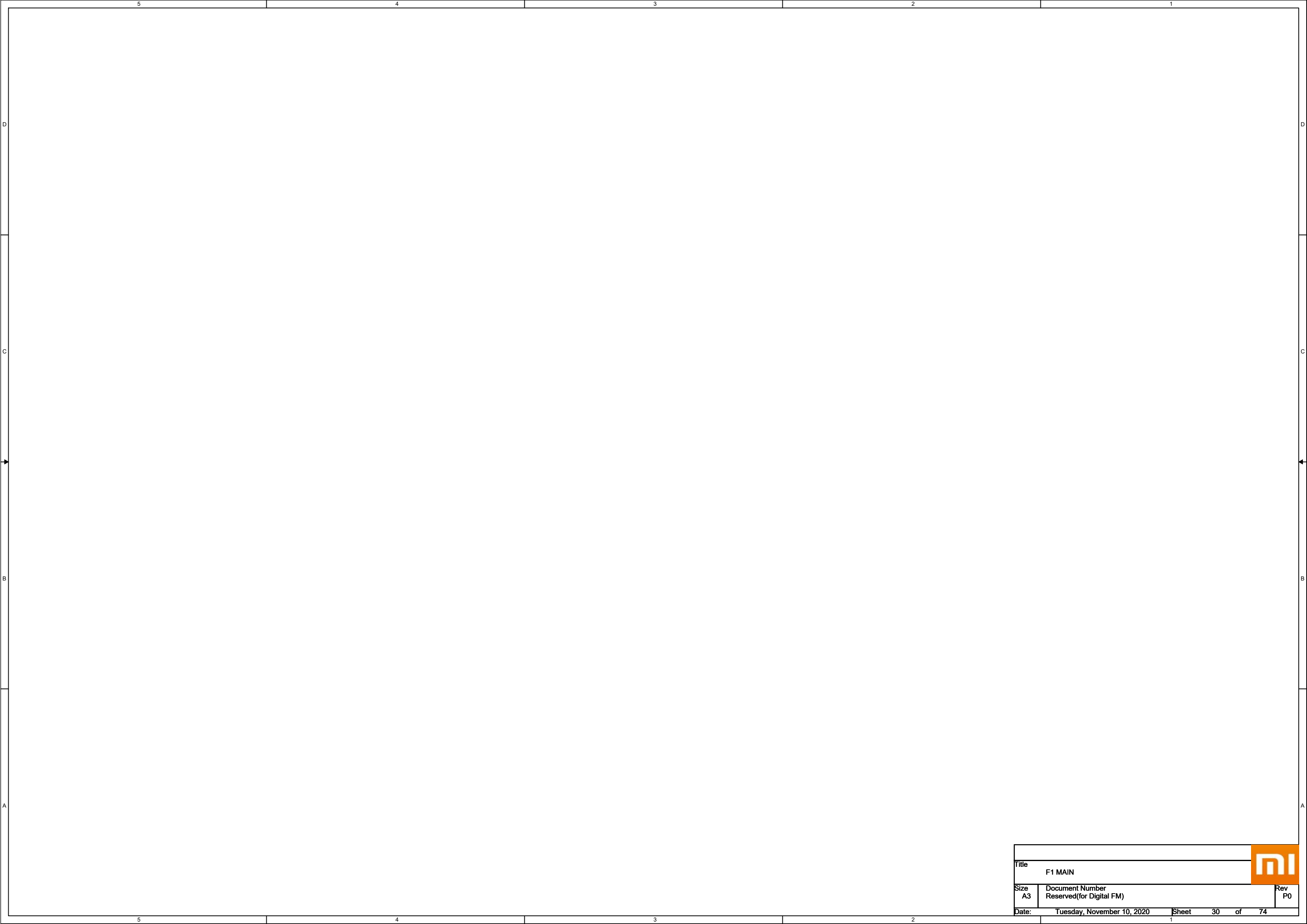
F1 MAIN		Rev P0
Doc A1	Document Number Reserved for 11ad(1ax)	
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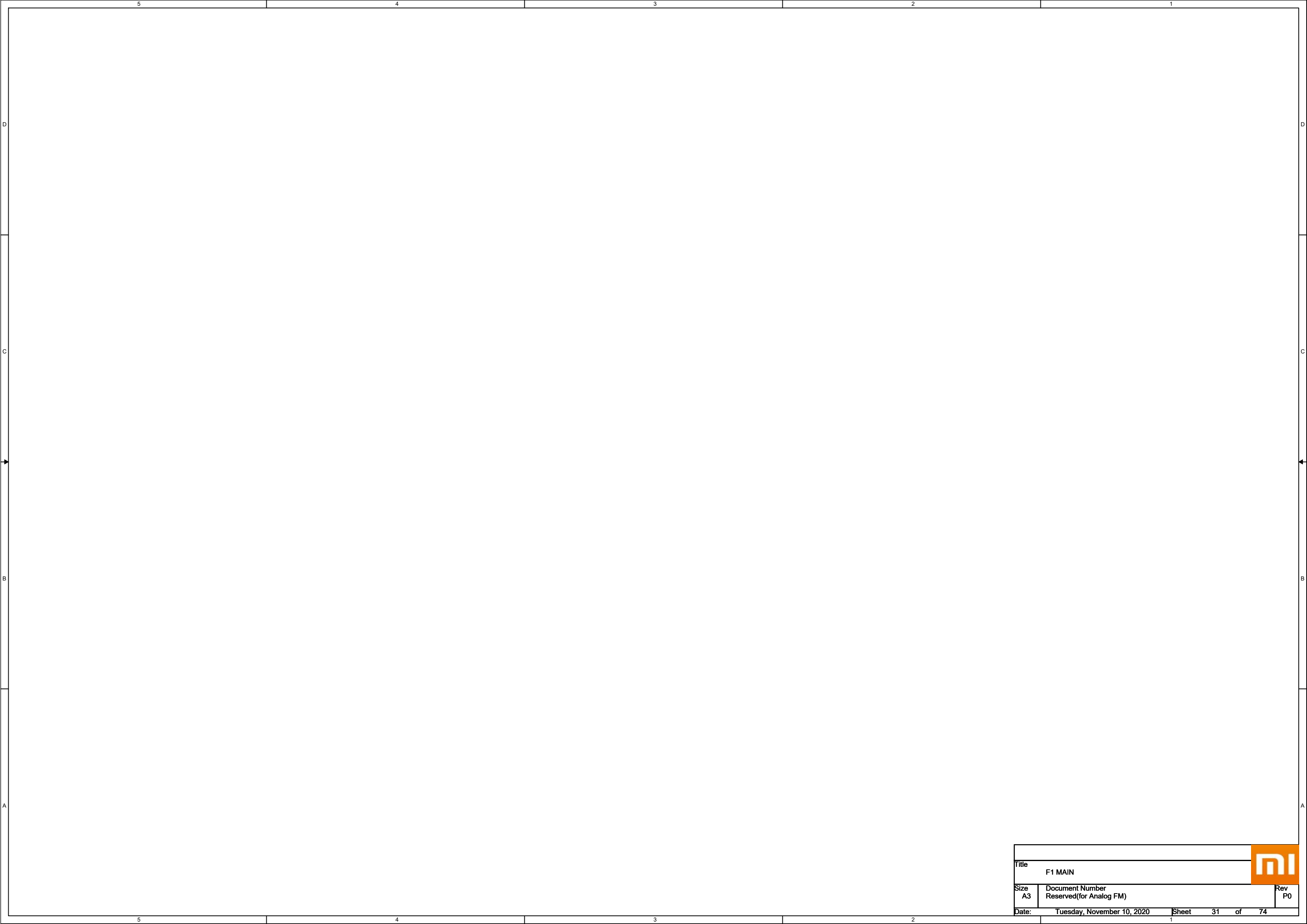





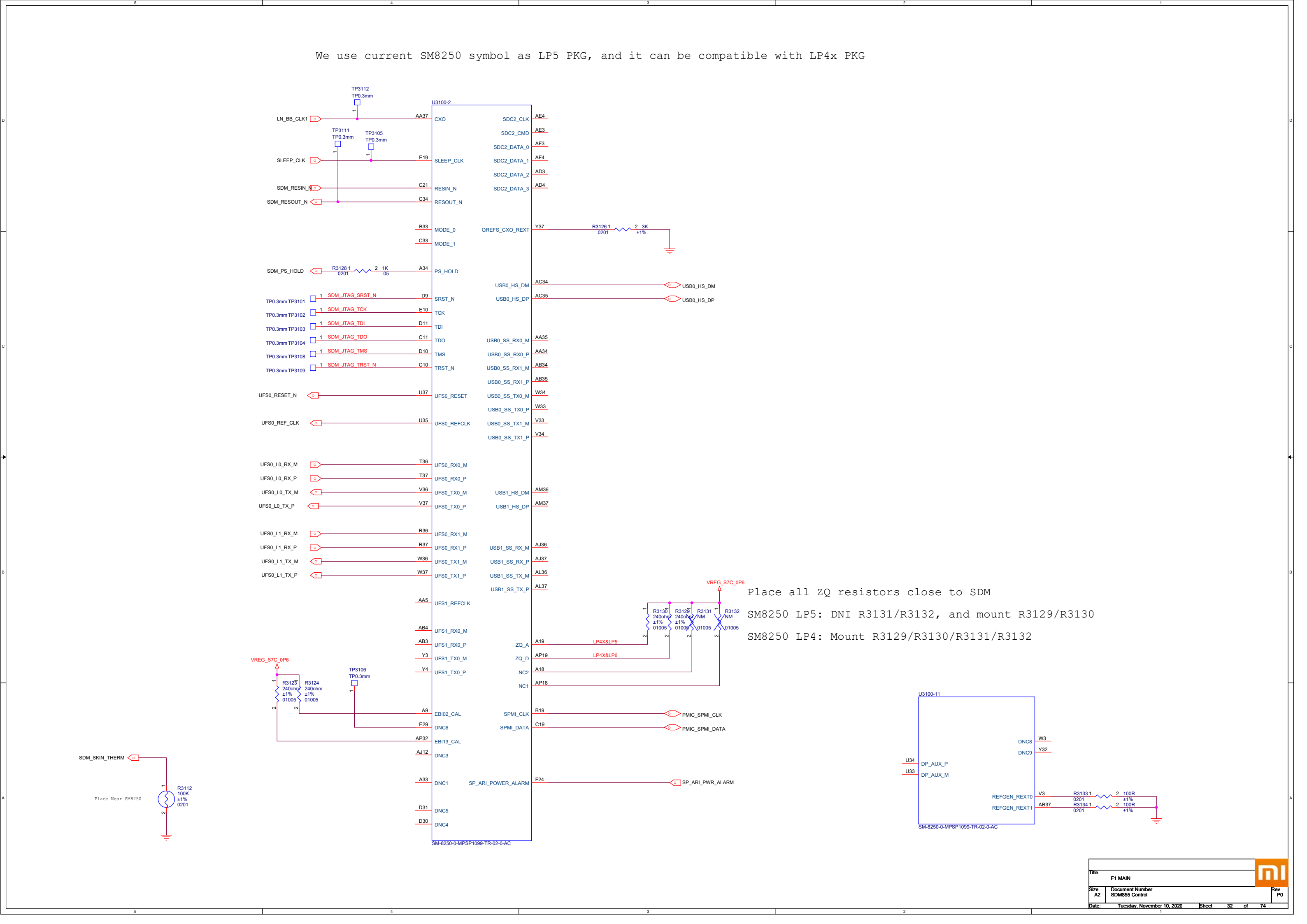
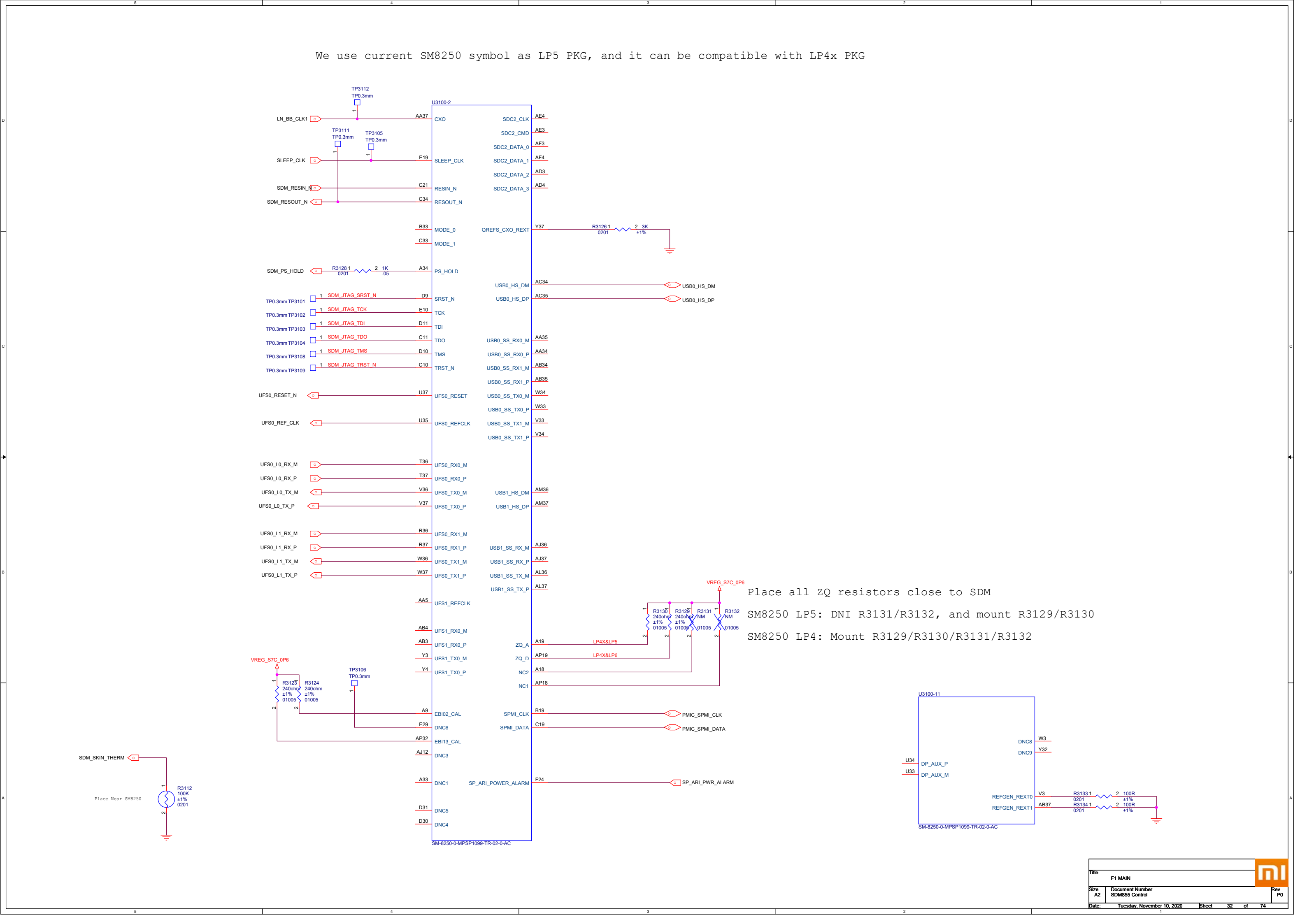
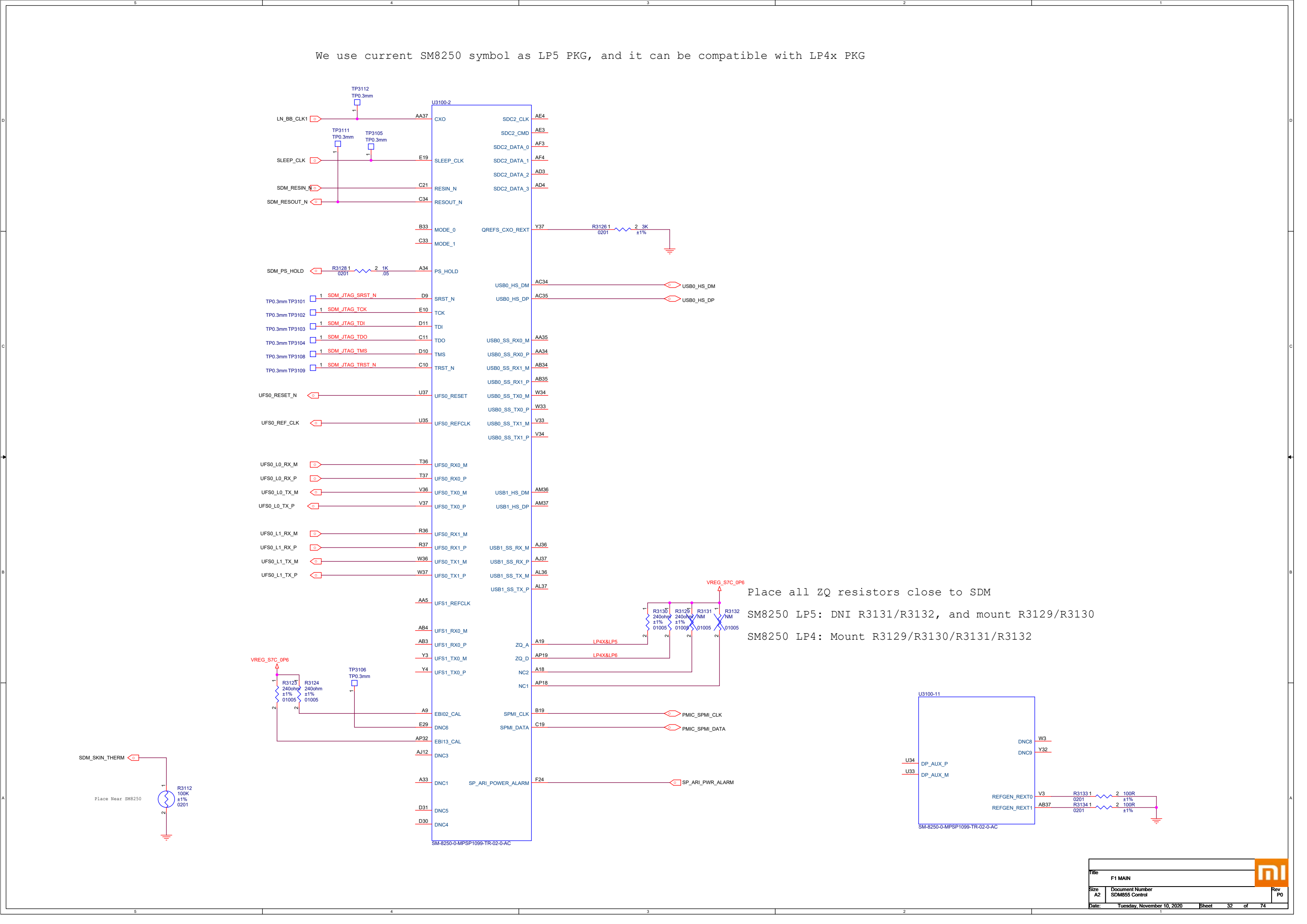


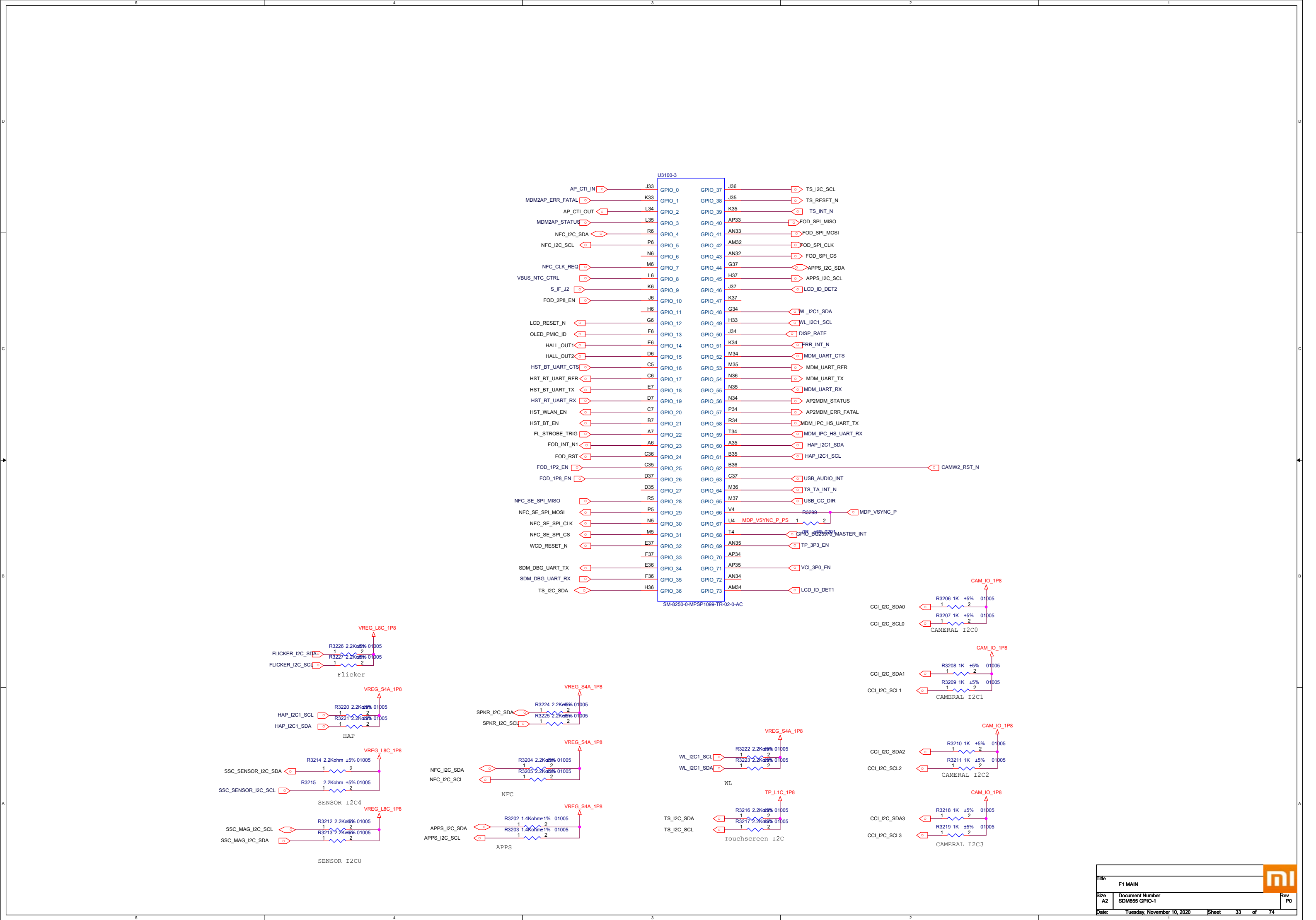
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Size	A3	Document Number	WIFI FEM	
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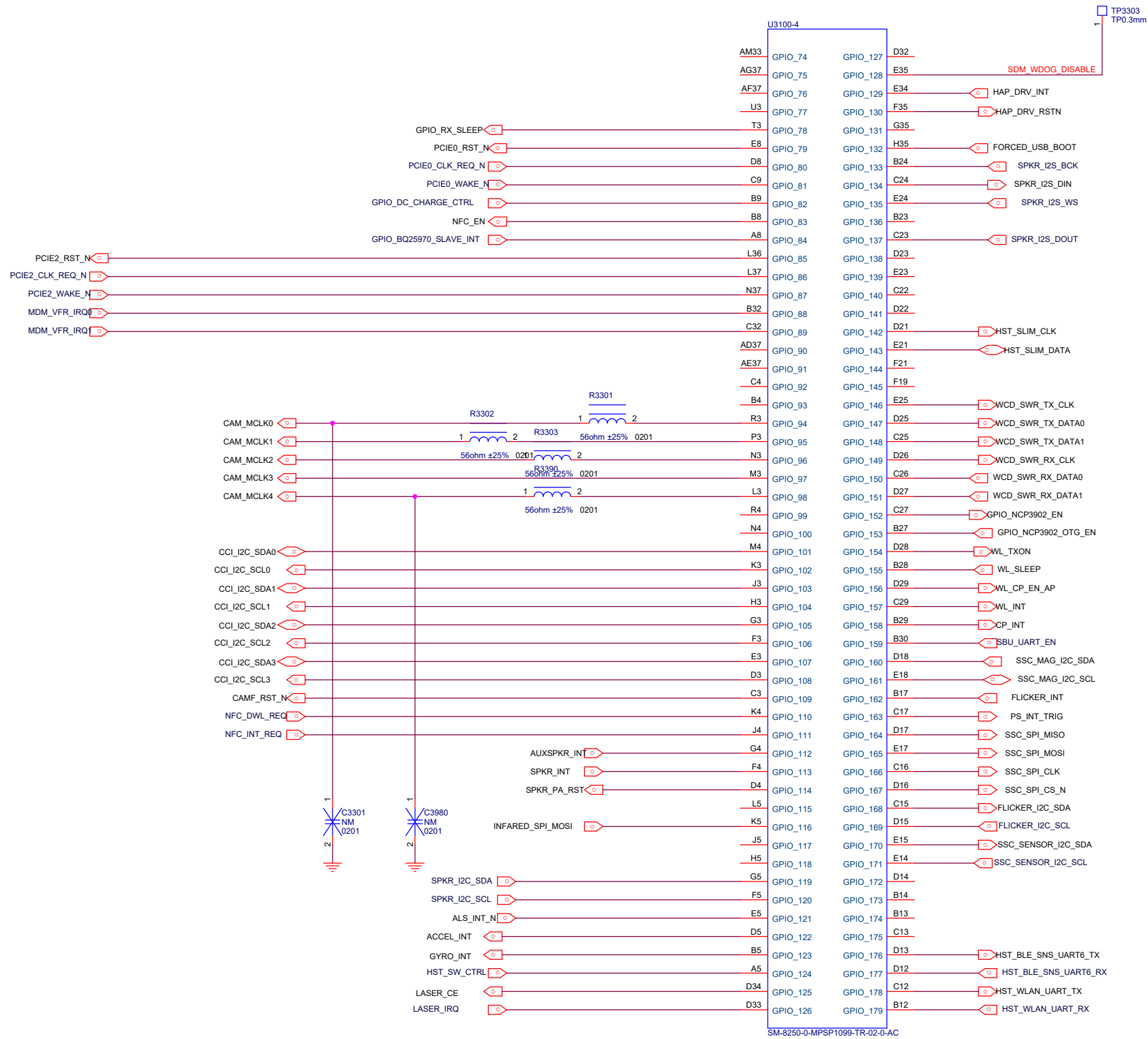


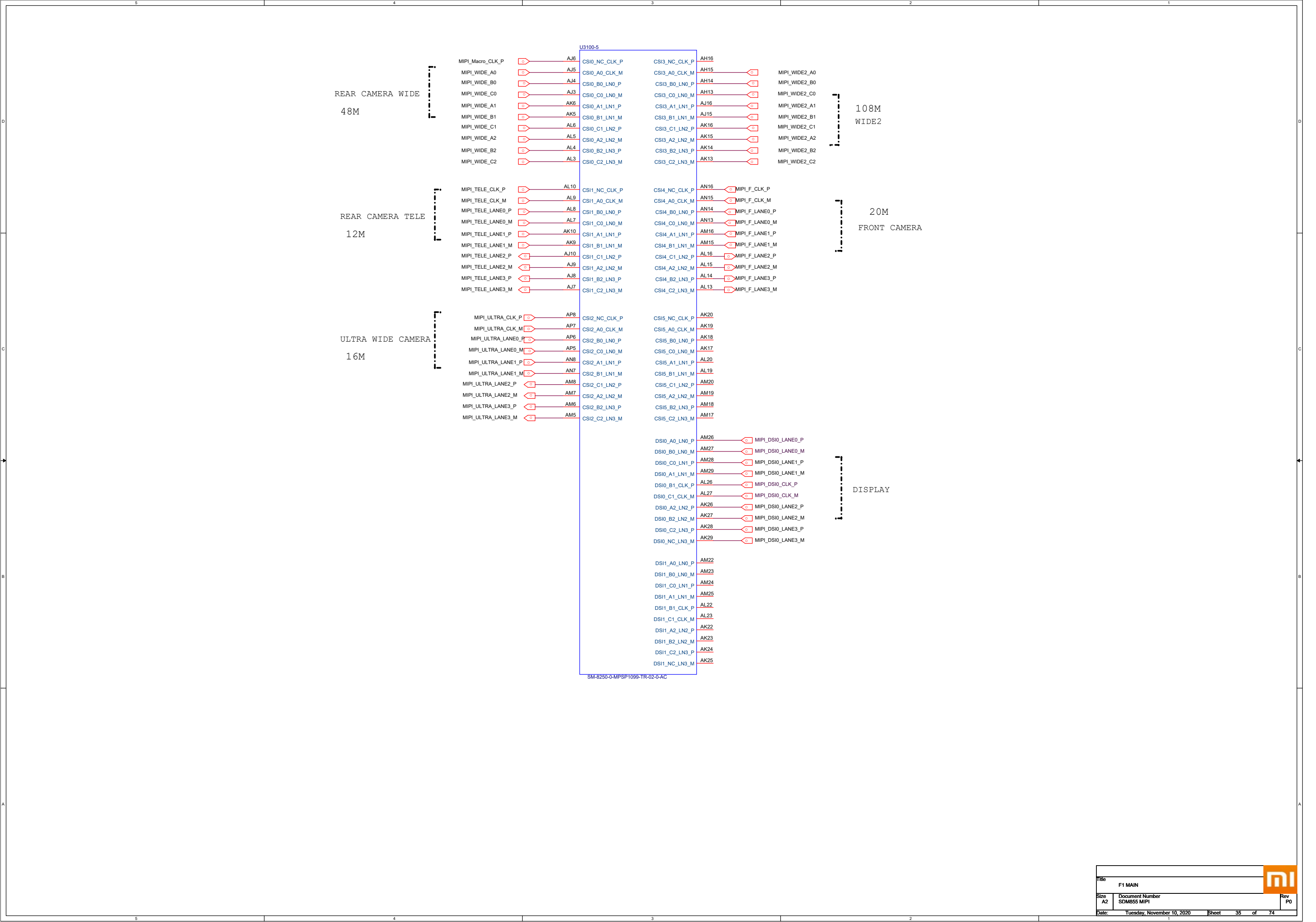


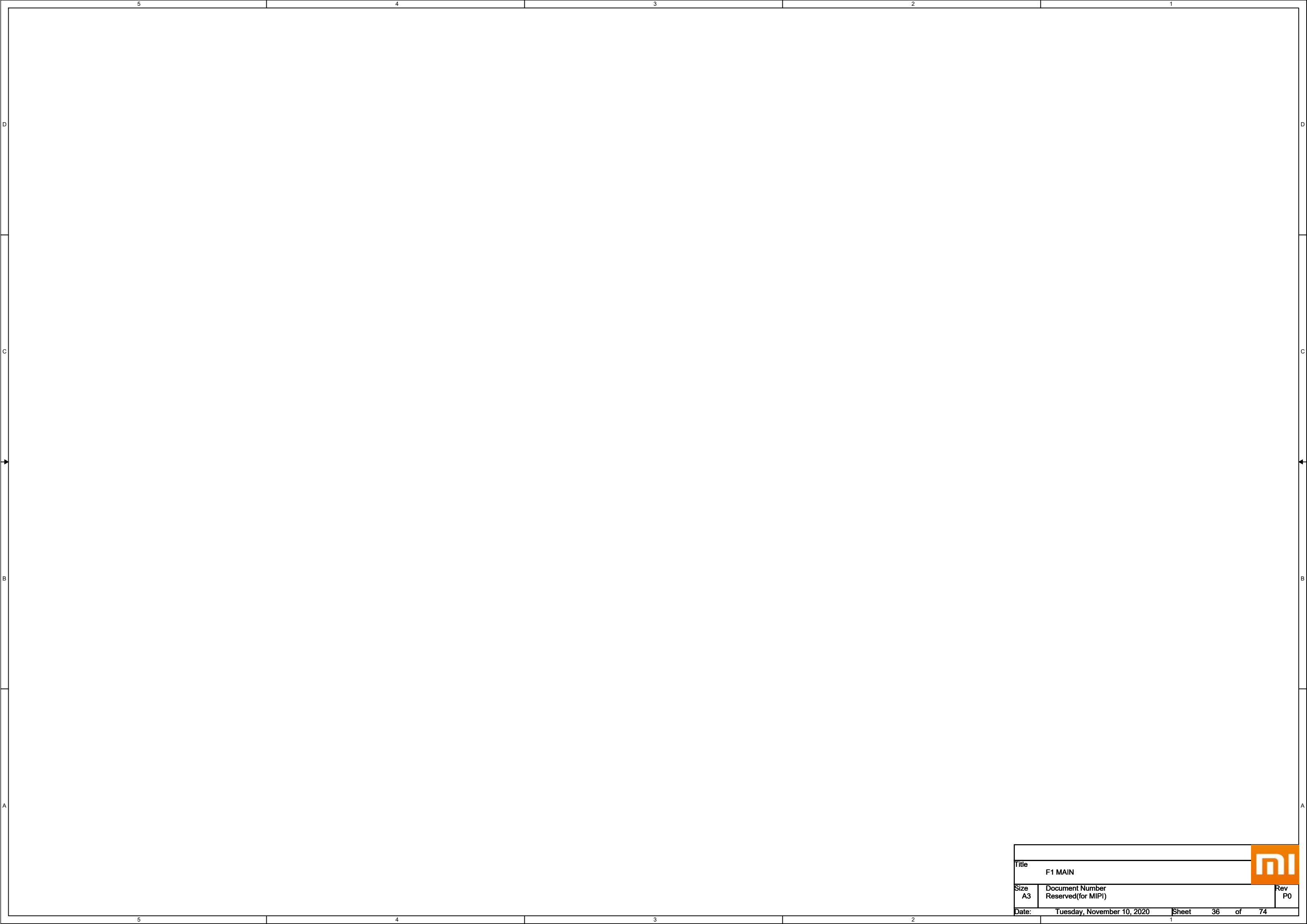
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Size	A3	Document Number	Reserved(for Analog FM)	
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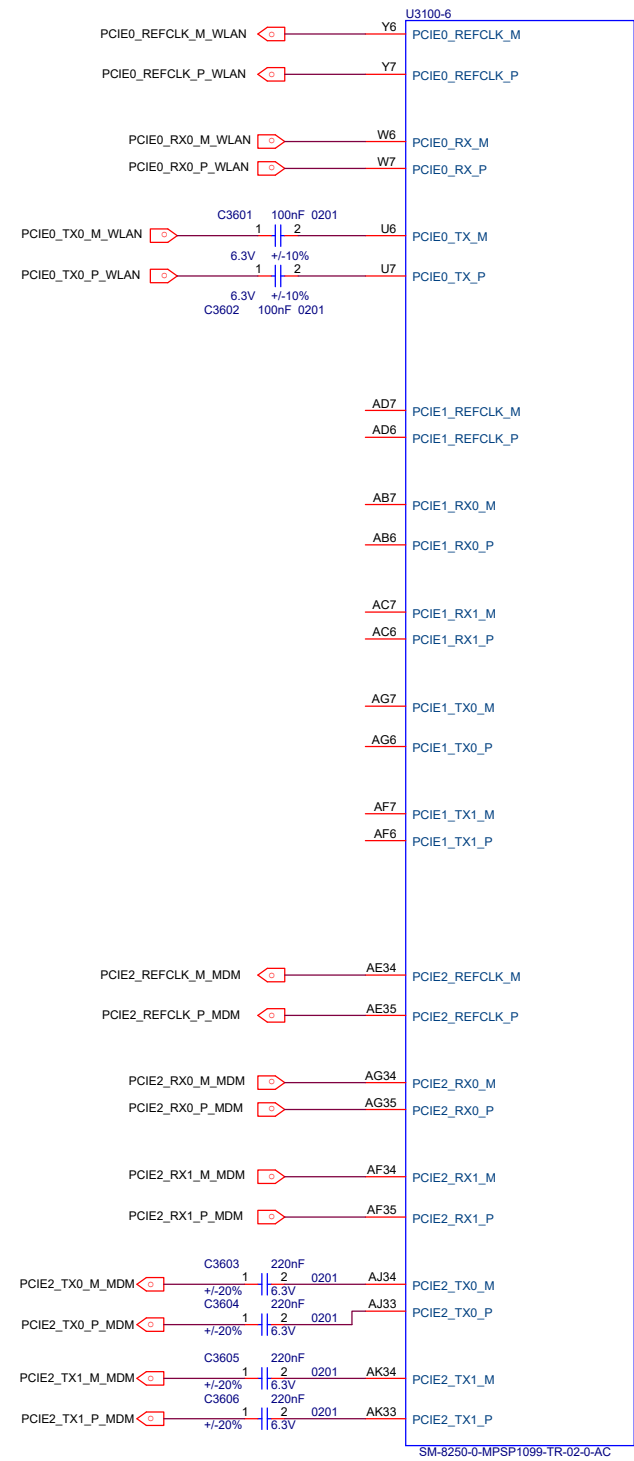
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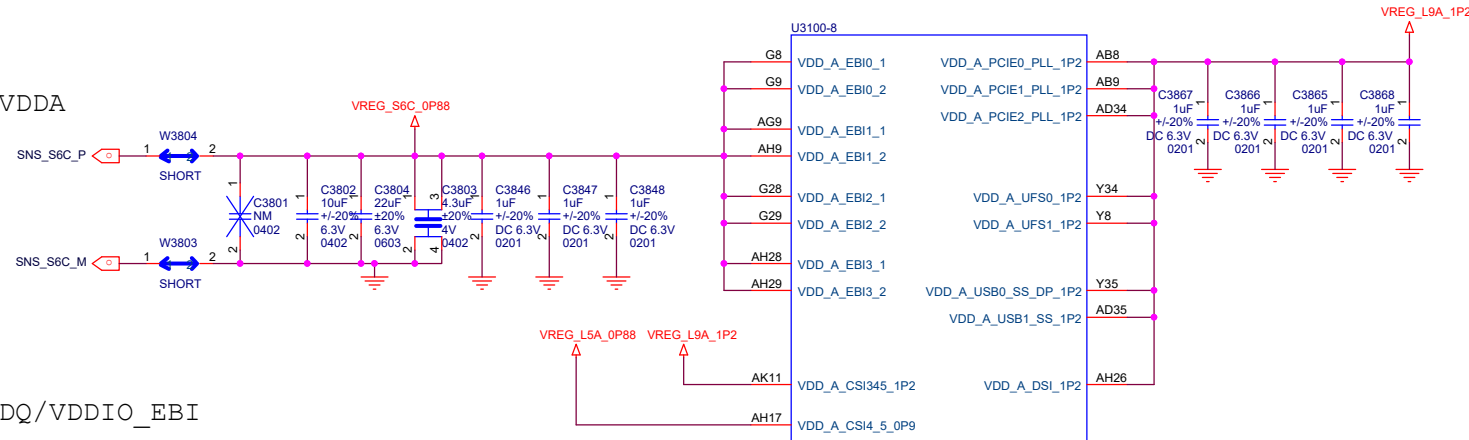




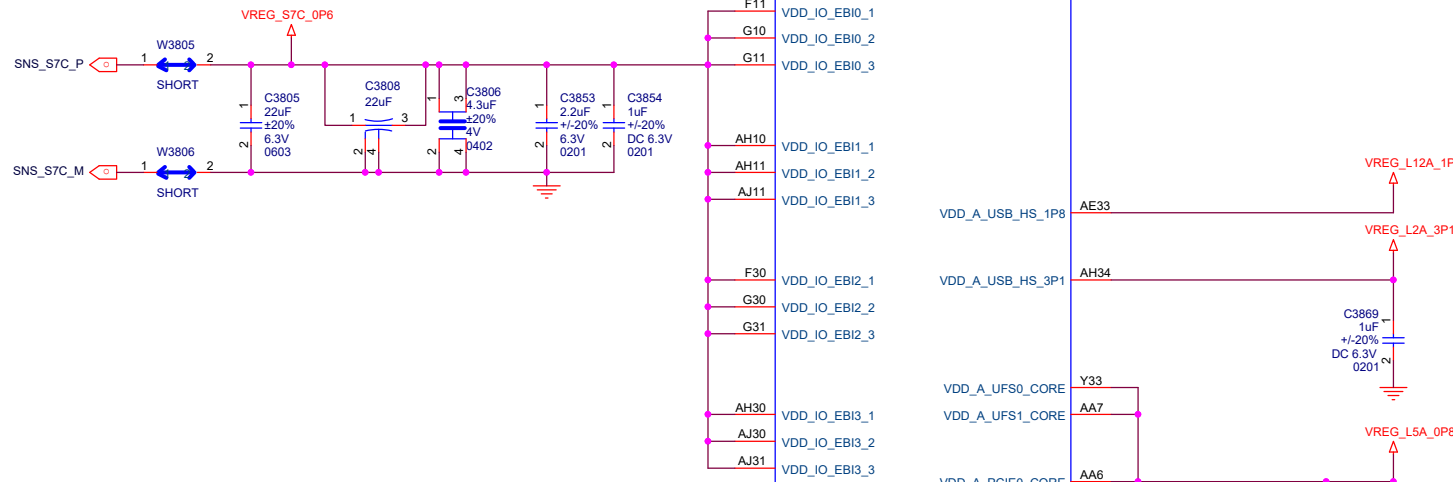




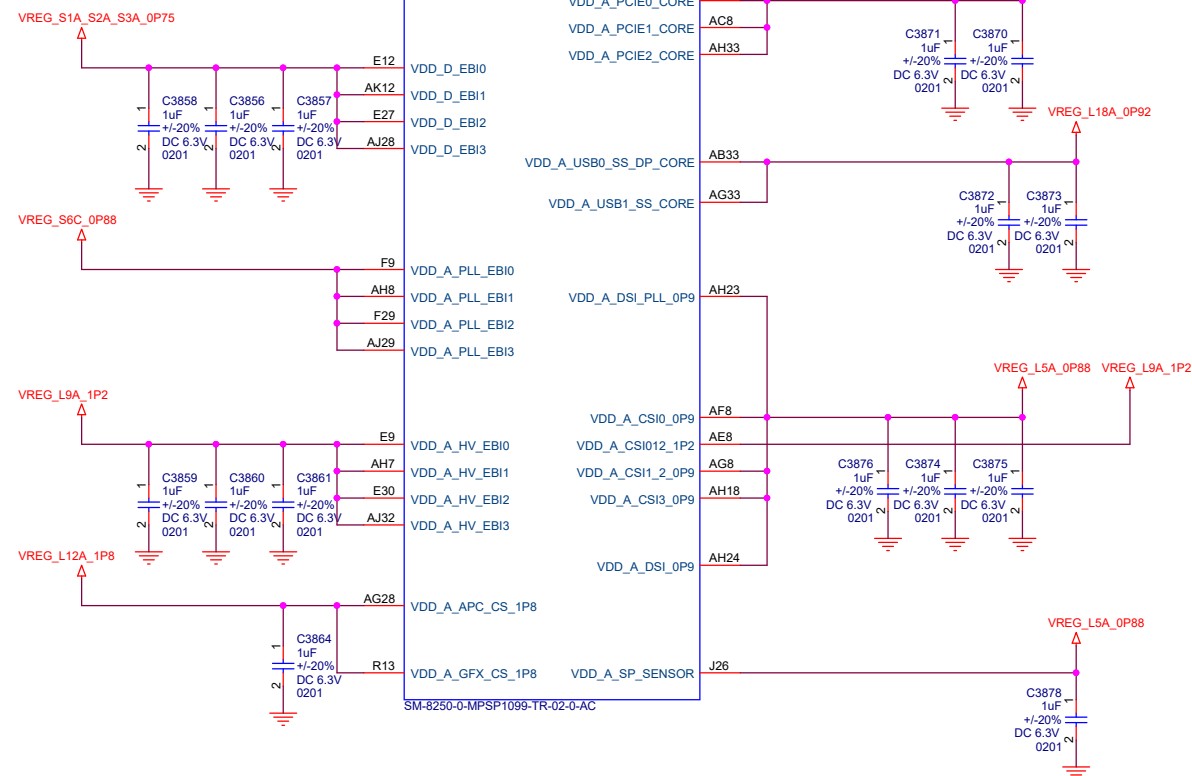
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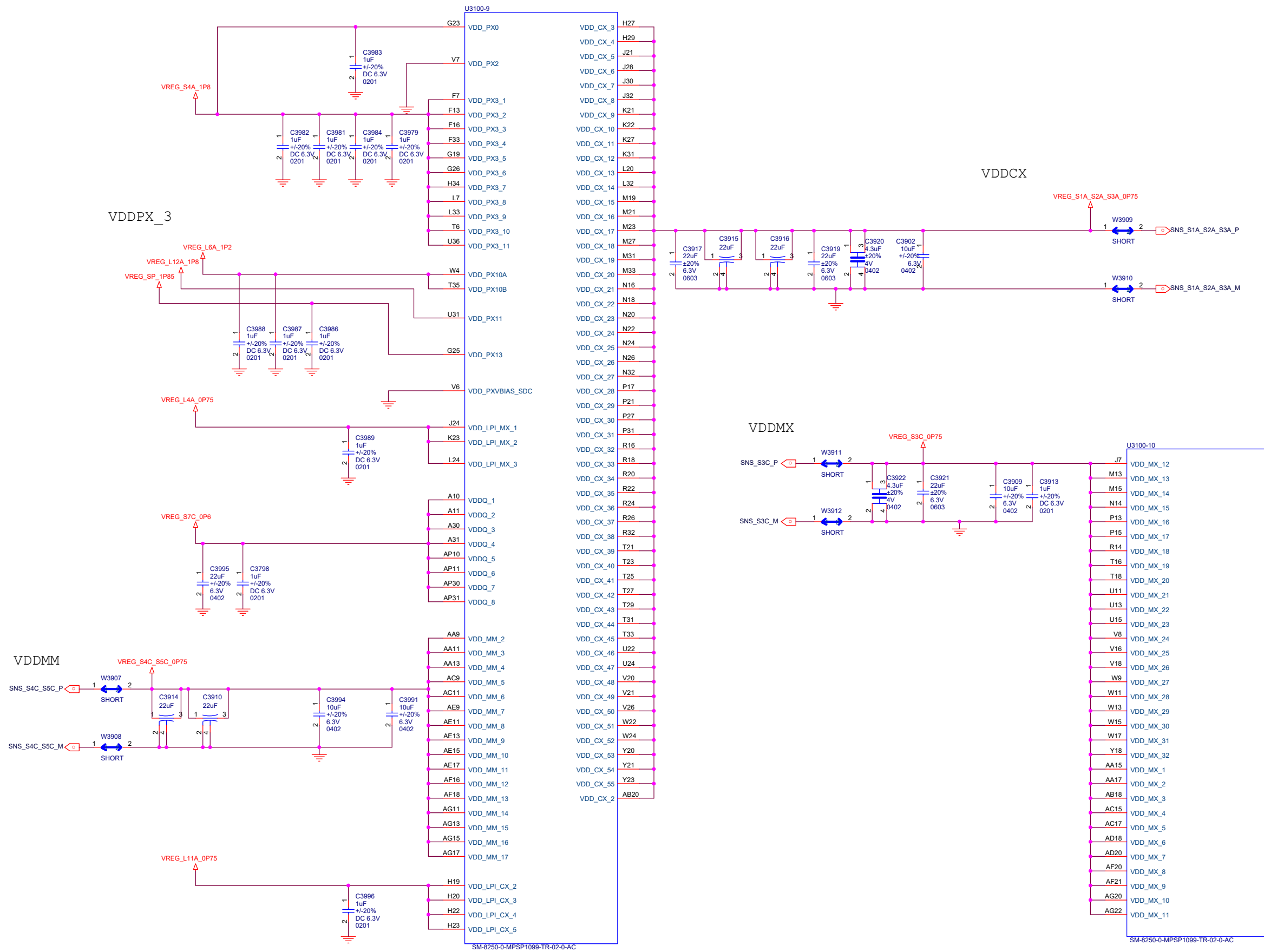


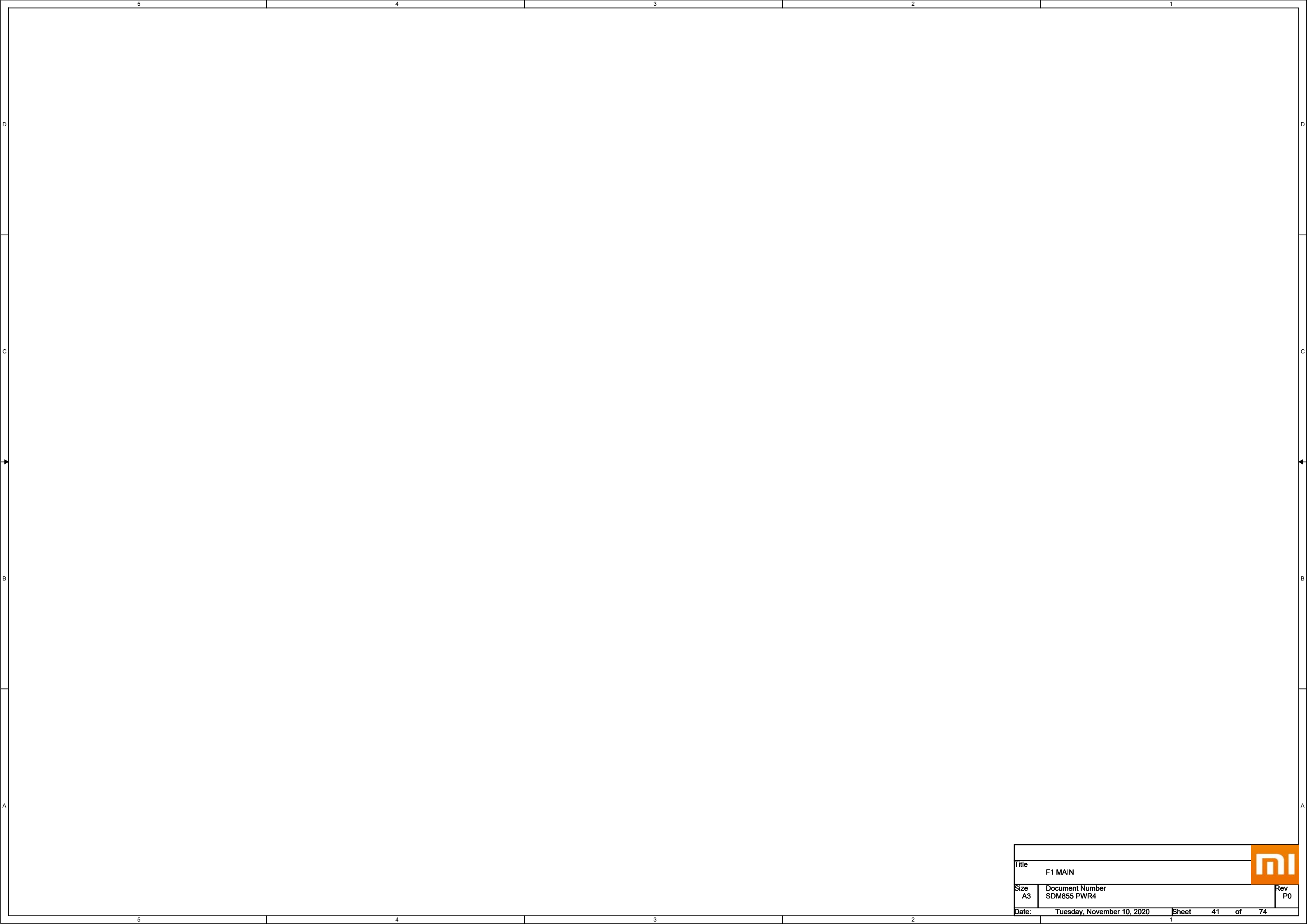
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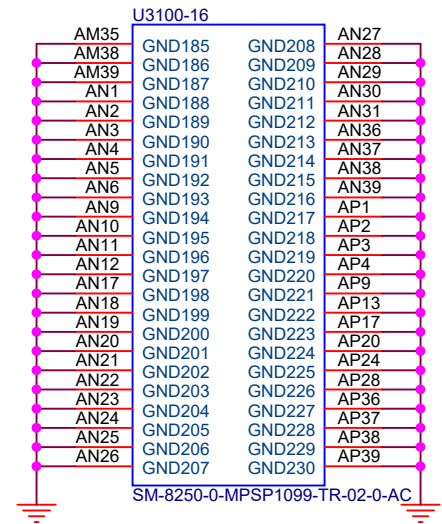
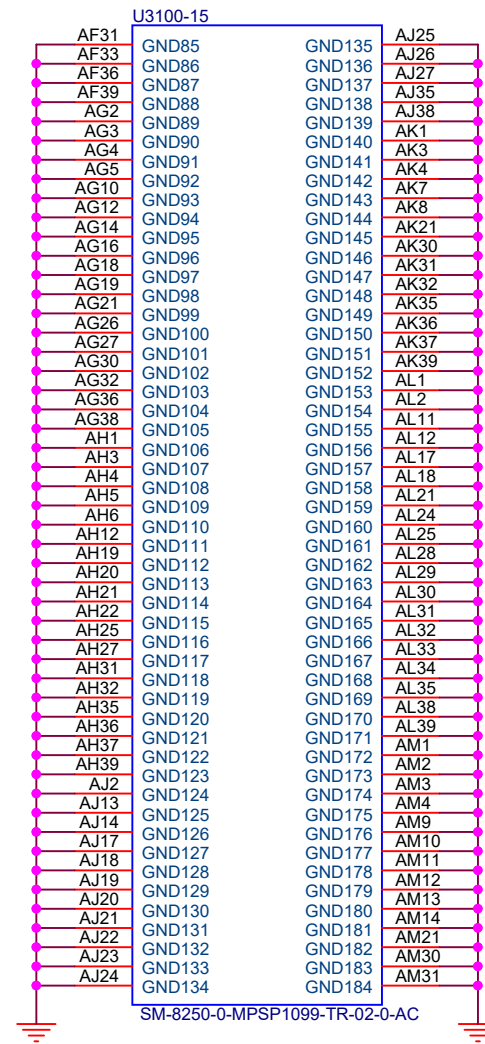
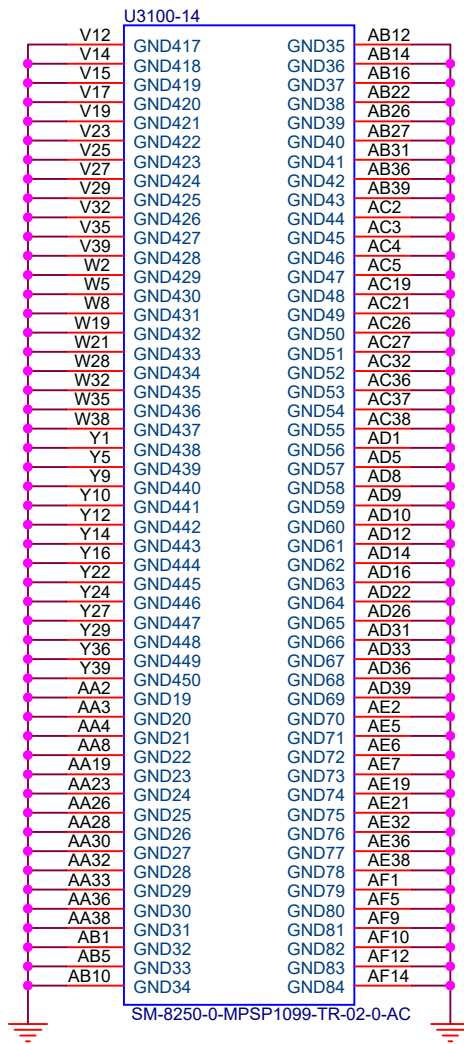
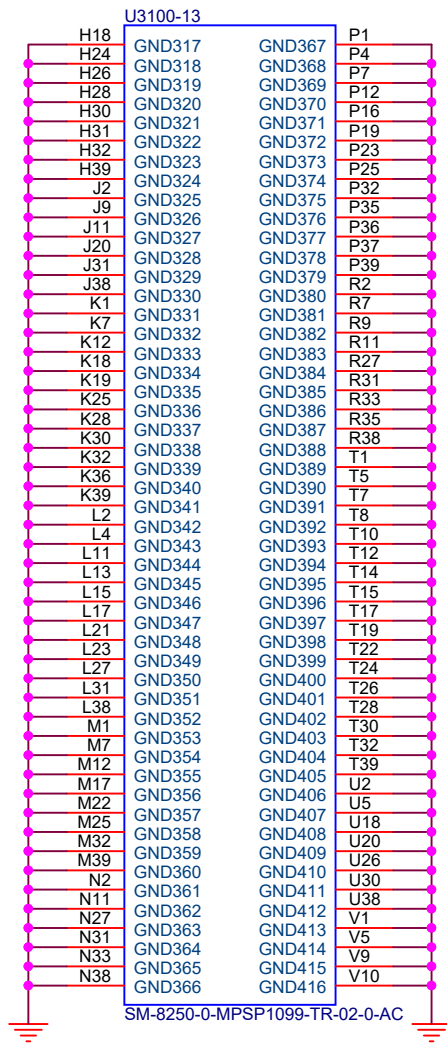
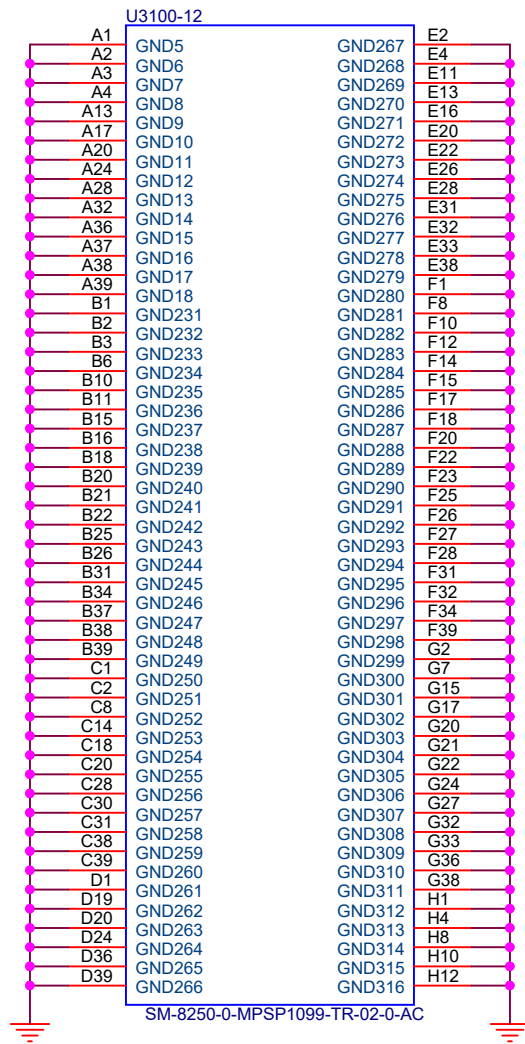
Share caps with
VDDC







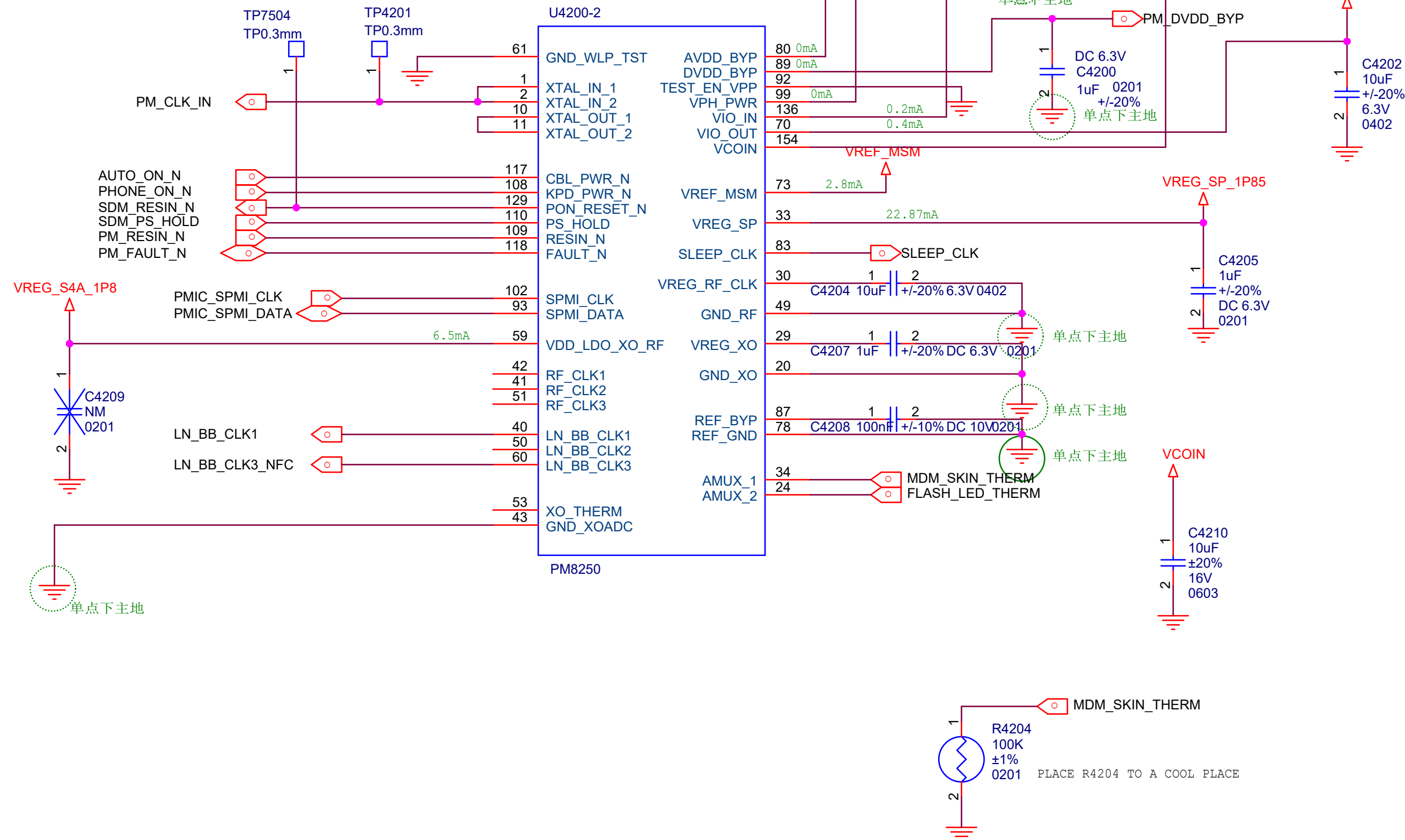
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Note: Refer to 80-VP447-10 for XO Layout App Note.

Layout Note:

No trace shall be present in at-least two immediate layers below XTAL.
Route XTAL_IN and XTAL_OUT traces on surface layer only, with length
between 3mm and 10mm.




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电容下主地

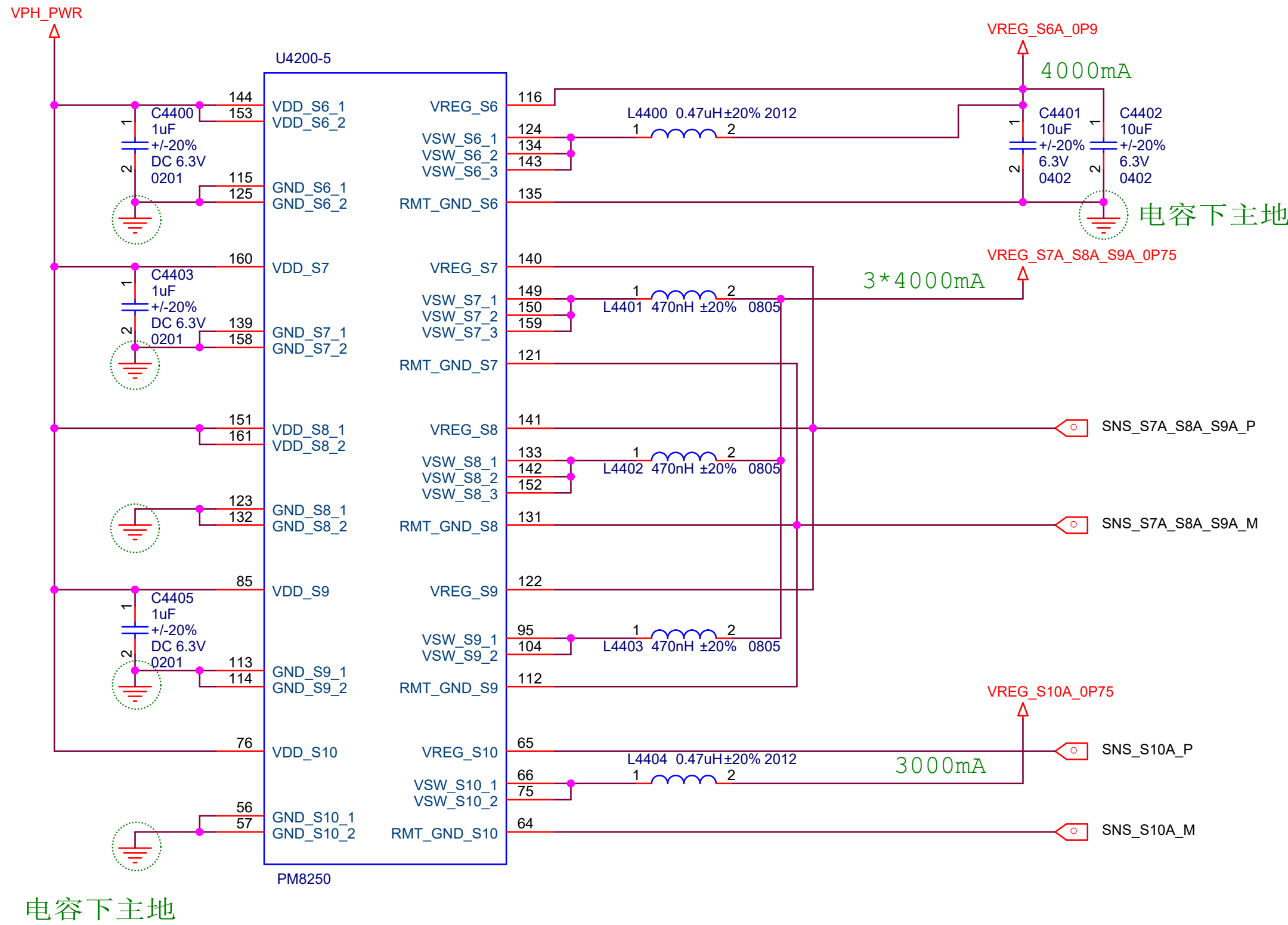
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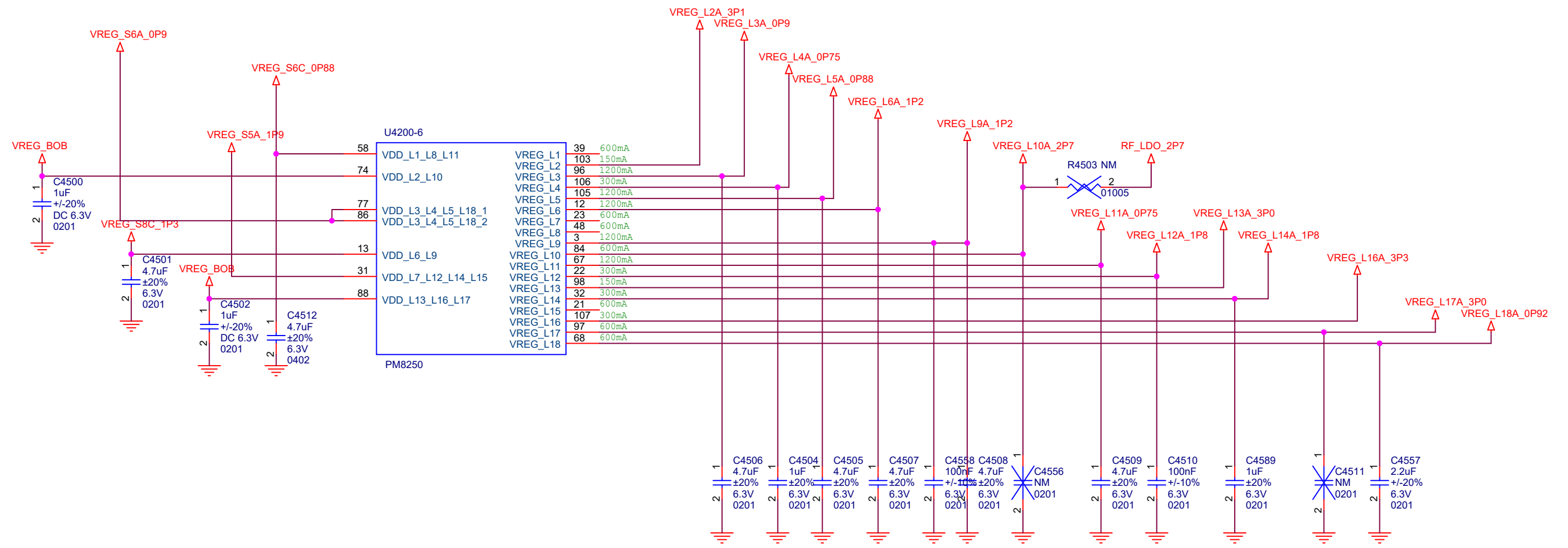
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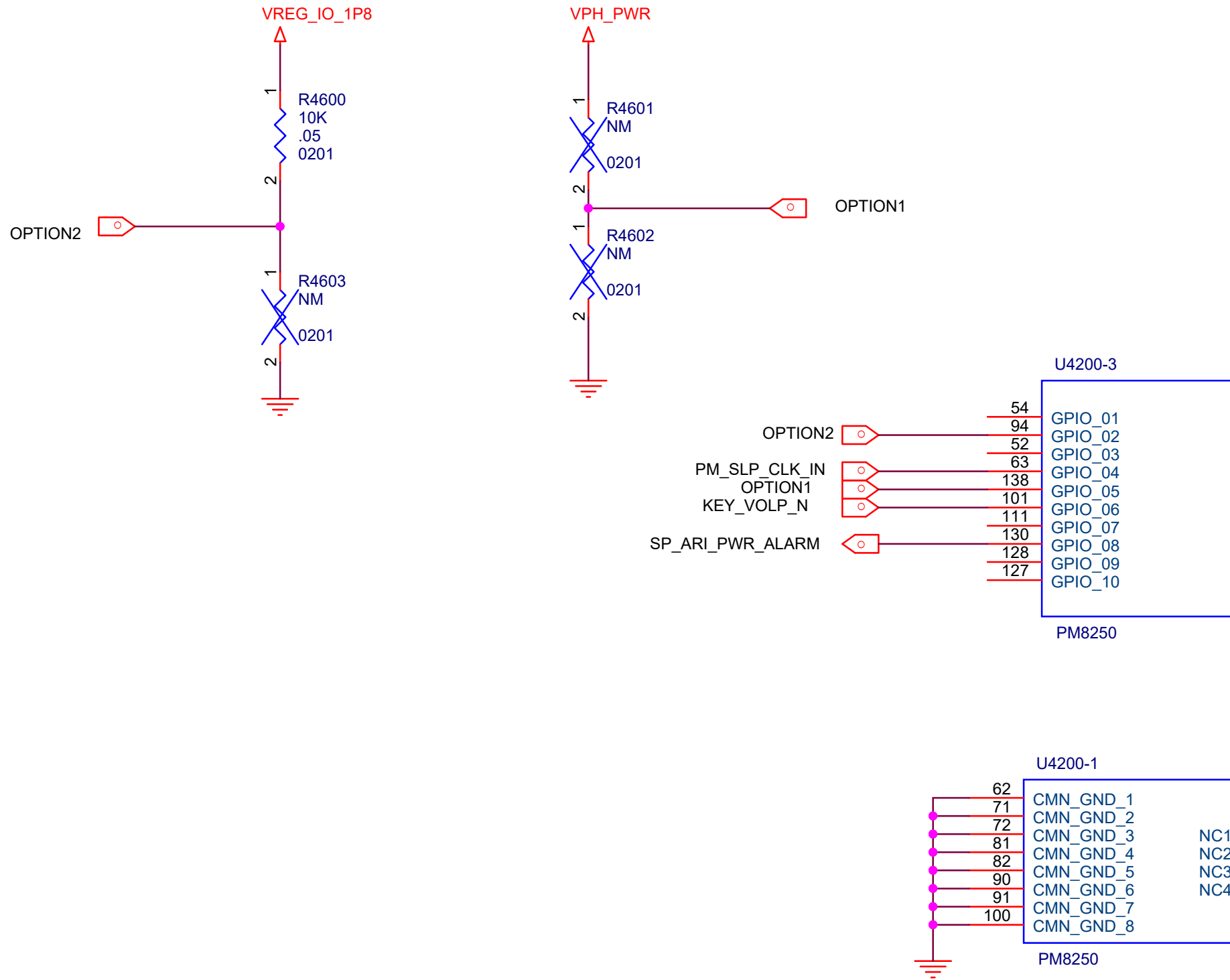
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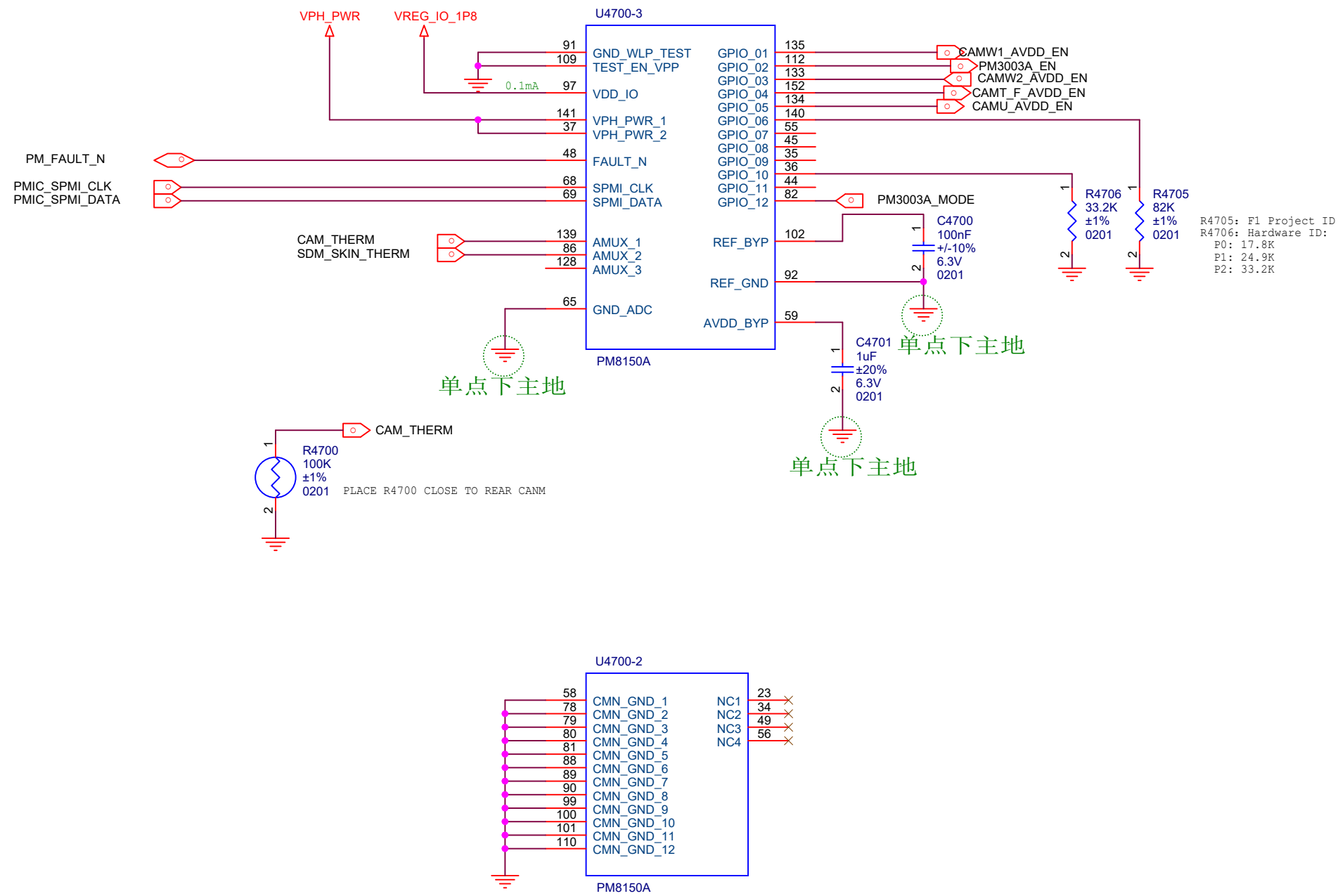
					
Title F1 MAIN					
Size A4	Document Number PM855 BUCK CONVERTER1				Rev P0
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2		1			

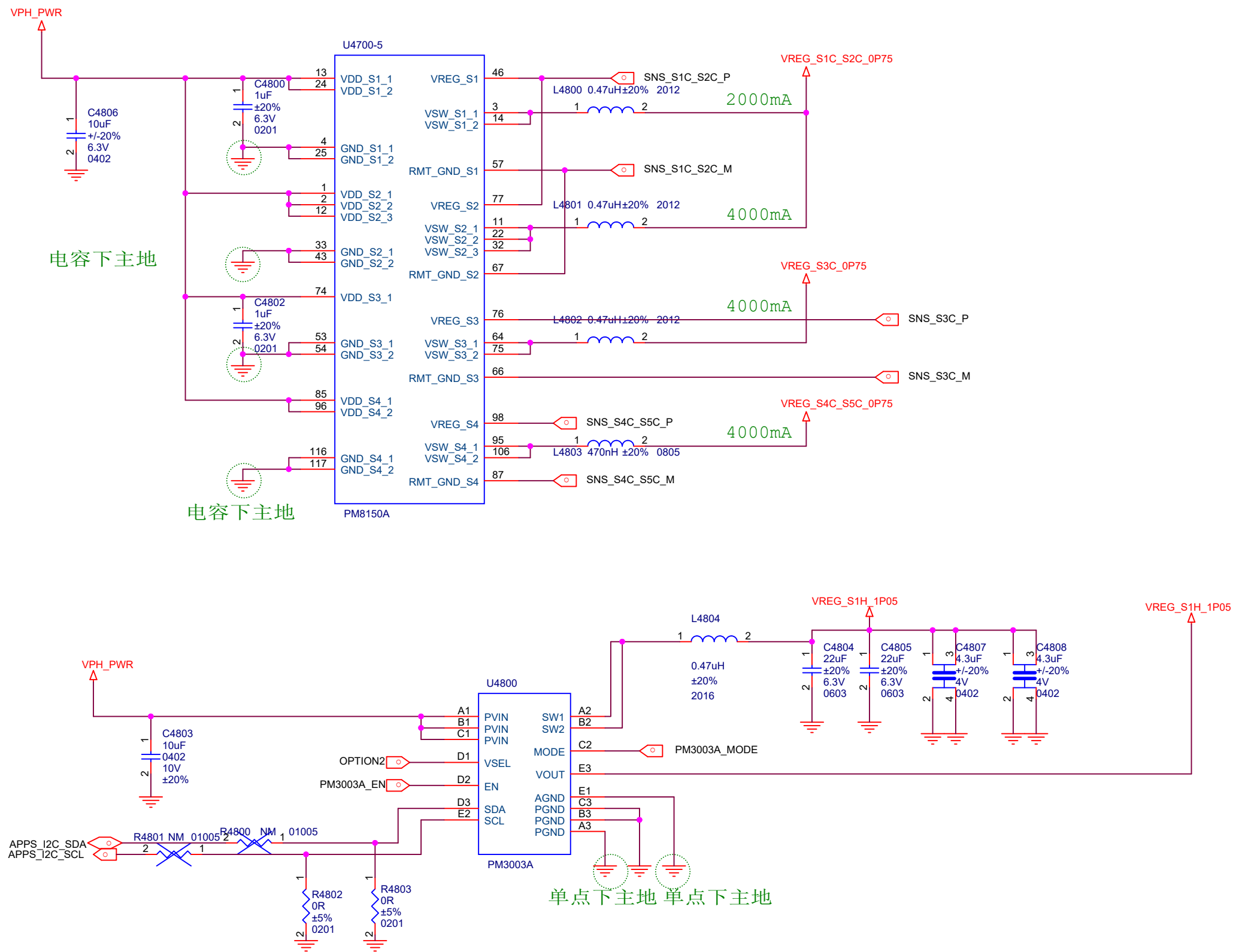
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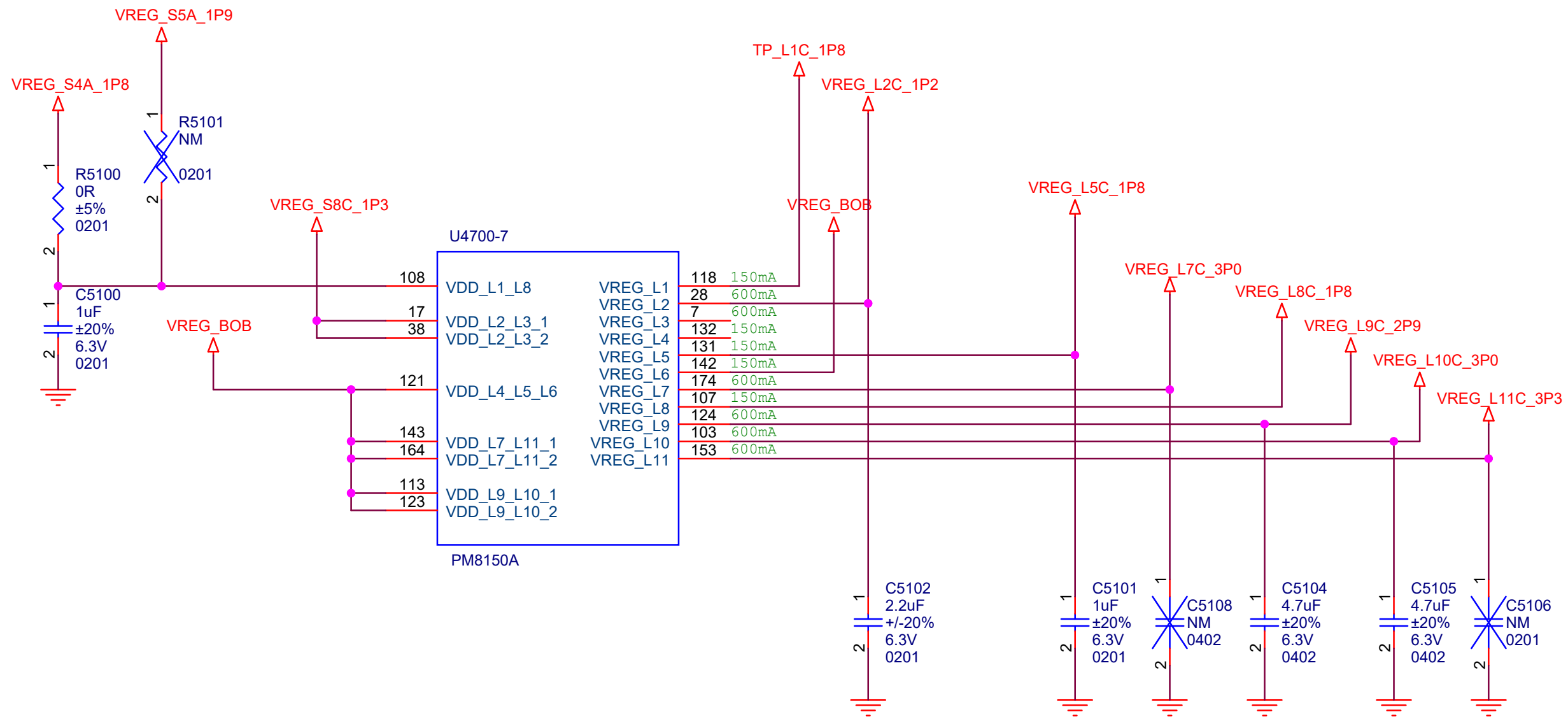


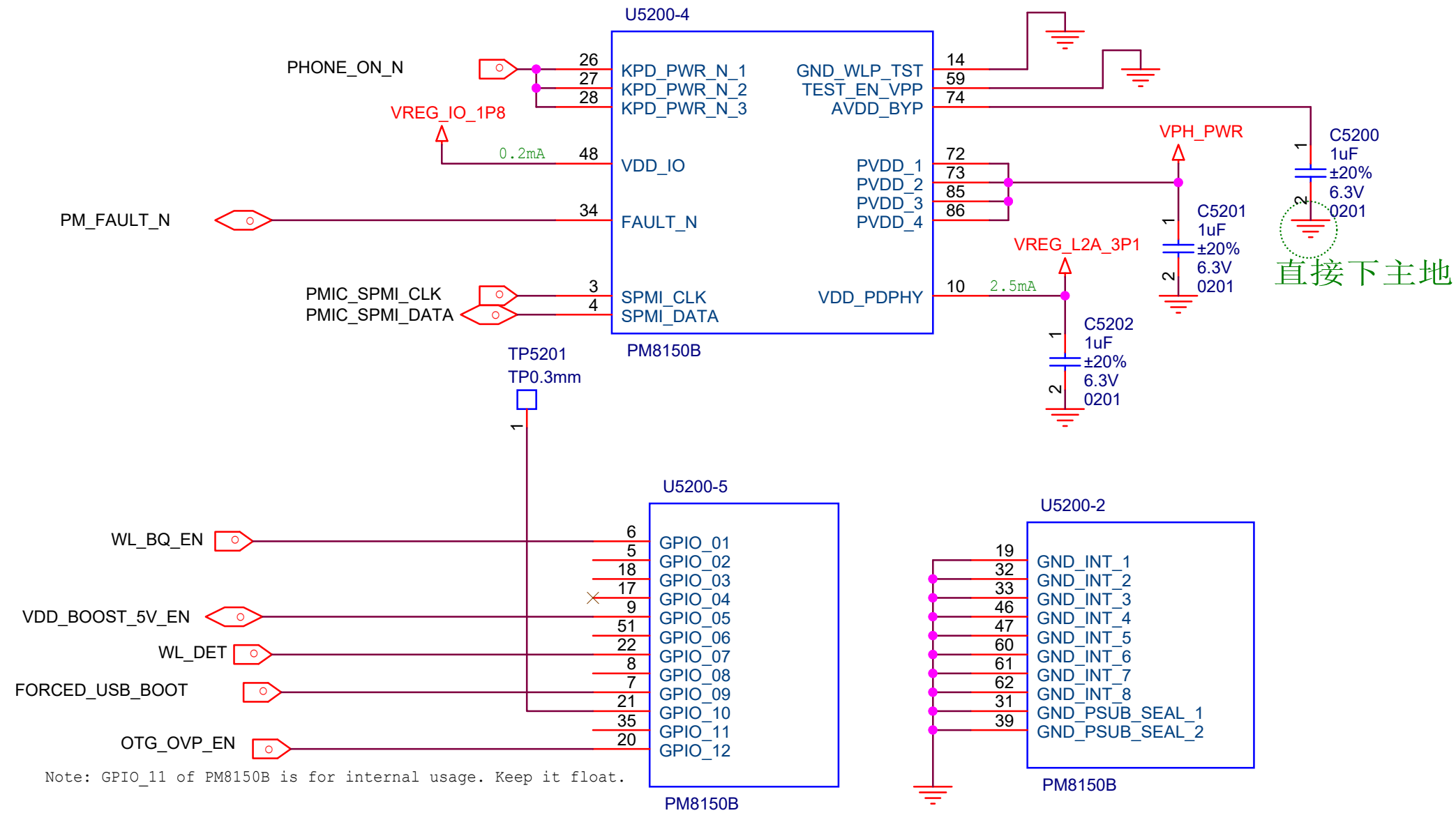
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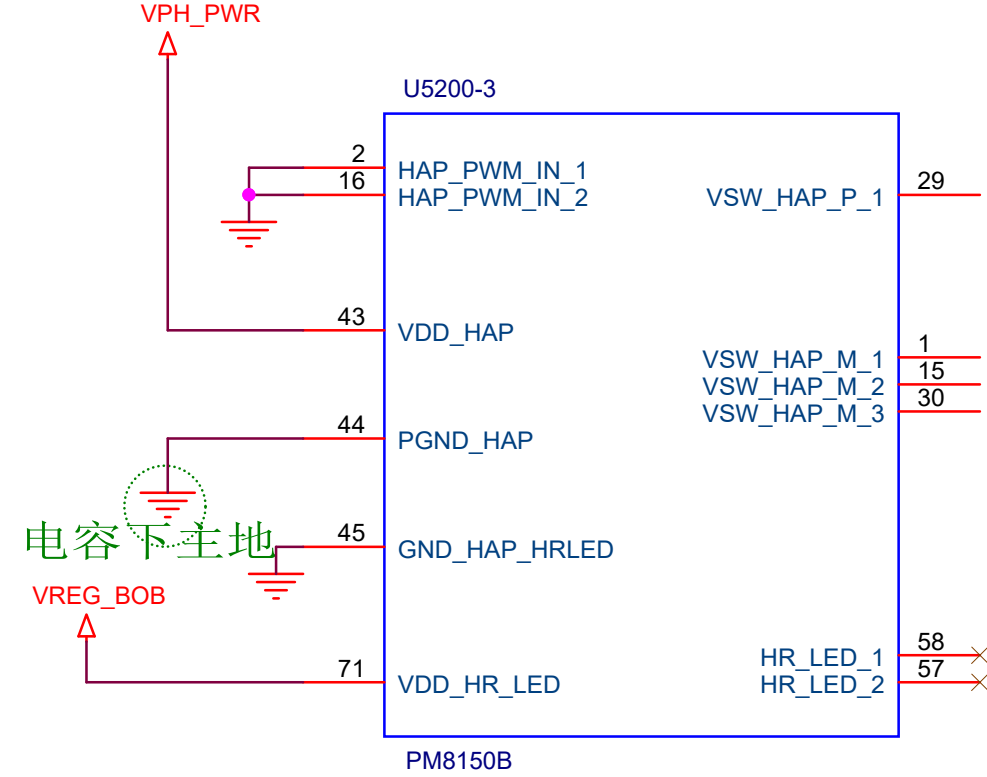
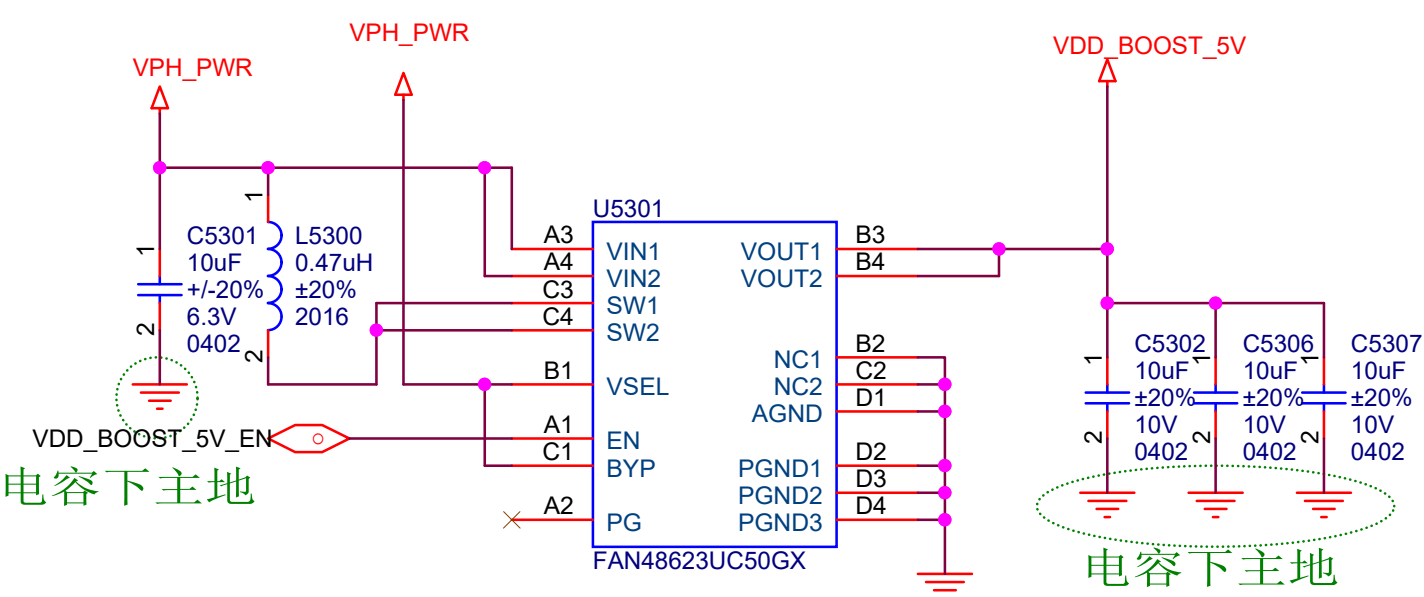
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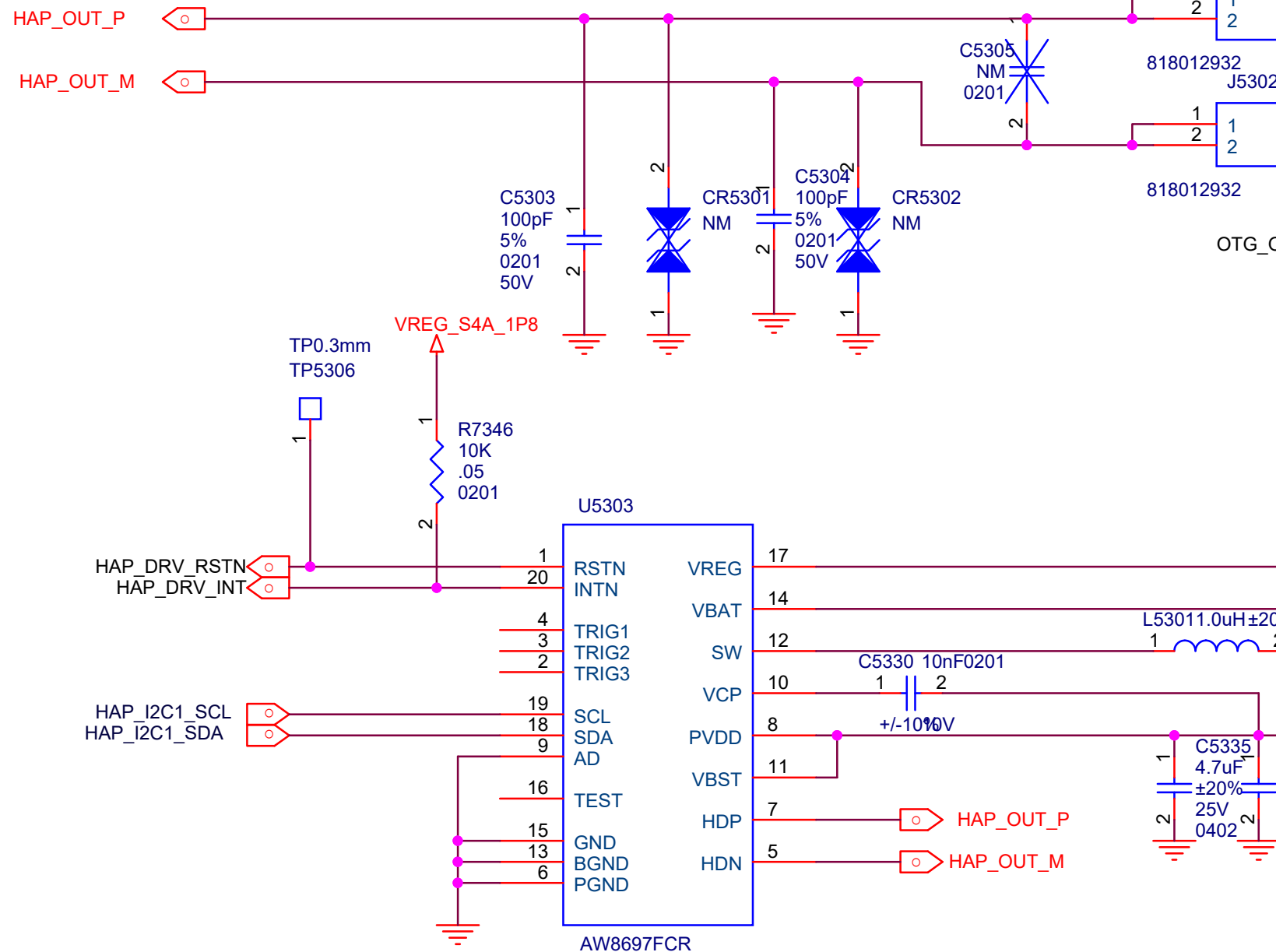
					
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Size	Document Number				Rev
A4	PM855A BUCK CONVERTER2				P0
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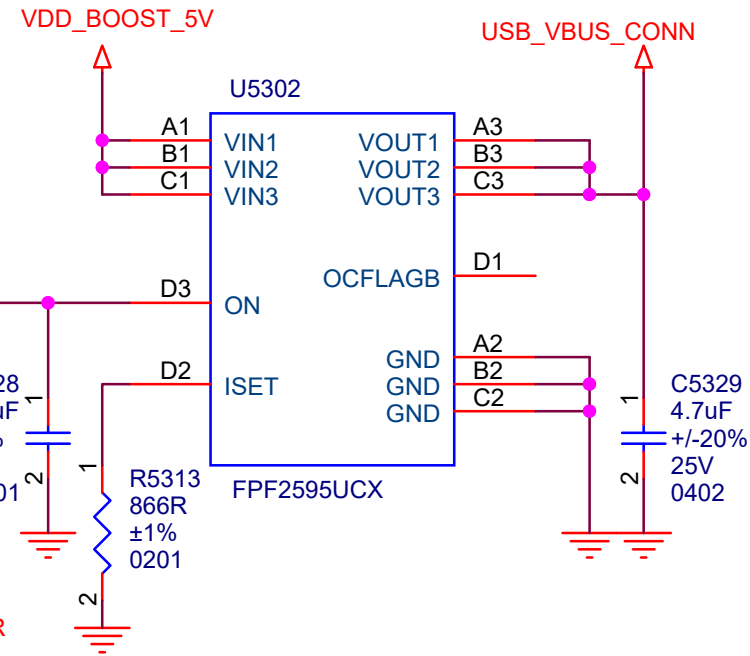


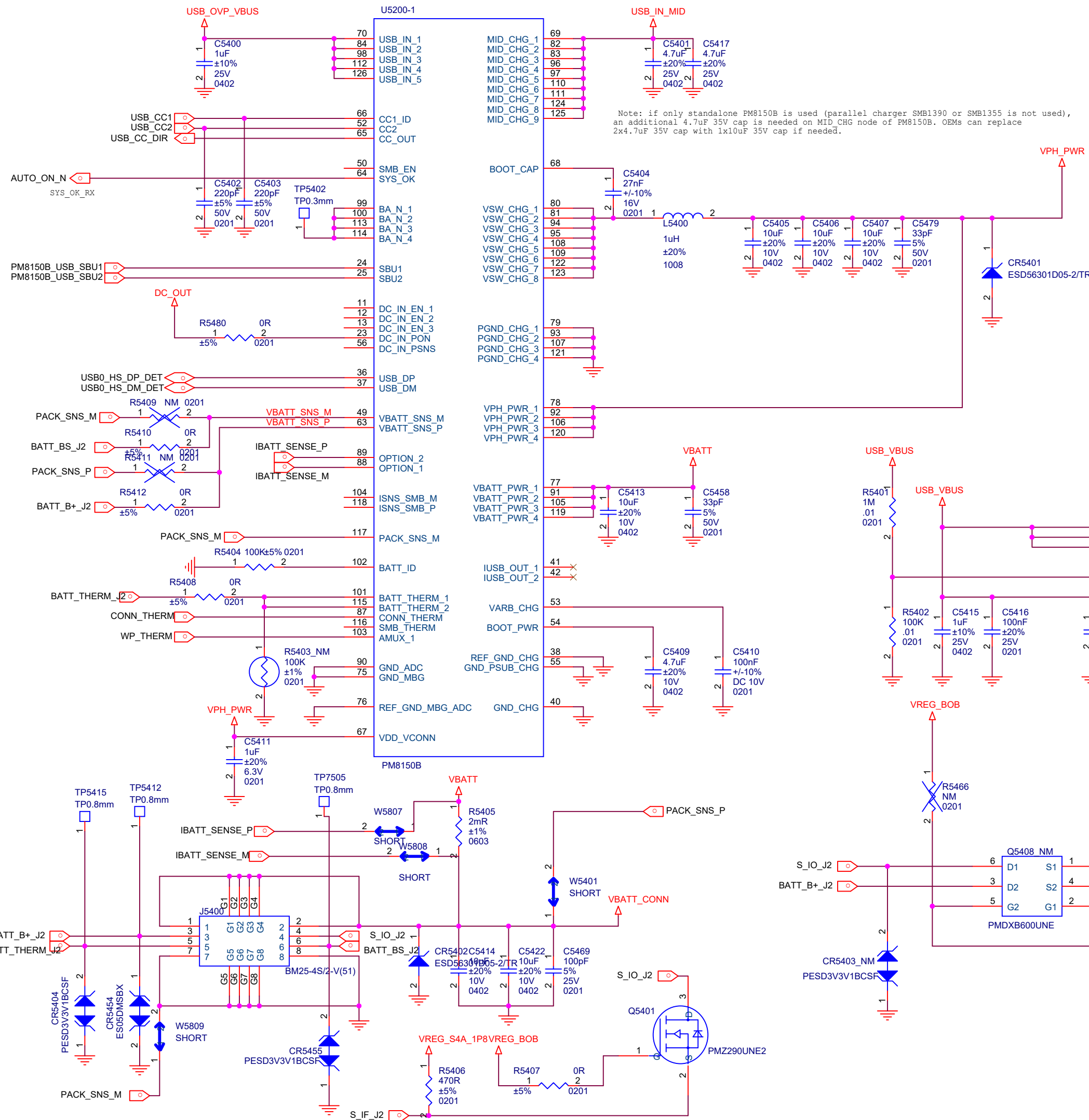


HAPTICS



OTG





J1方案BOM:

J2方案BOM:

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R5411不贴

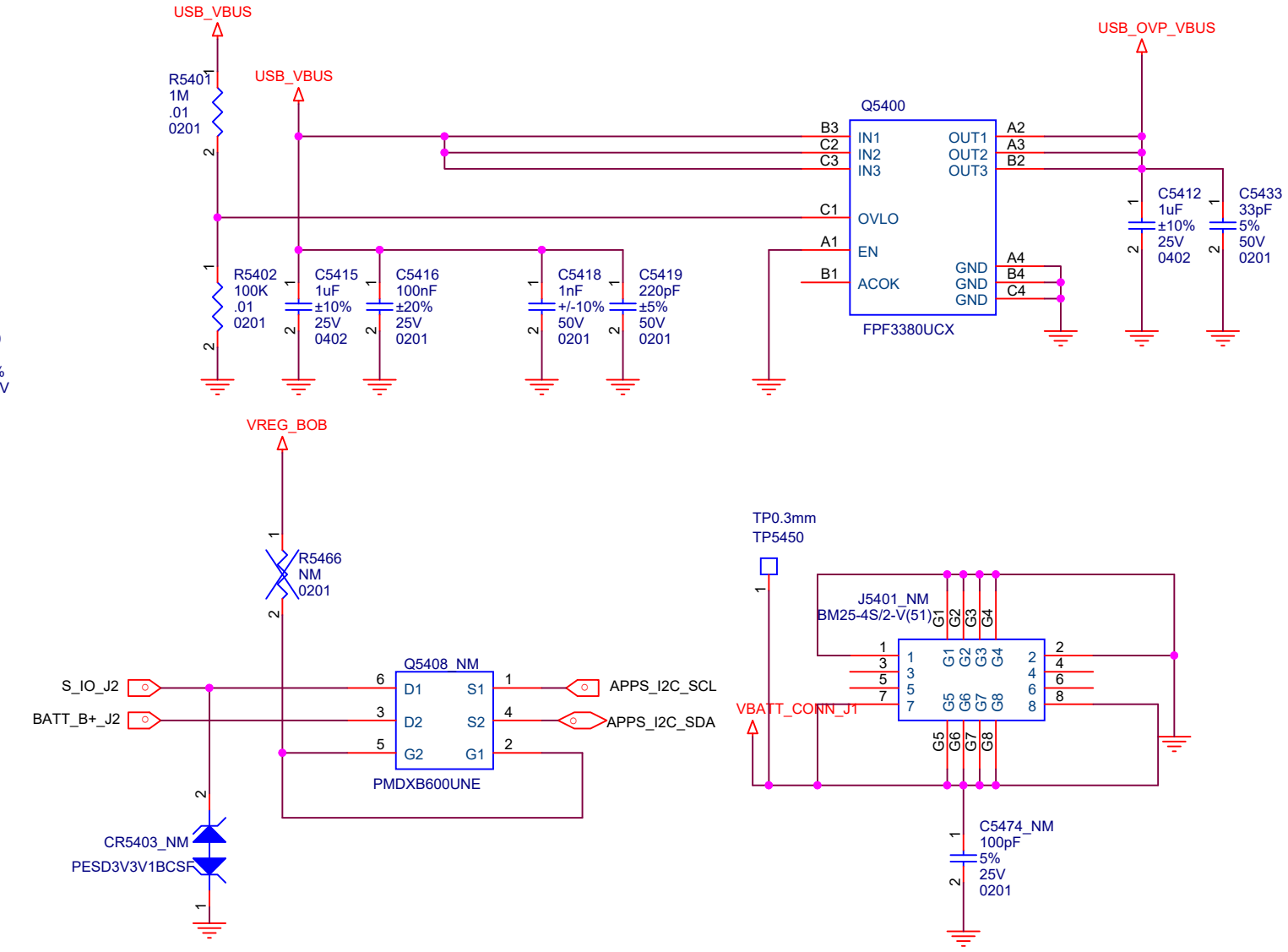
R5403不贴

R5466不贴

Q5408不贴

J5401不贴

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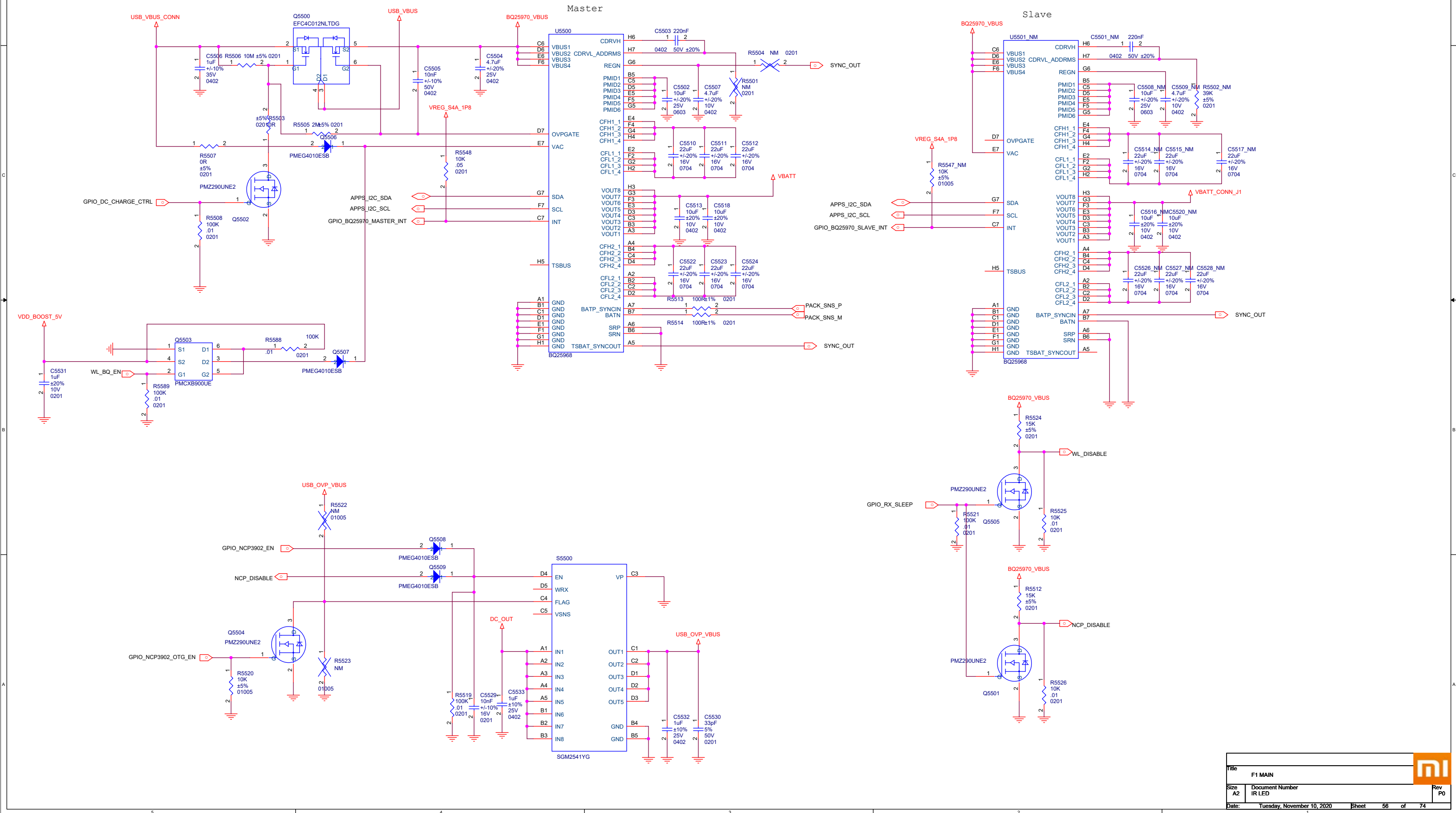
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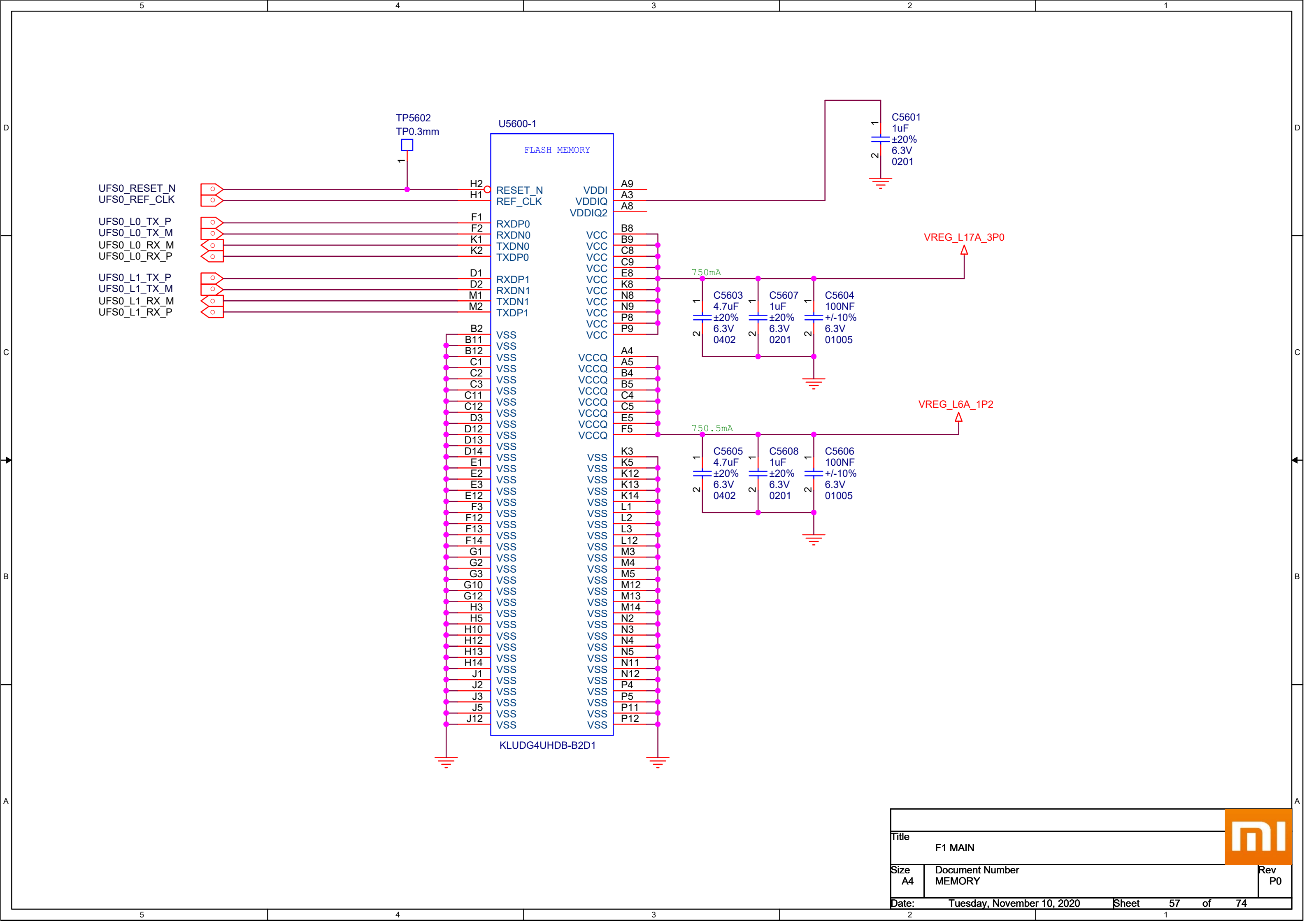
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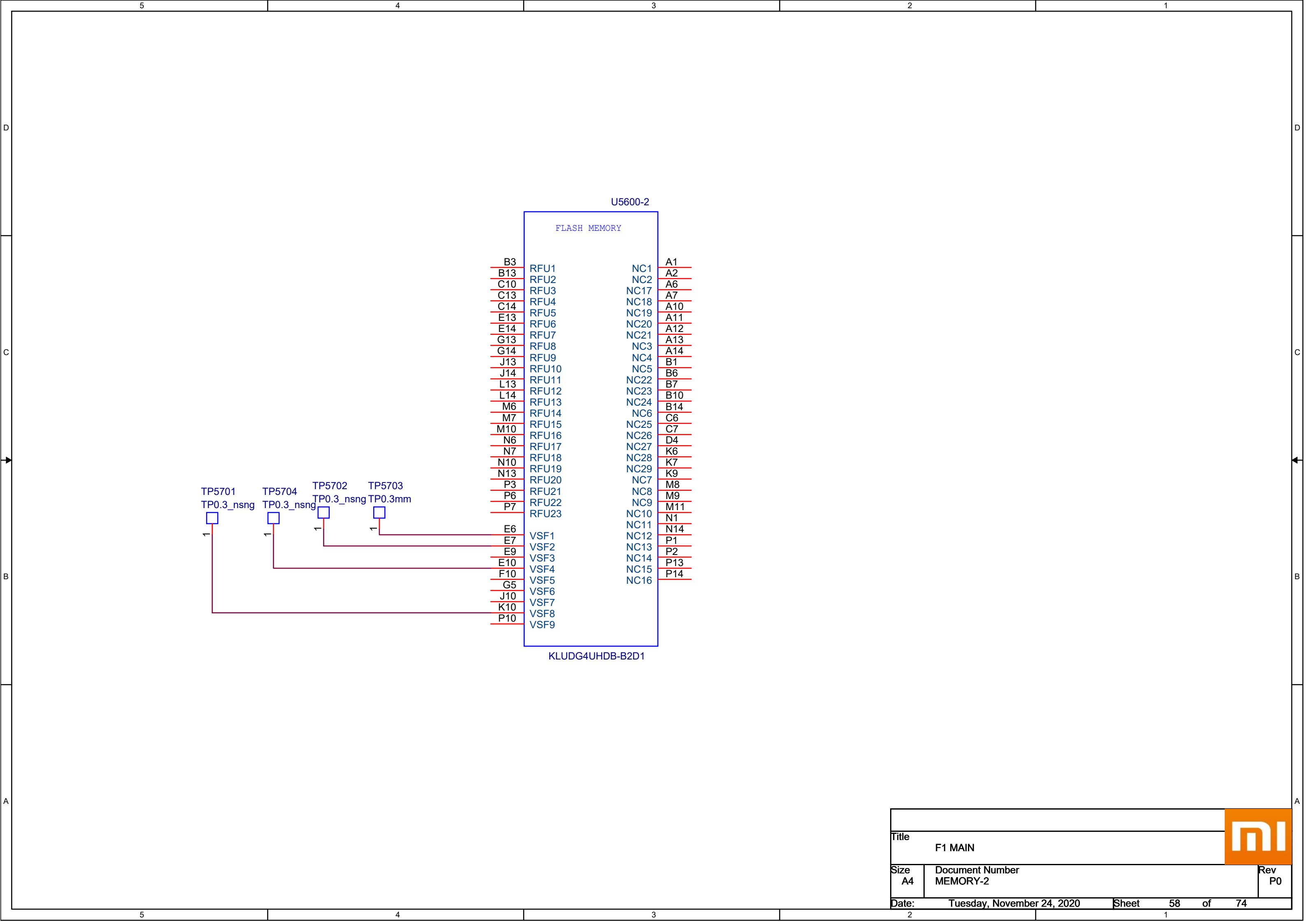
R5504不贴

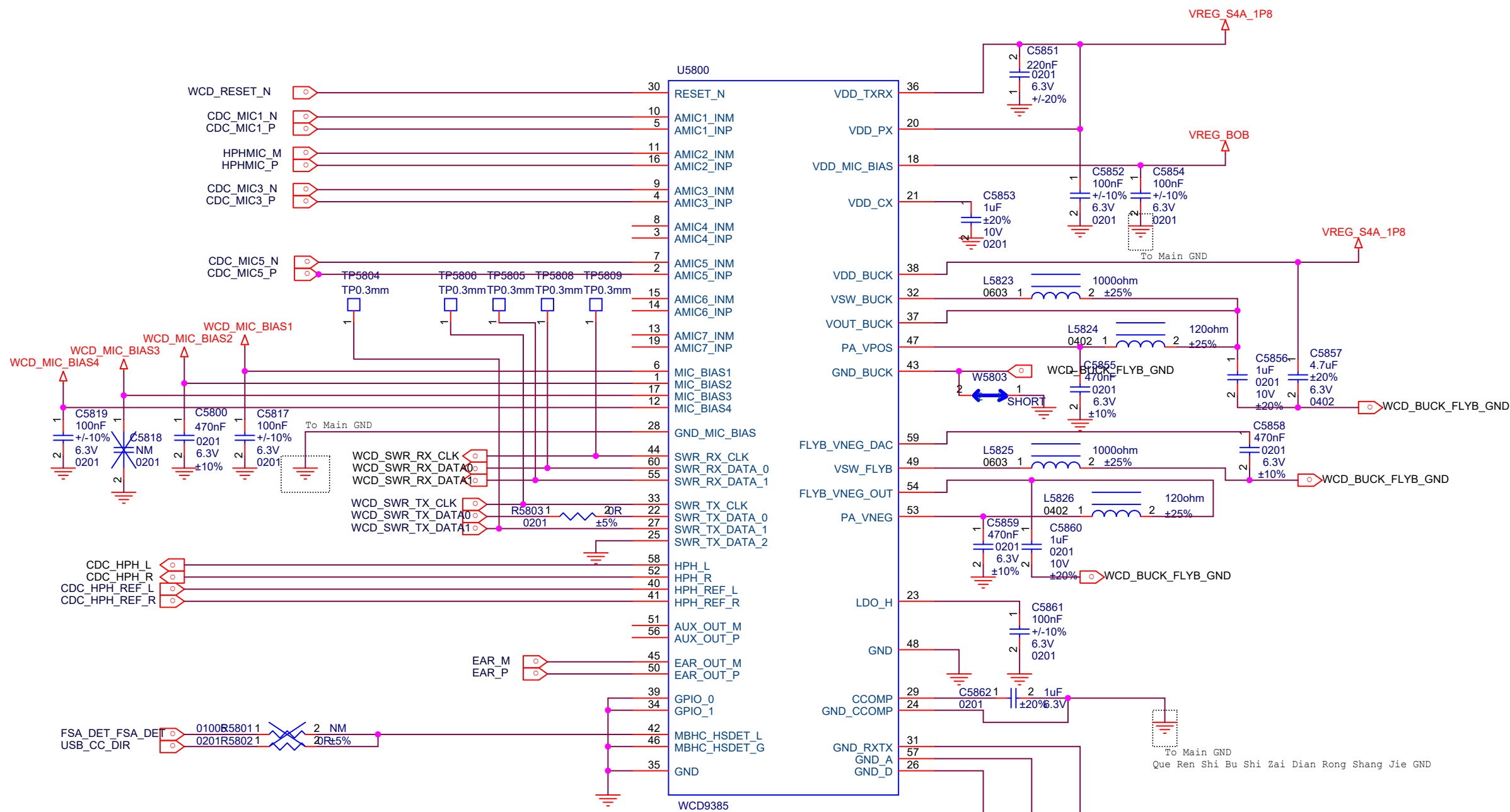
R5515贴0欧姆电阻

Slave charger的BOM全部不贴

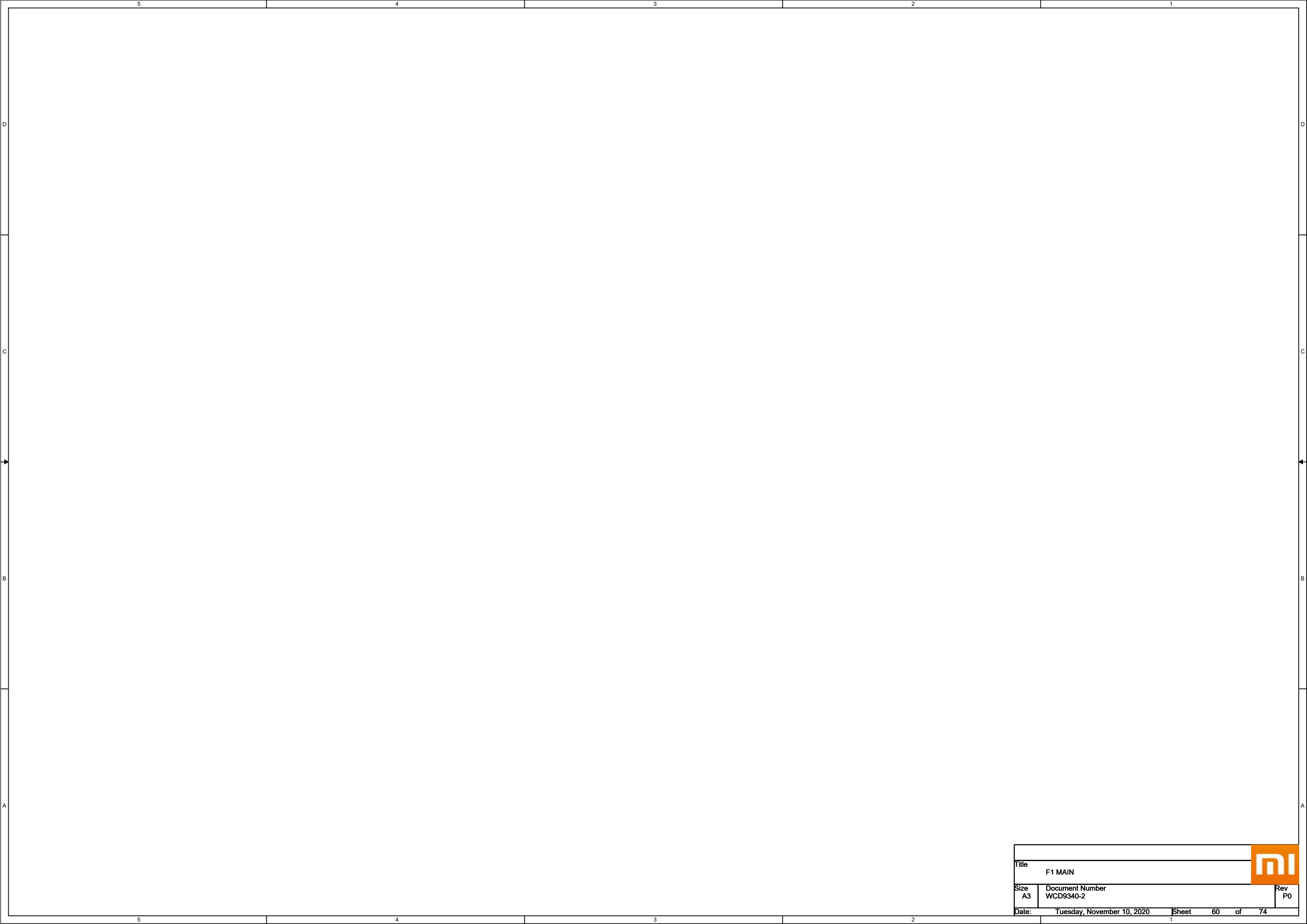







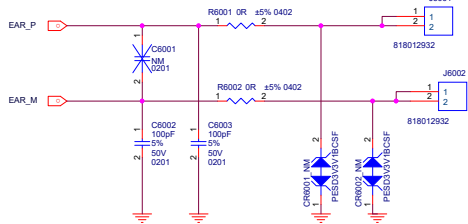


NOTE 1: ROUTE AN ISOLATED GROUND TRACE FROM CCOMP_REF TO THE CAP GROUND, THEN VIA TO THE MAIN GROUND PLANE
NOTE 2: PLACE VDD_BUCK CAP AS CLOSE TO CHIP EDGE AS POSSIBLE AND MINIMIZE LOOP INDUCTANCE
NOTE 3: USE SHORT AND WIDE TRACES FOR BUCK AND FLYBACK RELATED NETS
NOTE 4: ANALOG MIC INPUTS SHOULD BE ISOLATED WITH GROUND FROM NOISY OR SWITCHING SIGNALS AND SUPPLIES



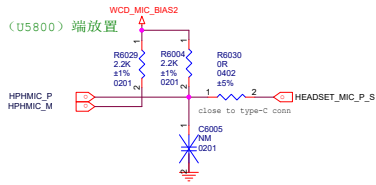
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RECEIVER

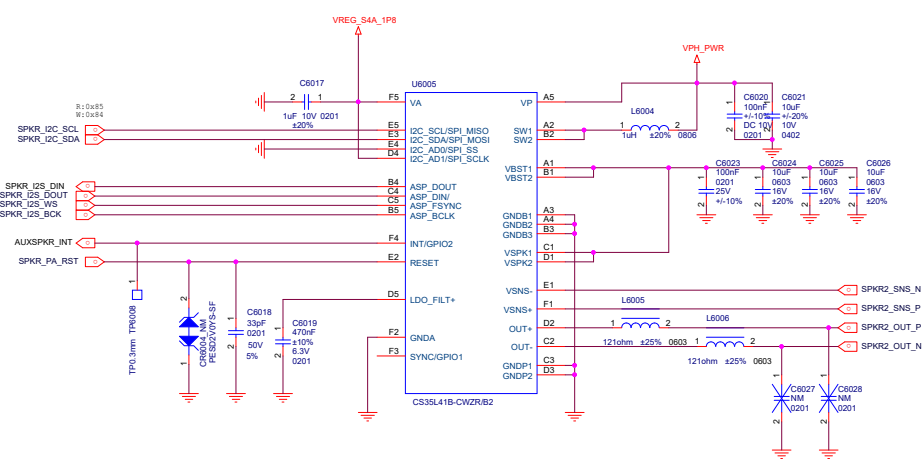
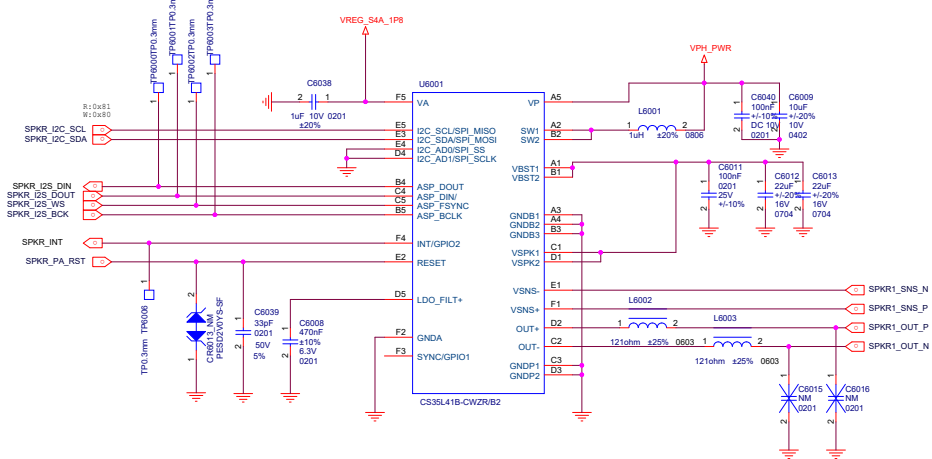


HEADSET

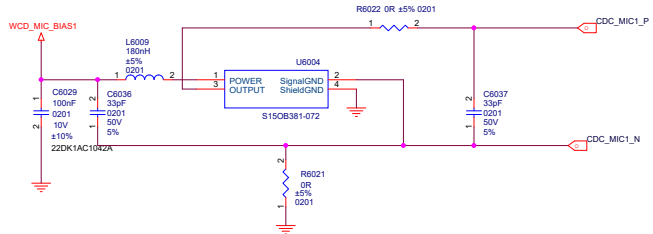
R6029、R6004、C6005靠近WCD9385 (U5800) 端放置



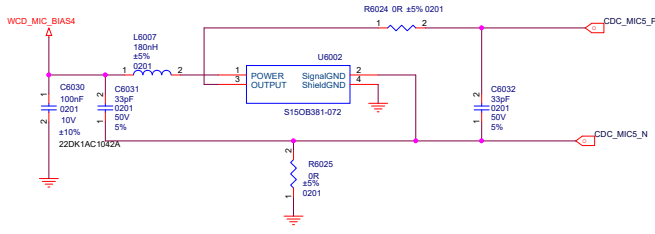
SPKR PA



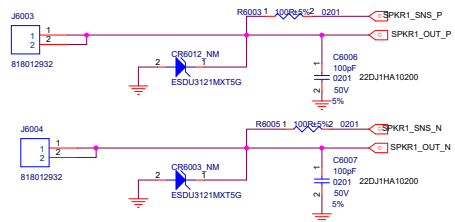
MAIN MIC



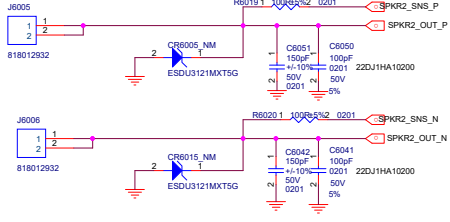
AUX MIC



SPK1

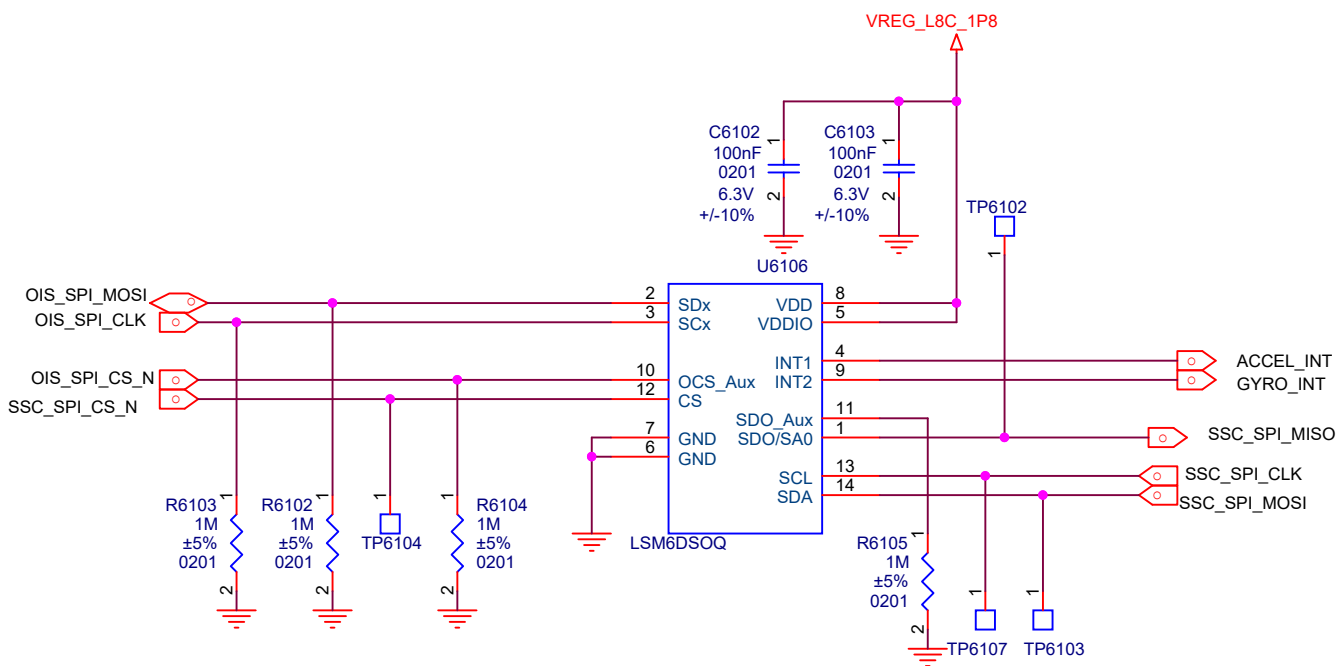


SPK2

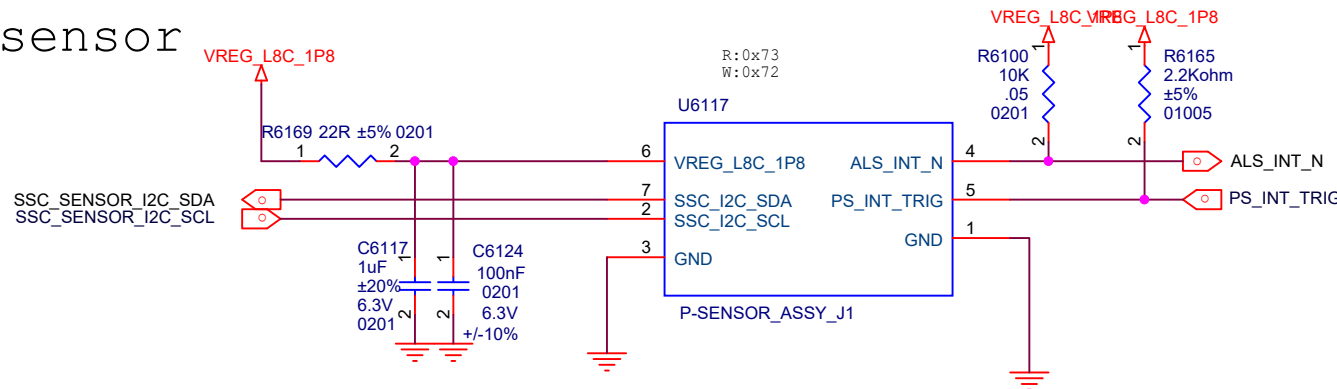


RGB DRIVER

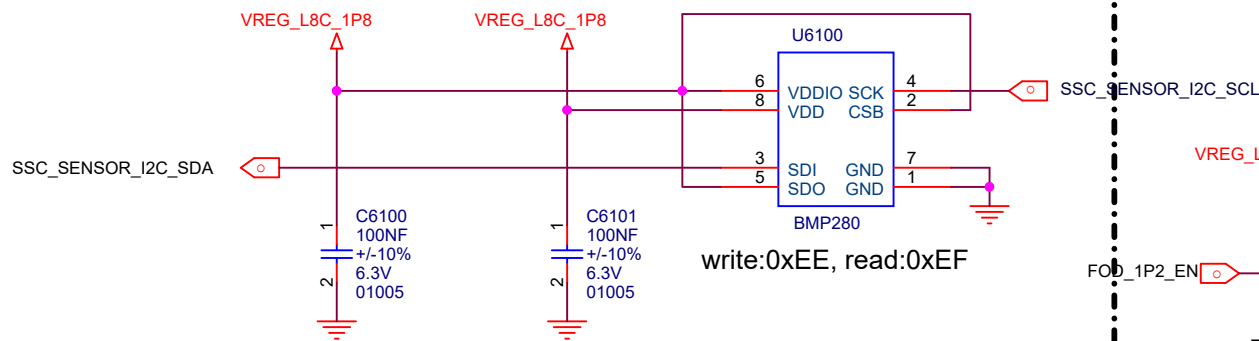
A+G



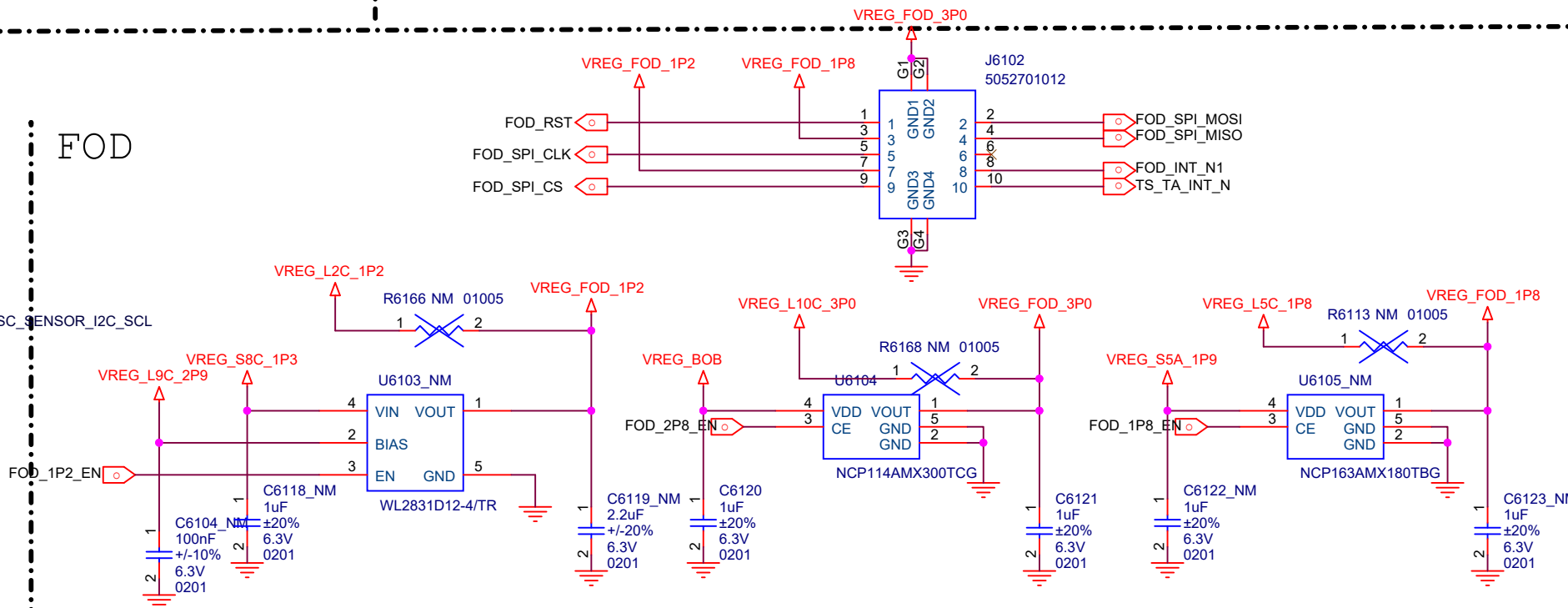
L-sensor



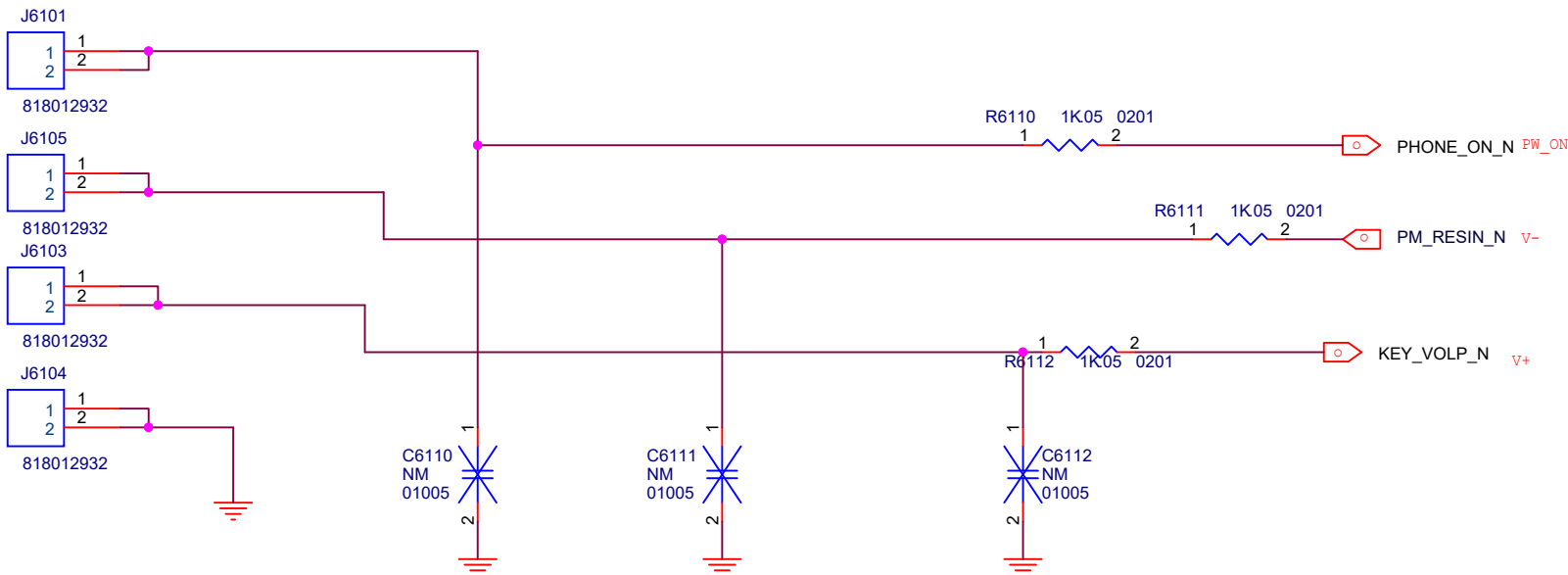
PRESSURE



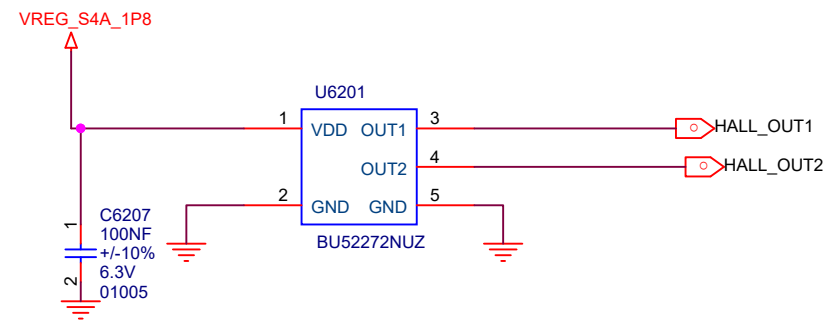
FOD



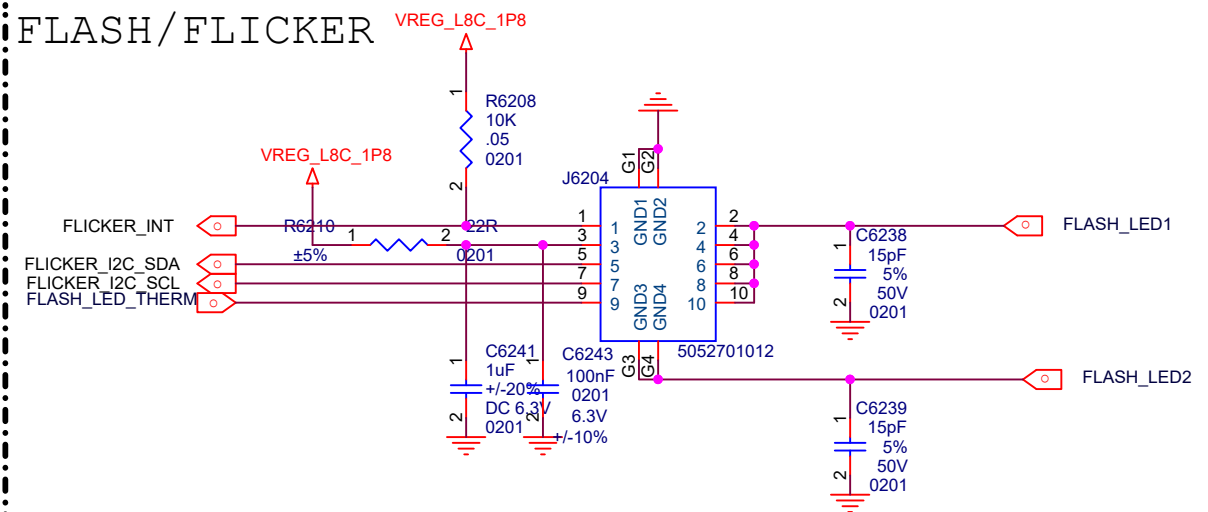
Sidekey



HALL

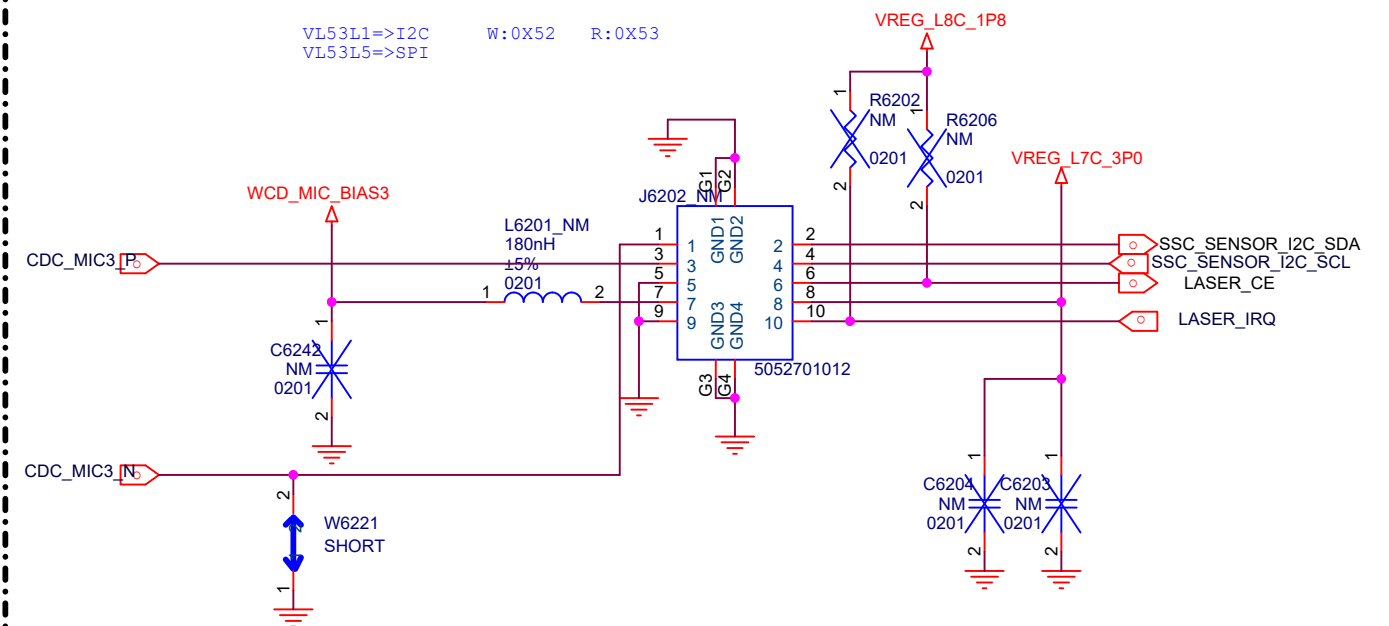


FLASH/FLICKER

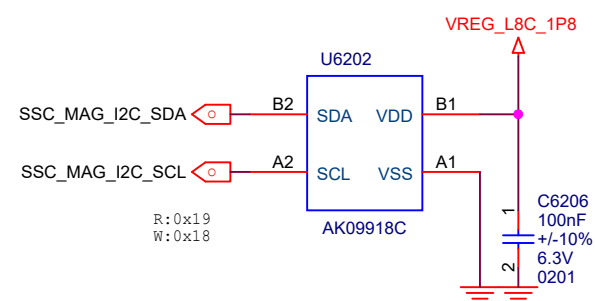


TOF

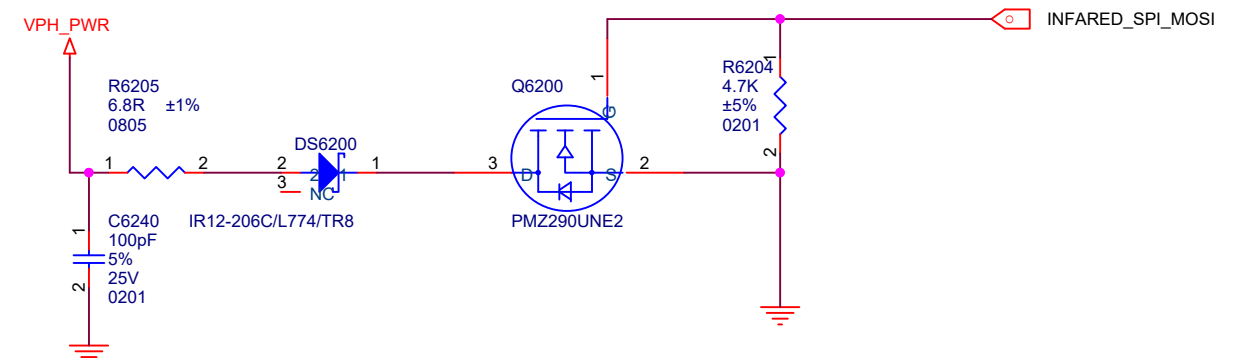
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VL53L1=>I2C      W:0X52    R:0X53
VL53L5=>SPI
```




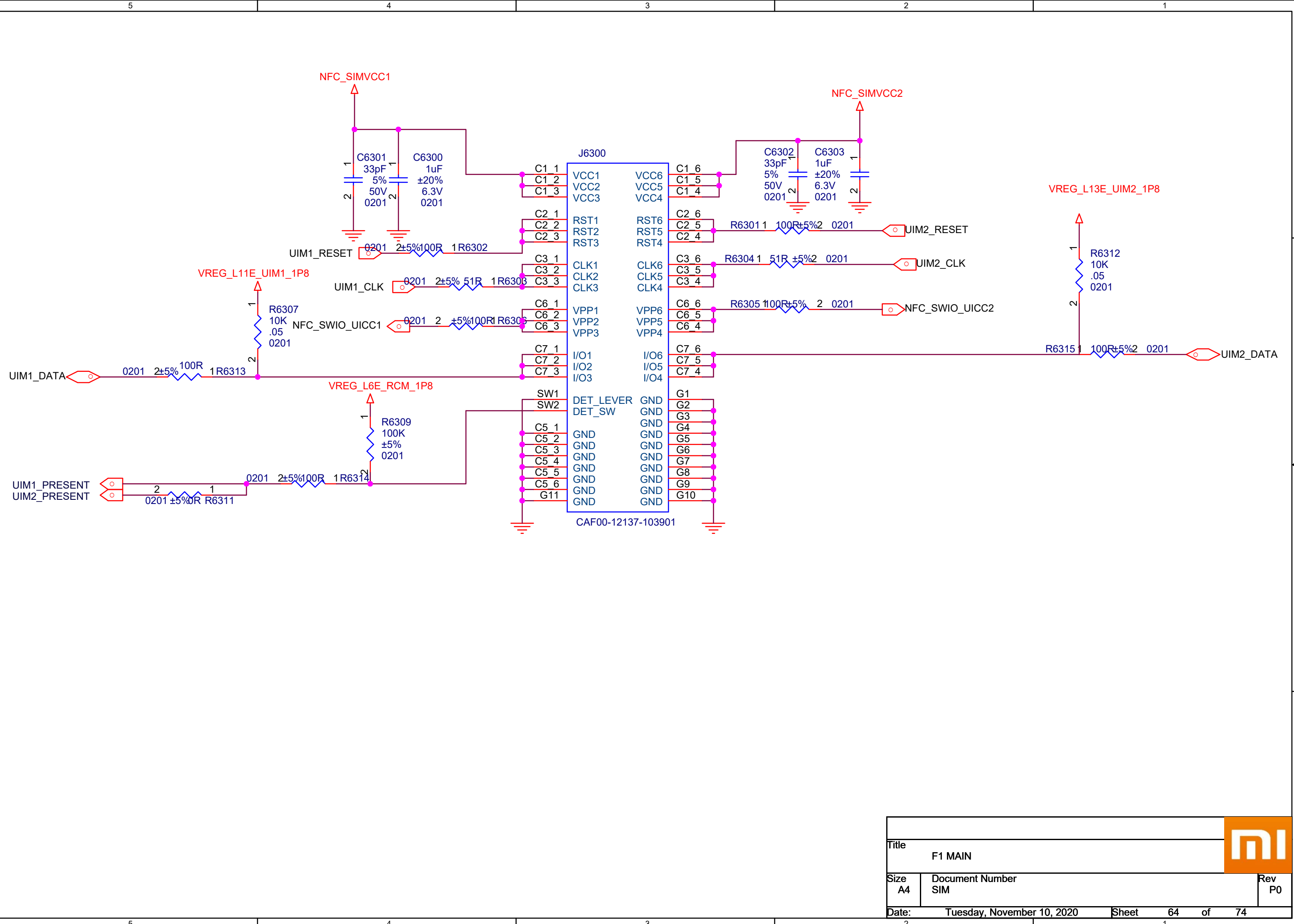
COMPASS

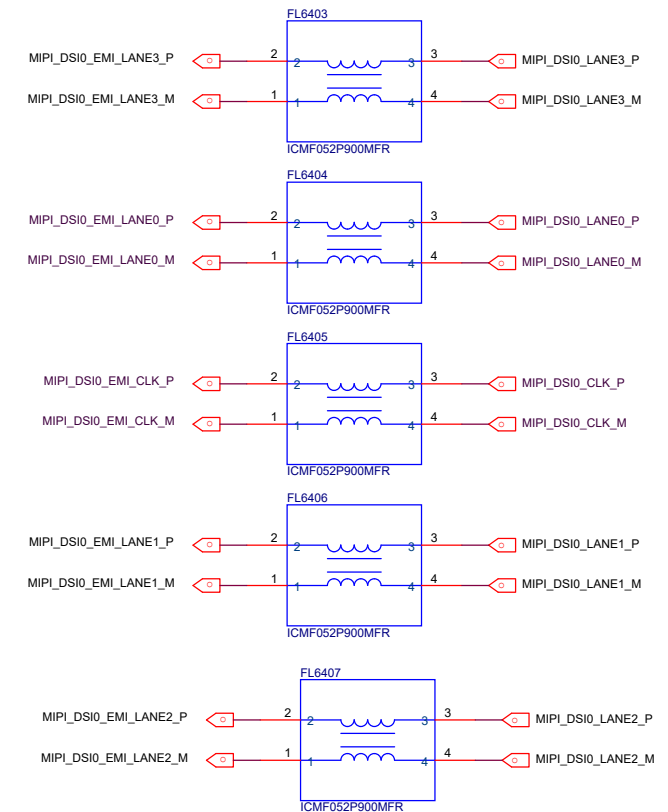
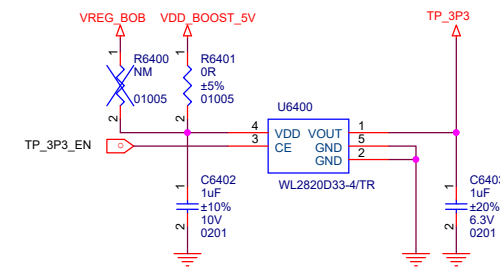


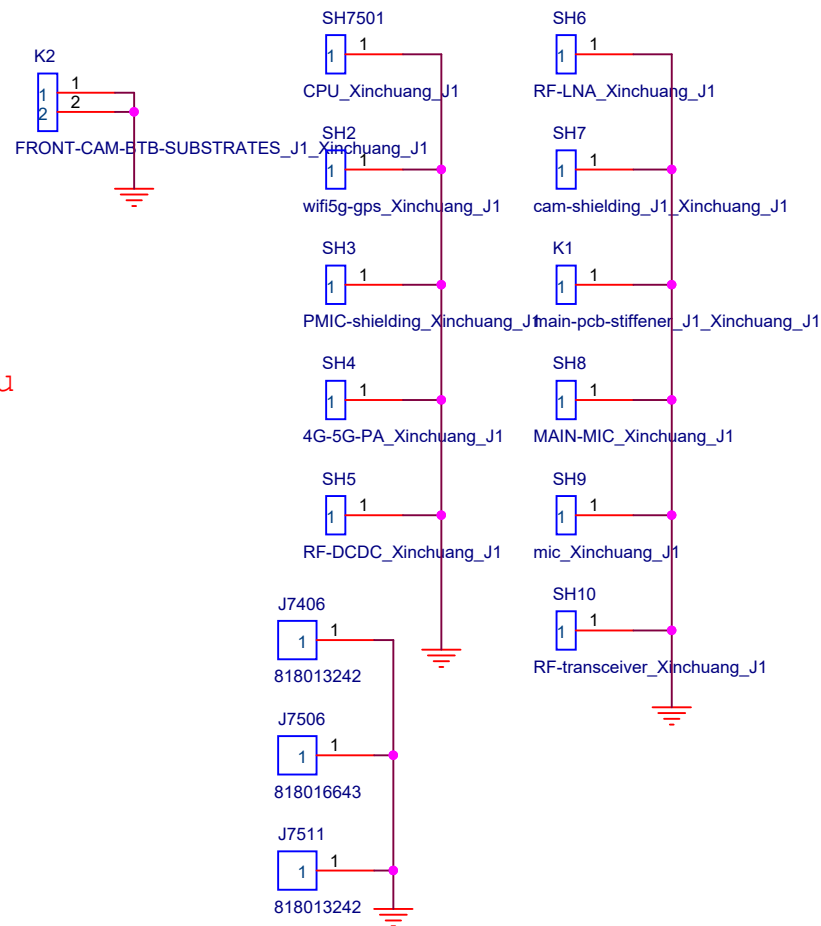
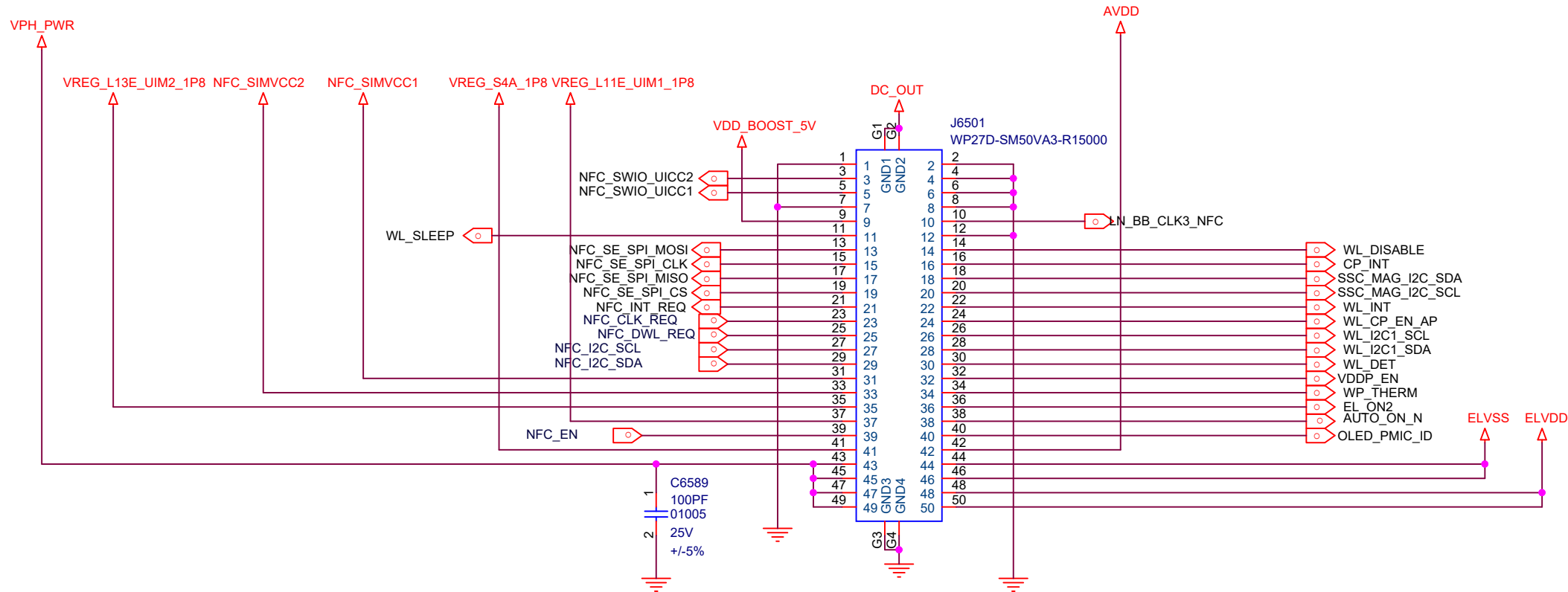
INFRARED



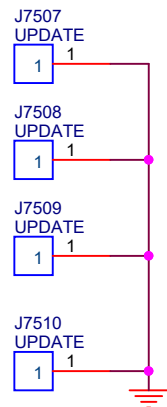
			
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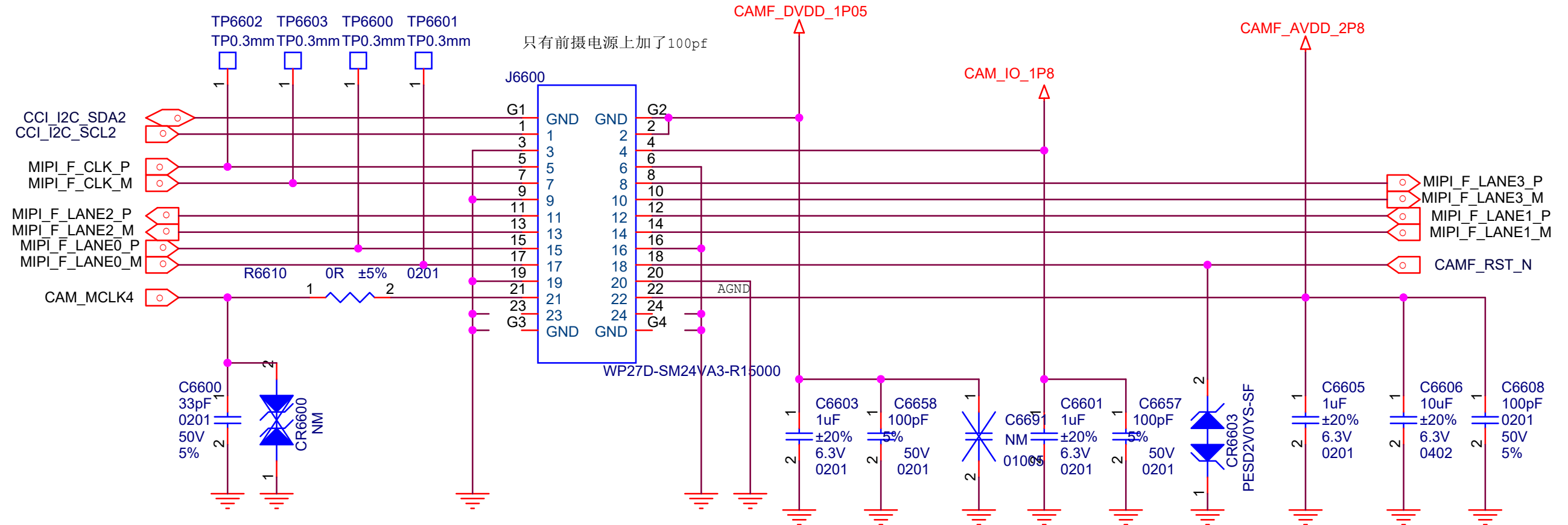


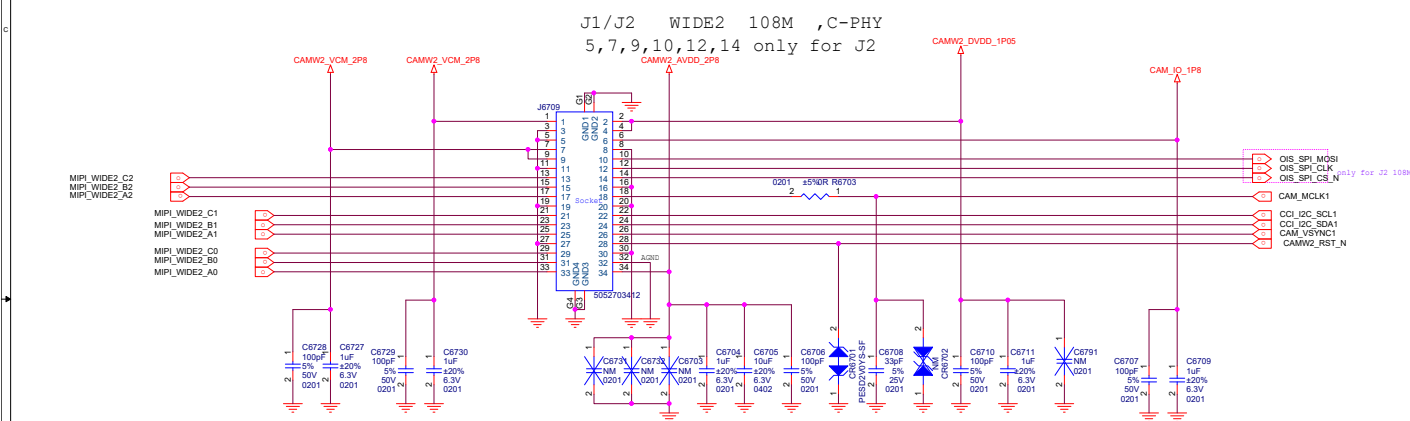
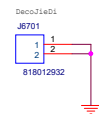
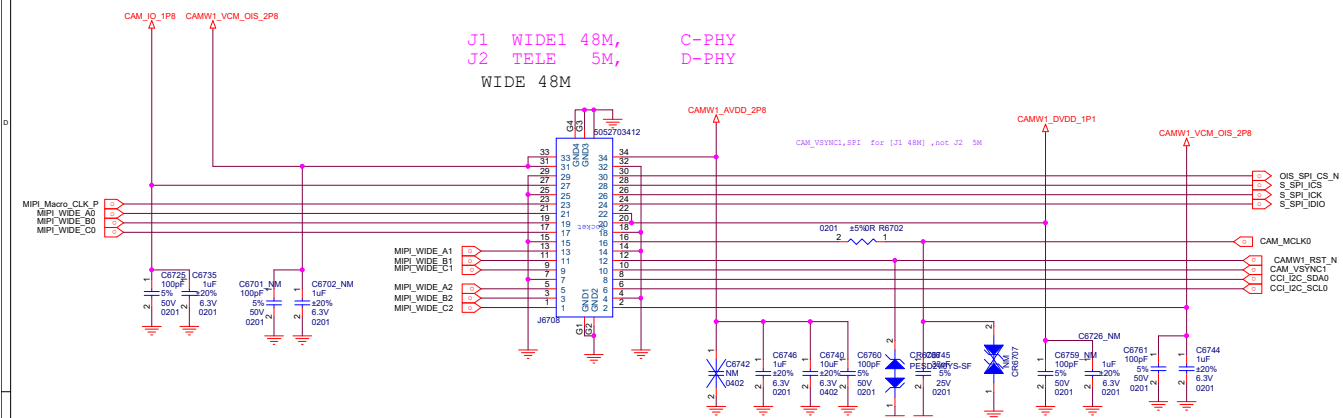


JiDeGengXinKu

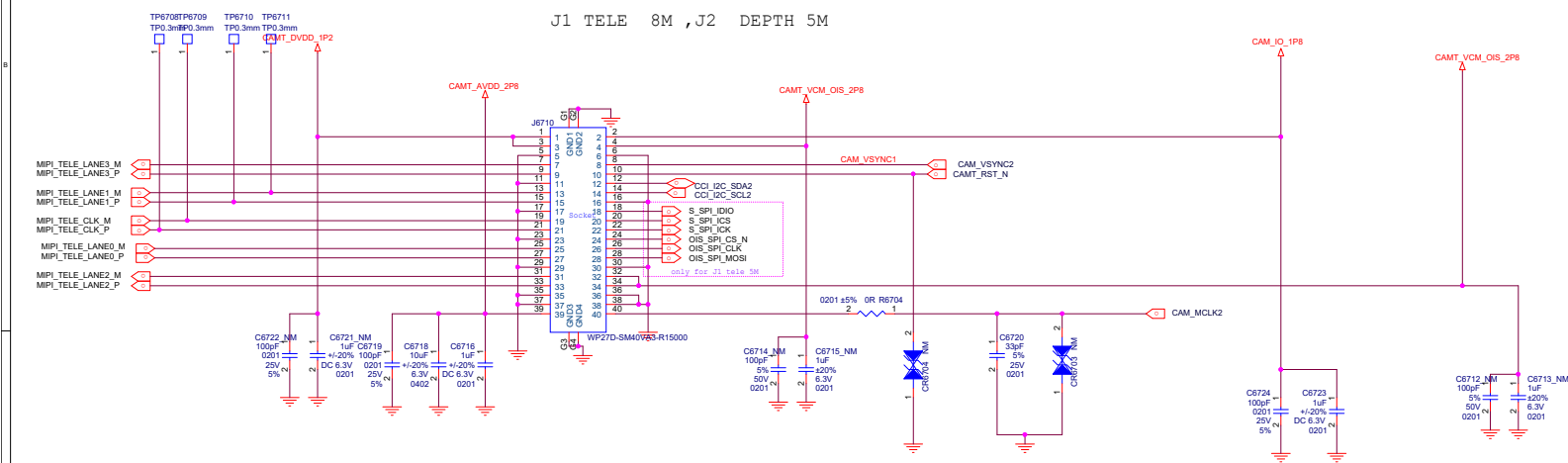


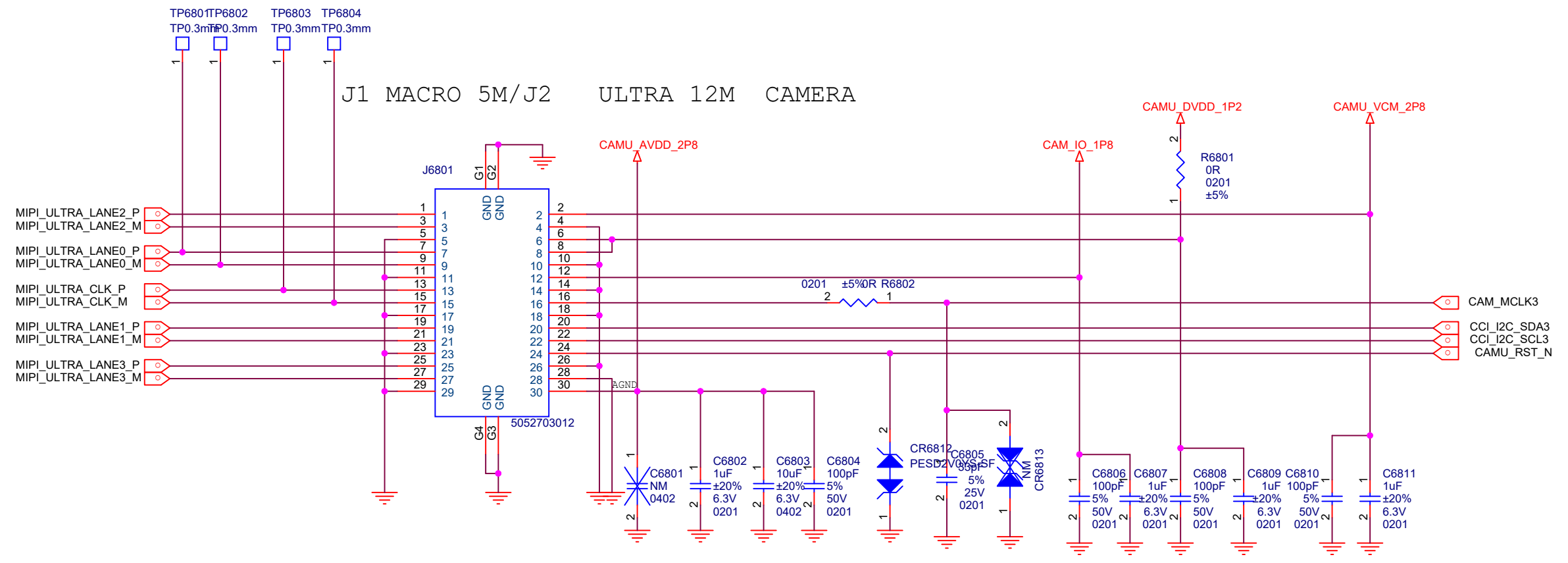
J1/J2 front 20M CAMERA

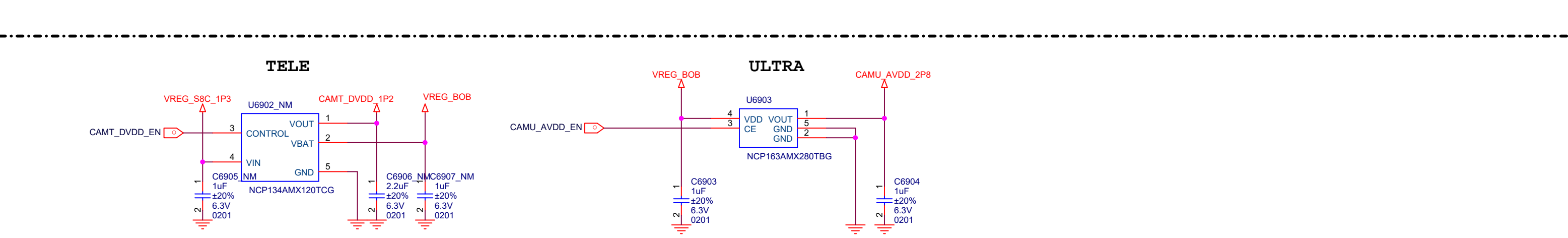
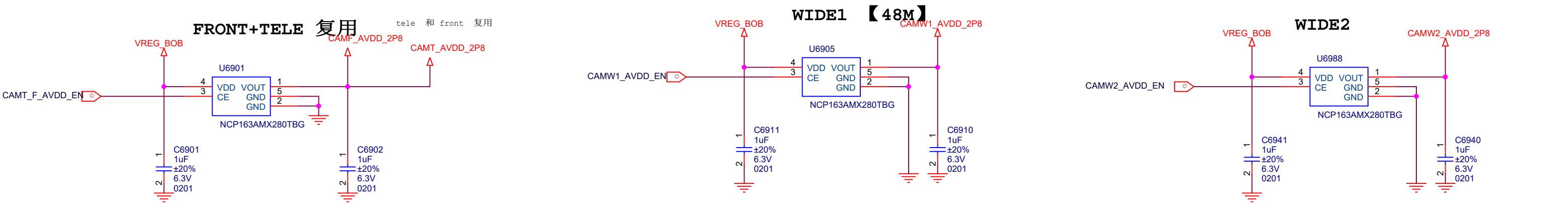


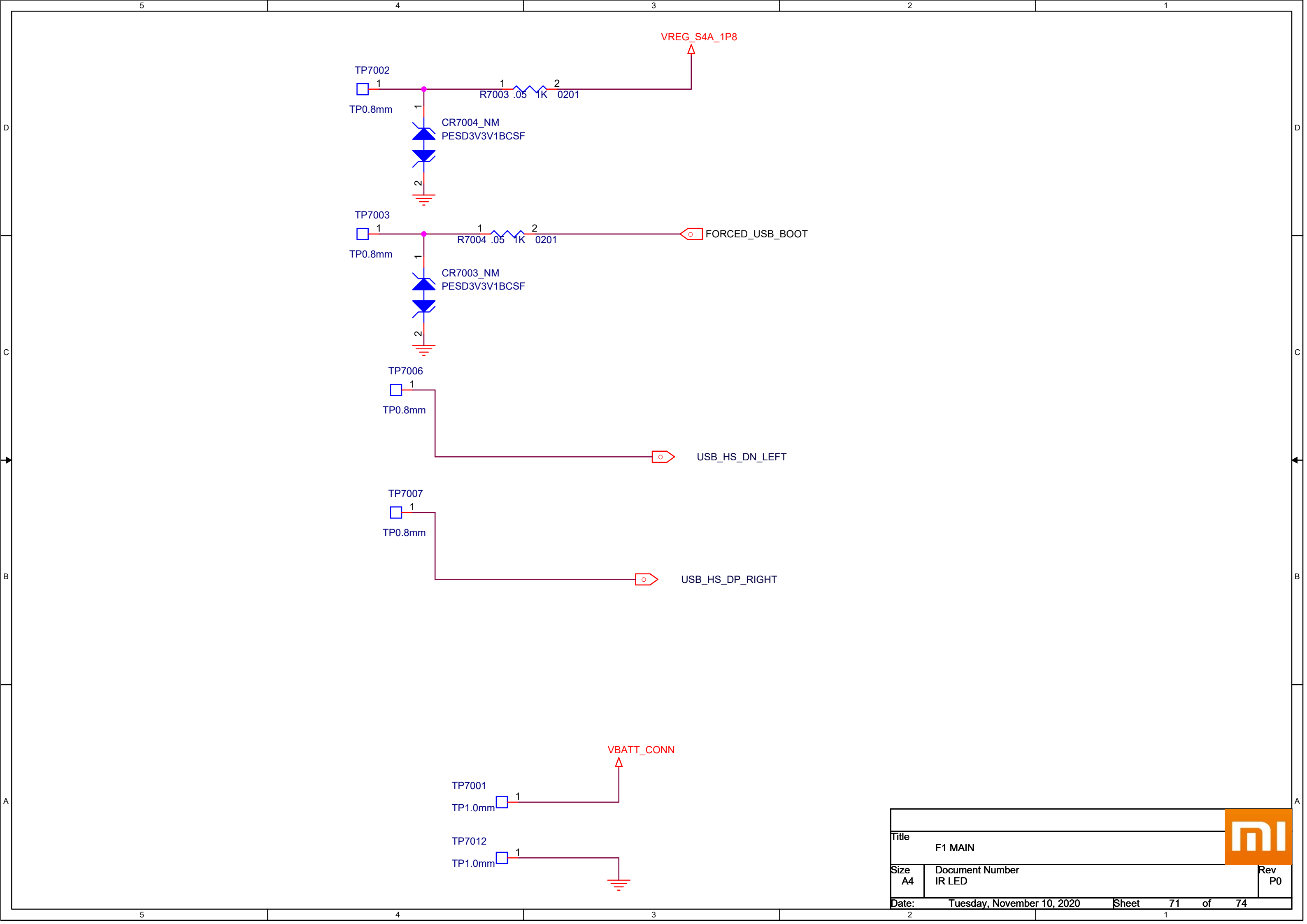


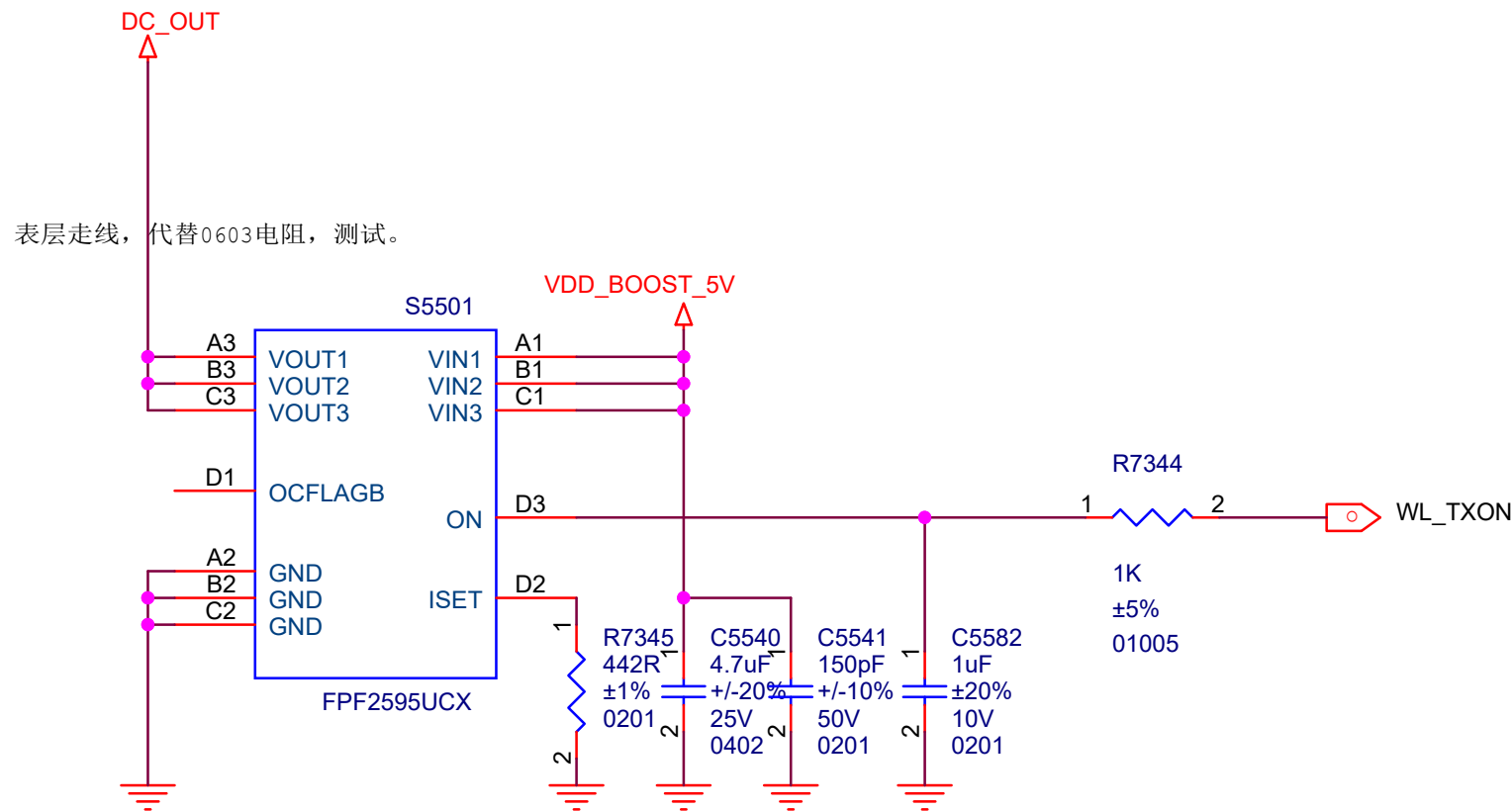
CAM	J1	J2	备注
Front_20M_24pin	相同		
J1(4x 8M)_J2(Depth 5M)_40pin	有 AF+OIS	2 Lan	J2 无 AF +OIS。 C6712, C6713 , C6714, C6715, J1 only, J2 NM
J1(Ultra108M)_J2(Wide108M)_34pin	有 AF .Cphy	有 AF+OIS, Cphy	J1 无 OIS。 C6727, C6728 J2 only, J1 NM
J1(Wide 48M)_J2(Tele 5M)_34pin	有 AF+OIS , Cphy	有 AF .Dphy 2Lan	J2 无 OIS。 C6701, C6702 J1 only, J2 NM
J1(Macro5M)_J2(Ultra 16M)_30pin	有 AF .Dphy 2Lan	有 AF	



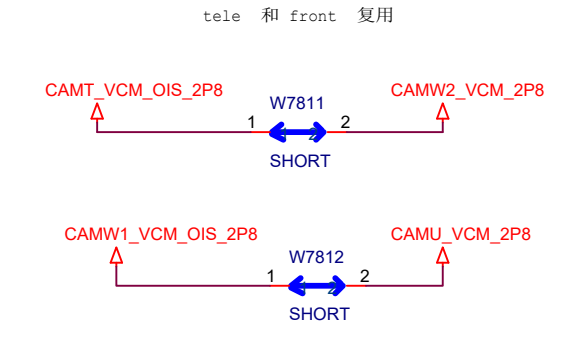
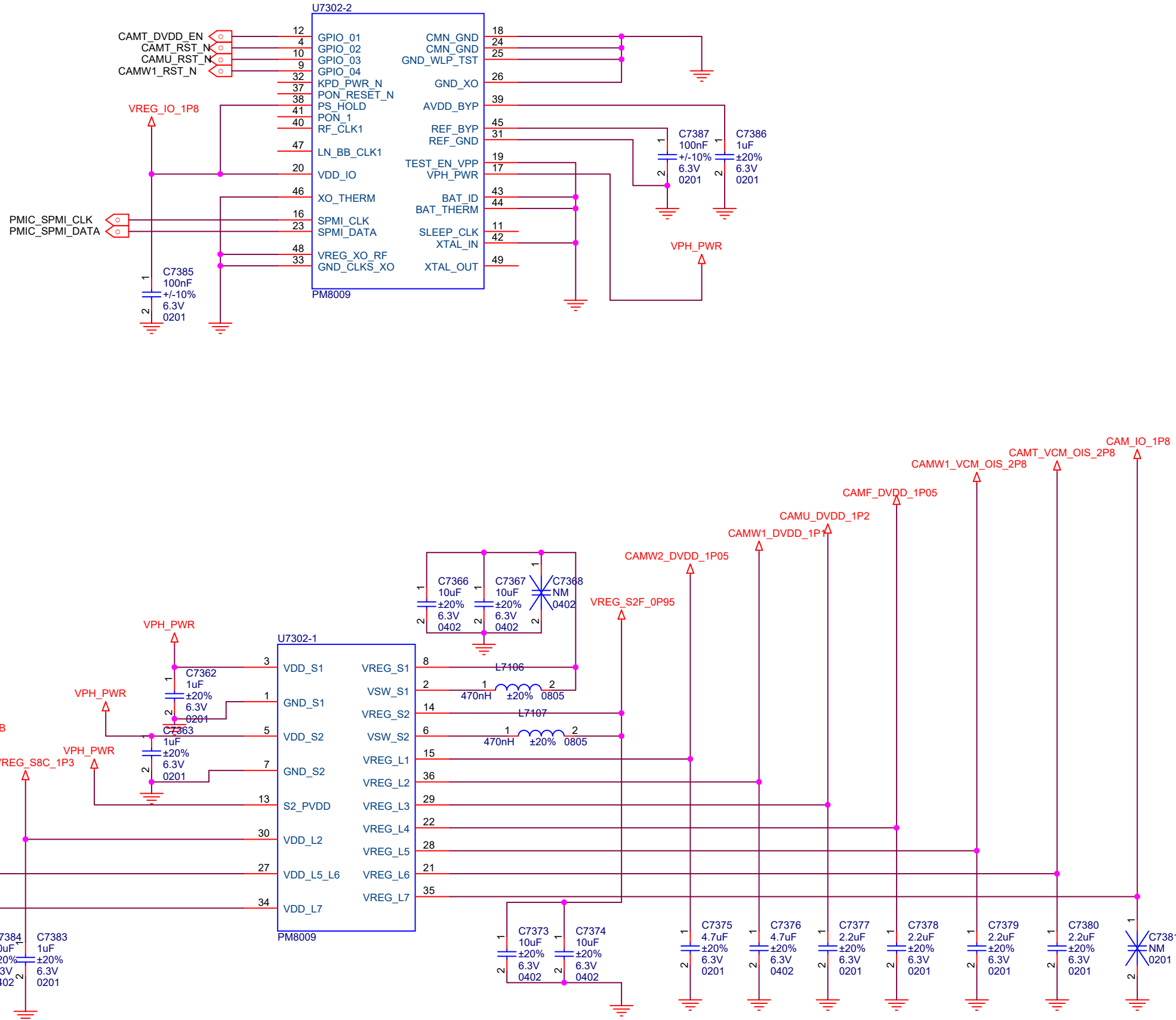








RsetΩ	Min. Current mA	Typ Current mA	Max. Current mA
415	2250	2500	2750
460	2025	2250	2475
520	1800	2000	2200



DRX_ANT

TUNER_CTR

